User's Manual

UTAdvanced

UTAdvanced Series Communication Interface (Open Network) User's Manual

IM 05P07A01-02EN

vigilantplant®

YOKOGAWA
Yokogawa Electric Corporation

IM 05P07A01-02EN
7th Edition

Introduction

Thank you for purchasing the UTAdvanced Series digital indicating controller (hereinafter referred to as “UTAdvanced”).
This manual describes how to use the communication functions (PROFIBUS-DP, DeviceNet and CC-Link communication) of the UTAdvanced. Read this manual thoroughly beforehand to ensure correct use of the UTAdvanced.
Note that the manuals for the UTAdvanced comprise the following eighteenth documents.
To use the UTAdvanced, you must have a sufficient knowledge of the communication specifications of the host computer that the UTAdvanced is connected to, communication hardware, the program language used for communication, and other communication-related information.

- **Printed manual**

Manual Name	Manual Number	Description
UT55A/UT52A Operation Guide (for Standard model)	IM 05P01C31-11EN	This manual describes the basic operation method.
UT55A/UT52A Operation Guide (for Detailed model)	IM 05P01C31-15EN	This manual describes the basic operation method.
UT35A/UT32A Operation Guide (for Standard model)	IM 05P01D31-11EN	This manual describes the basic operation method.
UT35A/UT32A Operation Guide (for Detailed model)	IM 05P01D31-15EN	This manual describes the basic operation method.
UP55A Operation Guide (for Standard model)	IM 05P02C41-11EN	This manual describes the basic operation method.
UP55A Operation Guide (for Detailed model)	IM 05P02C41-15EN	This manual describes the basic operation method.
UP35A Operation Guide (for Standard model)	IM 05P02D41-11EN	This manual describes the basic operation method.
UP35A Operation Guide (for Detailed model)	IM 05P02D41-15EN	This manual describes the basic operation method.
UM33A Operation Guide	IM 05P03D21-11EN	This manual describes the basic operation method.

- **Electronic manuals**

Manual Name	Manual Number	Description
UT55A/UT52A Operation Guide (for Standard model)	IM 05P01C31-11EN	This is identical to the printed manual.
UT55A/UT52A Operation Guide (for Detailed model)	IM 05P01C31-15EN	This is identical to the printed manual.
UT35A/UT32A Operation Guide (for Standard model)	IM 05P01D31-11EN	This is identical to the printed manual.
UT35A/UT32A Operation Guide (for Detailed model)	IM 05P01D31-15EN	This is identical to the printed manual.
UP55A Operation Guide (for Standard model)	IM 05P02C41-11EN	This is identical to the printed manual.
UP55A Operation Guide (for Detailed model)	IM 05P02C41-15EN	This is identical to the printed manual.
UP35A Operation Guide (for Standard model)	IM 05P02D41-11EN	This is identical to the printed manual.
UP35A Operation Guide (for Detailed model)	IM 05P02D41-15EN	This is identical to the printed manual.
UM33A Operation Guide	IM 05P03D21-11EN	This is identical to the printed manual.
UT55A/UT52A User’s Manual	IM 05P01C31-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UT35A/UT32A User’s Manual	IM 05P01D31-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UP55A User’s Manual	IM 05P02C41-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UP35A User’s Manual	IM 05P02D41-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.

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Manual Name	Manual Number	Description
UM33A User's Manual	IM 05P03D21-01EN	This manual describes the usage of all functions except the communication functions.
UTAdvanced Series Communication Interface (RS-485, Ethernet) User's Manual	IM 05P07A01-01EN	This manual describes how to use the UTAdvanced in Ethernet and serial communications. For communication wiring, see the Operation Guide or User's Manual.
UTAdvanced Series Communication Interface (Open Network) User's Manual	IM 05P07A01-02EN	This manual. It describes how to use the UTAdvanced in PROFIBUS-DP/DeviceNet/CC-Link communication. For communication wiring, see the Operation Guide or User's Manual.
LL50A Parameter Setting Software Installation Manual	IM 05P05A01-01EN	This manual describes how to install and uninstall the LL50A.
LL50A Parameter Setting Software User's Manual	IM 05P05A01-02EN	This manual describes how to use the LL50A, ladder sequence function, peer-to-peer communication, and network profile creating function.

* User's Manual can be downloaded from a website.

Intended Readers

This manual is intended for people familiar with the functions of the UTAdvanced such as control engineers and personnel in charge of the maintenance of instrumentation and control equipment.

Notice

- The contents of this manual are subject to change without notice as a result of continuing improvements to the instrument's performance and functions.
- Every effort has been made to ensure accuracy in the preparation of this manual. Should any errors or omissions come to your attention, however, please inform Yokogawa Electric's sales office or sales representative.
- Under no circumstances may the contents of this manual, in part or in whole, be transcribed or copied without our permission.
- The document concerning TCP/IP software has been created by Yokogawa based on the BSD Networking Software, Release 1 that has been licensed from the University of California.

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- Modbus is a registered trademark of Schneider Electric.
- PROFIBUS-DP is a registered trademark of PROFIBUS User Organization.
- DeviceNet is a registered trademark of Open DeviceNet Vender Association, Inc.
- CC-Link is a registered trademark of CC-Link Partner Association (CLPA.)
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Symbols Used in This Manual

This symbol is used on the instrument. It indicates the possibility of injury to the user or damage to the instrument, and signifies that the user must refer to the user's manual for special instructions. The same symbol is used in the user's manual on pages that the user needs to refer to, together with the term "WARNING" or "CAUTION."

WARNING

Calls attention to actions or conditions that could cause serious or fatal injury to the user, and indicates precautions that should be taken to prevent such occurrences.

CAUTION

Calls attention to actions or conditions that could cause injury to the user or damage to the instrument or property and indicates precautions that should be taken to prevent such occurrences.

Note

Identifies important information required to operate the instrument.

▶

Indicates related operations or explanations for the user's reference.

[ ]

Indicates a character string displayed on the display.

Setting Display

Indicates a setting display and describes the keystrokes required to display the relevant setting display.

Setting Details

Provides the descriptions of settings.

Description

Describes restrictions, etc. regarding a relevant operation.

Procedure

Describes procedures.

How to Use This Maunual

Usage

First read through the Operation Guide to understand the basic operation and then read this manual.
This user's manual is organized into Chapters 1 to 4 as shown below.

Chapter	Title and Description
1	**Overview** Describes types of communication and communication specifications.
2	**Setting Communication Functions** Describes communication parameter setting items.
3	**Description of PROFIBUS-DP/DeviceNet Communication (for UTAdvanced with PROFIBUS-DP/DeviceNet Communication)** Describes how to use PROFIBUS-DP/DeviceNet communication.
4	**Description of CC-Link Communication (for UTAdvanced with CC-Link Communication)** Describes how to use CC-Link communication.

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Contents

Chapter 4 Description of CC-Link Communication (for UTAdvanced with CC-Link Communication)

Revision Information

1.1 Open Network

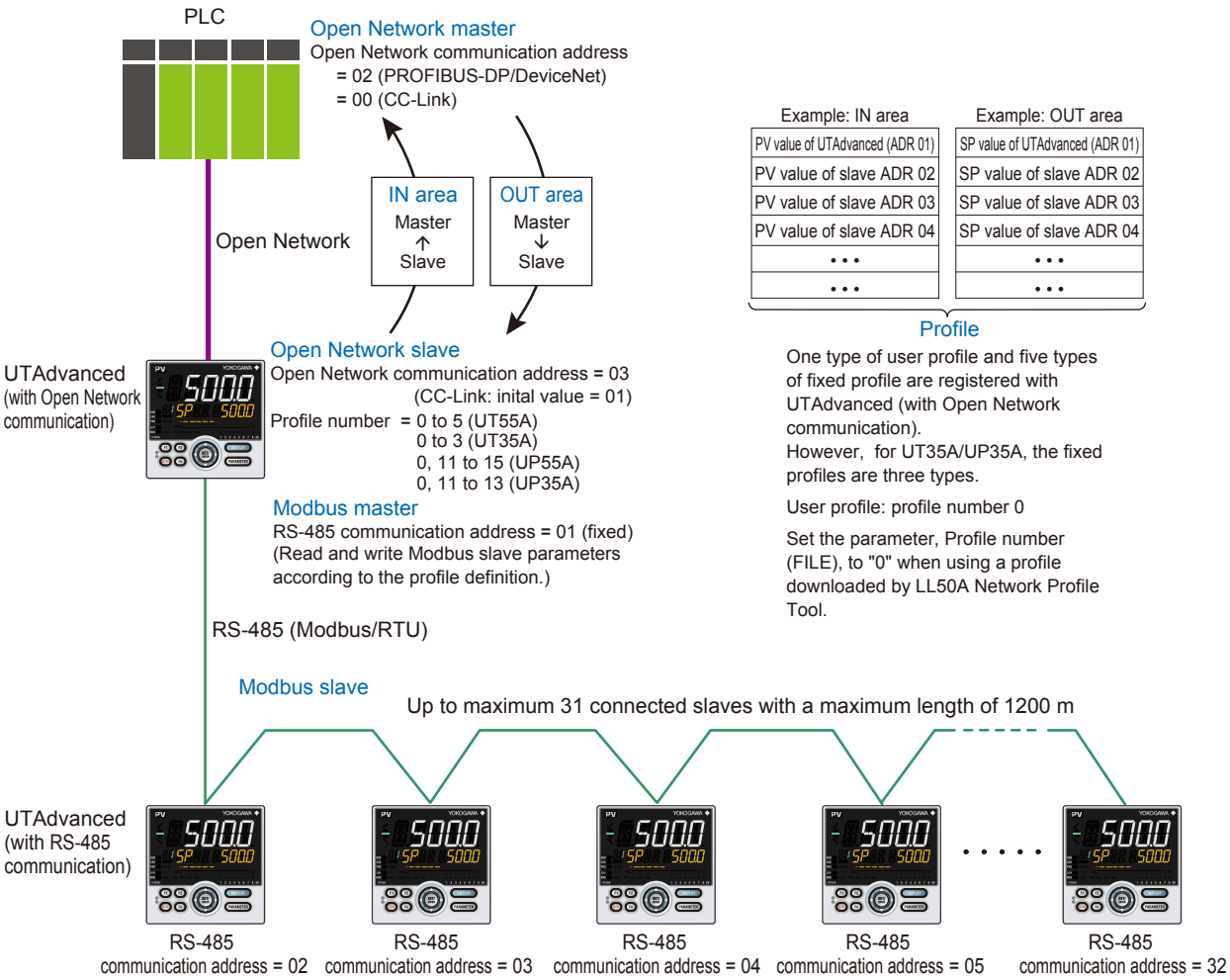
In UTAdvanced, PROFIBUS-DP, DeviceNet and CC-Link are collectively called Open Network.
UTAdvanced with Open Network communication runs as a slave controller for Open Network communication. Mainly PLC is used as a master controller for Open Network communication.
Furthermore, UTAdvanced with Open Network communication is equipped with a Open Network communication terminal and RS-485 terminal. The RS-485 terminal allows it to run also as a master controller for Modbus communication. UTAdvanced with RS-485 communication is used as a slave controller for Modbus communication.
UTAdvanced with Open Network communication is able to handle its own parameters, and the parameters of another UTAdvanced with RS-485 communication connected via the RS-485 terminal.

Communication function	Protocol	Connectable device	Terminal position	Suffix code
PROFIBUS-DP communication	PROFIBUS-DP	PCL (sequencer)	Rear Open Network terminal (E3-terminal area)	PROFIBUS-DP: Type 3 = 4
DeviceNet communication	DeviceNet			DeviceNet: Type 3 = 5
CC-Link communication	CC-Link			CC-Link: Type 3 = 4
RS-485 communication	Modbus/RTU	UTAdvanced		

► Terminal position: UTAdvanced Operation Guide or User's Manual

1.1.1 Explanation of Terms

Example: Overview of Open Network Communication Connection



Open network master

(PROFIBUS-DP master, DeviceNet master, CC-Link master)

An open network master is a controller which exchanges information with slaves periodically and which is a PLC or PC (which is called a class 1 master in PROFIBUS-DP).

An engineering or configuration device is also an open network master which is a PC on which configuration software is installed or software (which is called a class 2 master in PROFIBUS-DP.)

Open Network slave/Modbus master

(PROFIBUS-DP slave, DeviceNet slave, CC-Link slave)

This is an input and output device that is accessed by the master. UTAdvanced (with Open Network communication) runs as a Open Network slave.

Furthermore, UTAdvanced (with Open Network communication), which runs as a Open Network slave, also runs as a Modbus master using the RS-485 terminal.

It reads and writes the parameters of Modbus slaves, which are connected via RS-485 communication, according to the profile definition.

Modbus slave

This is UTAdvanced (with RS-485 terminal). Up to 31 slaves can be connected.

IN area (UTAdvanced → PLC)

This is an area for a PLC to refer to the slave data. Parameters of the Open Network and Modbus slaves that are defined in the profile are always updated.

OUT area (PLC → UTAdvanced)

This is an area for a PLC to rewrite the slave parameters. When the OUT area is rewritten, the corresponding parameters of the Open Network slave and Modbus slaves are also rewritten.

Profile

This defines how to assign the slave parameters to the IN and OUT areas.

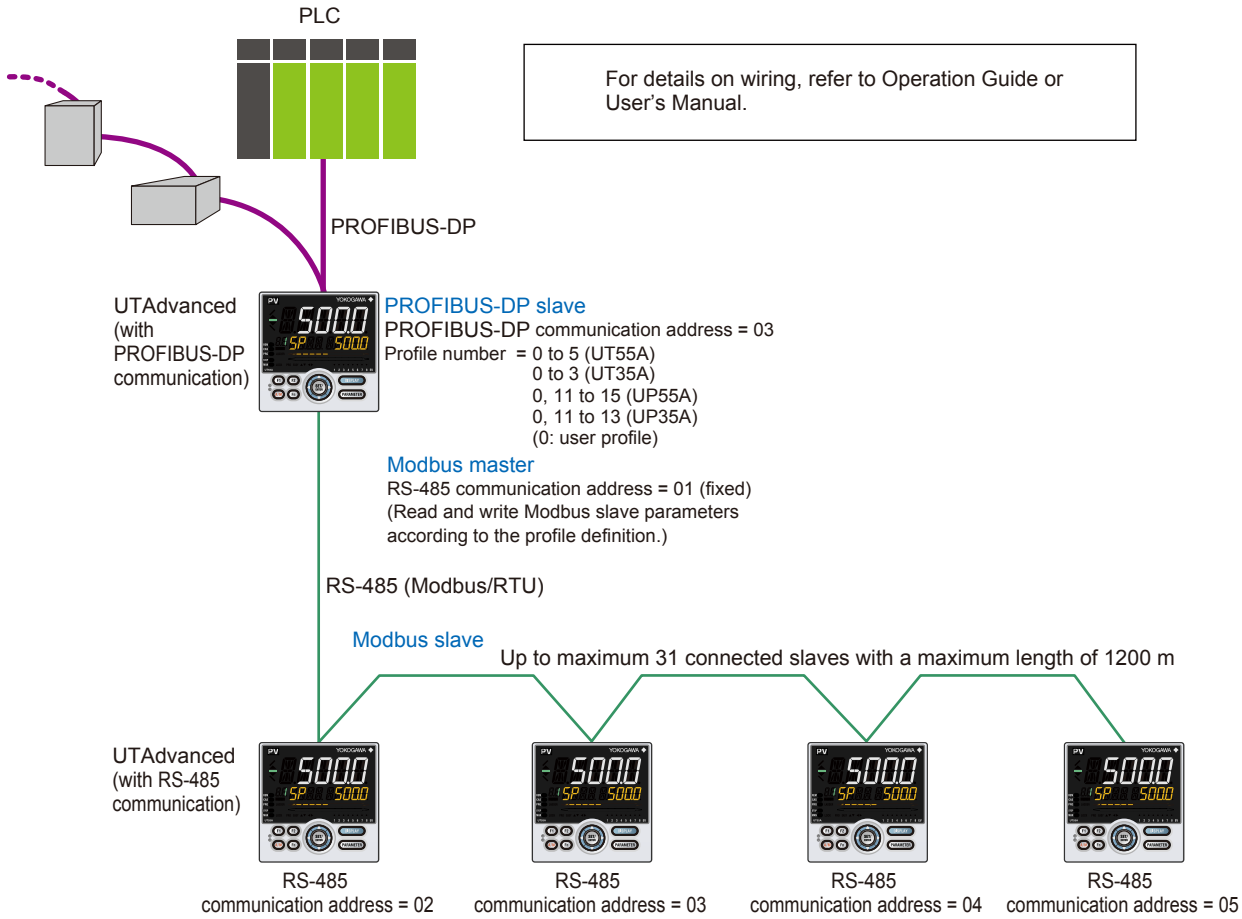
1.2 PROFIBUS-DP Communication

1.2.1 Communication Specifications of UTAdvanced (with PROFIBUS-DP Communication)

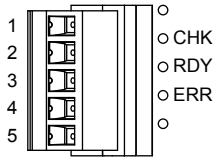
	Communication specifications
Type	Slave
Supported on version	DP V0
Baud rate	9.6k, 19.2k, 93.75k, 187.5k, 0.5M, 1.5M, 3M, 6M, 12M, 45.45k bps, AUTO*
Transmission distance	1200m (9.6k, 19.2k, 45.45k, 93.75k bps), 1000m (187.5k bps), 400m (0.5M bps), 200m (1.5M bps), 100m (3M, 6M, 12M bps)
Communication address	0 to 125 It is recommended to use 3 to 125 for general use of PROFIBUS-DP.
Max. size of IN area	244 byte
Max. size of OUT area	244 byte
Terminal	5-pin terminal block
LED	CHK: Hardwea (red) RDY: Network Status (green) ERR: Communication failure (red)

*: AUTO automatically sets the baud rate to that of the host controller (PROFIBUS-DP master).

Example:



1.2.2 LEDs (on Rear Panel)

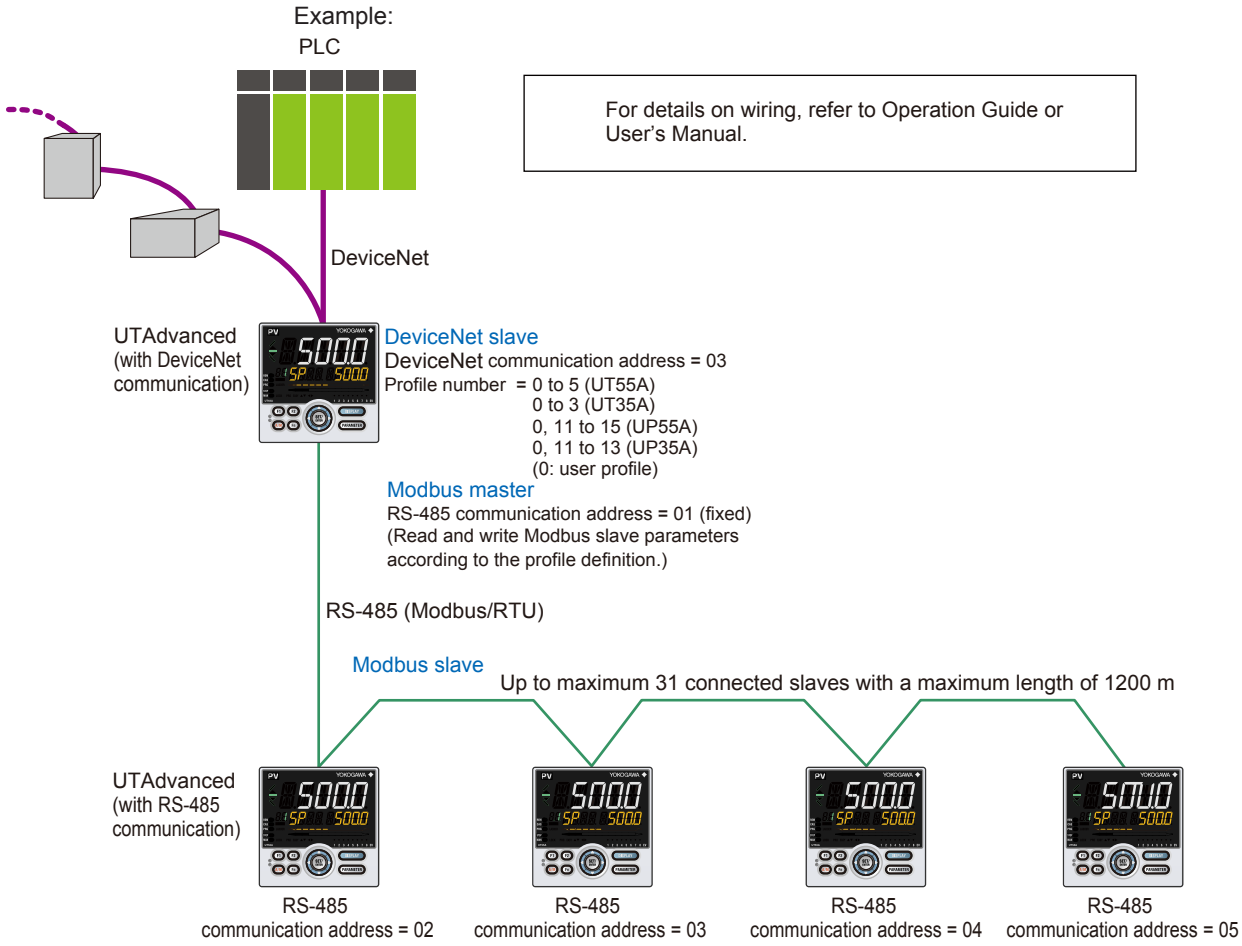


LED		Description	Remedy	Modbus communication
CHK (red)	Unlit	Normal	–	Normal
	Red, lit	User profile error	Download the user profile again.	Communication interruption
RDY (green)	Unlit	No power, or Communication failure	Check the power supply and connection condition.	–
	Green, lit	Normal Communicating successfully	–	
ERR (red)	Unlit	Normal	–	–
	Red, lit	Not connected	Check the wiring to the PROFIBUS-DP master and configuration.	
	Red, flashing	Communication failure	Check the condition of the connection to the PROFIBUS-DP master.	

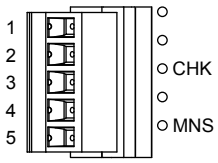
1.3 DeviceNet Communication

1.3.1 Communication Specifications of UTAdvanced (with DeviceNet Communication)

	Communication specifications
Type	Slave
Supported on version	-
Baud rate	125k, 250k, 500k bps
Transmission distance	500m (125k bps), 250m (250k bps), 100m (500k bps)
Communication address	0 to 63
Max. size of IN area	254 byte
Max. size of OUT area	254 byte
Terminal	5-pin terminal block
LED	CHK: Hardwea (red) MNS: Module Status, Network Status (red/green)



1.3.2 LEDs (on Rear Panel)



LED		Description	Remedy	Modbus communication
CHK (red)	Unlit	Normal	–	Normal
	Red, lit	User profile error	Download the user profile again.	Communication interruption
MNS (green/red)	Unlit	No electricity	Check the power supply and connection condition.	–
		Not on-line		
	Green, lit	Normal. Communicating successfully	–	
	Green, flashing	Not connected	Check the connection condition.	
	Red, lit	Critical link failure	When not communication, it is communication address duplication or bus-off error. If the node address is duplicated or a bus-off error has occurred, communication becomes impossible and the module stops operating. Remove the node address duplication or improve the network environment and restart the module. A bus-off error occurs when the error rate in the network is very high. If a hardware malfunction occurs, it may be necessary to repair or replace the unit.	–
	Red, flashing	Communication timeout	Check the power supply and connection condition.	
	Green/red, flashing	At power-on	–	Communication interruption
		Communication faulted	Check the connection condition.	–

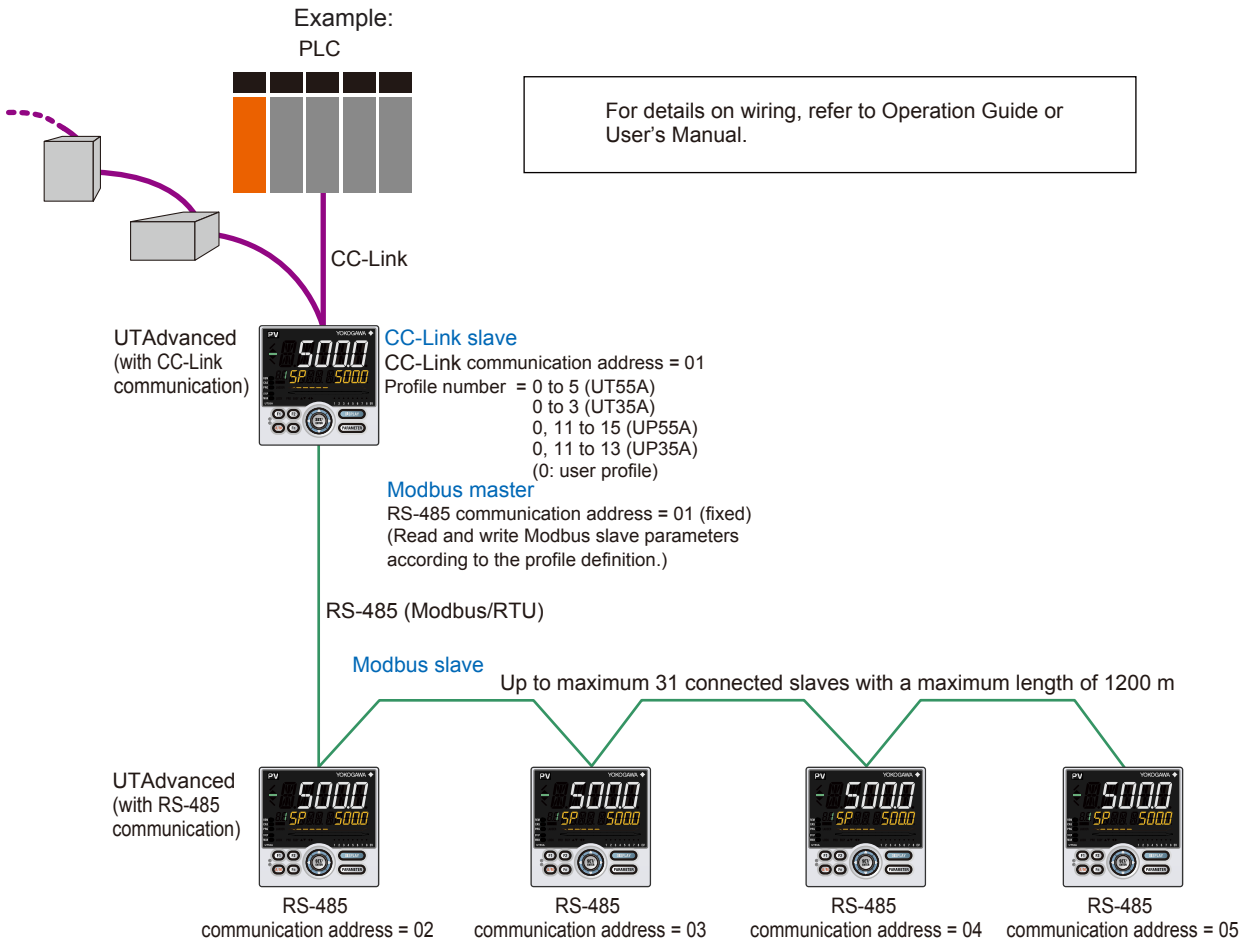
1.4 CC-Link Communication

1.4.1 Communication Specifications of UTAdvanced (with CC-Link Communication)

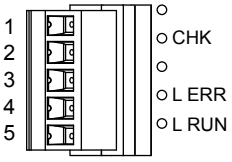
	Communication specifications
Type	Slave
Supported on version	Ver.1.10 and Ver.2.00
Baud rate	156k, 625k, 2.5M, 5M, 10M bps
Transmission distance	1.2km (156k bps), 600m (625k bps), 200m (2.5M bps), 150m (5M bps), 100m (10M bps) When used optical repeater: 7.6 km (156k) to 4.3 km (10M)
Communication address	0 to 64 *
Max. size of IN area	4-station occupied x8 setting: 368 byte (128 word + 896 bit)
Max. size of OUT area	4-stations occupied x8 setting: 368 byte (128 word + 896 bit)
Terminal	5-pin terminal block
LED	CHK: Hardwea (red) L ERR: Communication failure (red) L RDY: Network Status (green)

*: If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.

Example:



1.4.2 LEDs (on Rear Panel)



LED		Description	Remedy	Modbus communication
CHK (red)	Unlit	Normal	–	Normal
	Lit (red)	User profile error	Download the user profile again.	Communication interruption
		Address error	Change the setting so that the total number of addresses and occupied stations do not exceed 65.	
L RUN (green)	Unlit	No carrier detected/ Communication timeout	Check the power supply and connection condition.	–
	Lit (green)	Normal Communicating successfully	–	
L ERR (red)	Unlit	Normal	–	–
	Lit (red)	Communication failure (CRC error)	Check that there is no problem with the network cable, repeater, etc.	

1.5 RS-485 Communication (Modbus Master/Slave)

1.5.1 Communication Specifications

	Communication specifications
Communication interface	RS-485
Communication method	2-wire type
Connection method	1:n multi-drop method (n = max. 31)
Baud rate	9600, 19200, 38400 bps
Communication distance	Max. 1200 m
Protocol	Fixed at Modbus/RTU
Data length	Fixed at 1 bits
Parity bit	Fixed at EVEN
Stop bit	Fixed at 1 bit

Note

The RS-485 communication address as a Modbus master is 1 (fixed).

1.5.2 Connected Controller

UTAdvanced with RS-485 communication can be connected as a Modbus slave.

1.5.3 Conditions of Connected Controller

The RS-485 communication conditions of a Modbus slave are the same as those of the Modbus master.
The RS-485 communication address of a Modbus slave is set from 2 to 32 without duplication.

Note

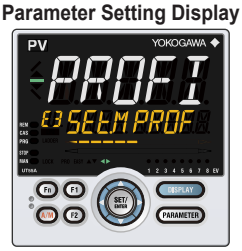
The update cycle of the data that is periodically read from each controller becomes longer as the number of connected controllers increases. If controllers that are not connected are registered in the profile, data updating in the first cycle will take a long time. The update cycle is optimized in and after the second cycle, because data is collected by skipping the controllers that are not connected.

2.1 Setting Parameters

2.1.1 Setting PROFIBUS-DP Communication (for PROFIBUS-DP Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > **PARAMETER** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**PROF**] Menu Display in E3) > **SET/ENTER** key (The setting parameter is displayed.)
(E3 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value
BR	**E3**	Baud rate	9.6k bps	9.6K	PROF	AUTO
			19.2k bps	19.2K		
			93.75k bps	93.75K		
			187.5k bps	187.5K		
			0.5M bps	0.5M		
			1.5M bps	1.5M		
			3M bps	3M		
			6M bps	6M		
			12M bps	12M		
			AUTO	AUTO		
			45.45k bps	45.45K		
ADR		Address	0 to 125			3
BPS		Baud rate	9600 bps	9600		38400
			19200 bps	19200		
			38400 bps	38400		
FILE		Profile number	User profile *1	0		0
			Simple PID control, 3 connected slaves (for UT)	1		
			Simple PID control, 5 connected slaves (for UT)	2		
			Simple PID control, 8 connected slaves (for UT)	3		
			Cascade control, 3 connected slaves (for UT) *3	4		
			Cascade control, 5 connected slaves (for UT) *3	5		
			Simple PID control, 2 connected slaves (for UP)	11		
			Simple PID control, 4 connected slaves (for UP)	12		
			Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		
			Cascade control, 2 connected slaves (for UP) *4	14		
			Cascade control, 1 connected slavee, with proguram pattern setting (for UP) *4	15		
SCAN *2		Automatic rescan time	OFF	OFF		OFF
			1 minute	1M		
			10 minutes	10M		
			30 minutes	30M		
			60 minutes	60M		

*1: The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.
► "3.4.2 Types of Profile" in this manual

*2: This parameter may not be displayed depending on the parameter display level (LEVL) setting.
► UTAdvanced Operation Guide or User's Manual (in CD-ROM)

*3: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly.
These numbers cannot be set for UT35A.

*4: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.
These numbers cannot be set for UP35A.

Description

- Baud rate (BR for PROFIBUS-DP)
  Set the same baud rate as that for the PROFIBUS-DP master controller to be connected. The unit is bps (bits per second). Selecting AUTO automatically sets the same baud rate as that for the master controller.

- Address
  This is the address of PROFIBUS-DP slave.
  An arbitrary number from 0 to 125 can be set. (A duplicate number cannot be set on the same network.)
  It is recommended to use 3 to 125 for general use of PROFIBUS.

- Baud rate (BPS for RS458 (Modbus/RTU) communication)
  Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).

- Profile number
  Set the profile number to be used.
  For UT55A: 0 to 5
  For UT35A: 0 to 3
  For UP55A: 0, 11 to 15
  For UP35A: 0, 11 to 13
  (0: user profile)

- Automatic rescan time
  Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

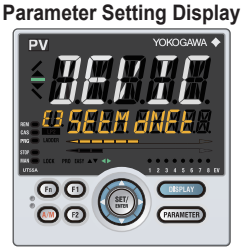
Note

The RS-485 communication address as a Modbus master is 1 (fixed).

2.1.2 Setting DeviceNet Communication (for DeviceNet Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > **PARAMETER** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**DNET**] Menu Display in E3) > **SET/ENTER** key (The setting parameter is displayed.)
(E3 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value
BR	**E3**	Baud rate	125k bps	125K	DNET	AUTO
			250k bps	250K		
			500k bps	500K		
ADR		Address	0 to 63			63
BPS		Baud rate	9600 bps	9600		38400
			19200 bps	19200		
			38400 bps	38400		
FILE		Profile number	User profile *1	0		0
			Simple PID control, 3 connected slaves (for UT)	1		
			Simple PID control, 5 connected slaves (for UT)	2		
			Simple PID control, 8 connected slaves (for UT)	3		
			Cascade control, 3 connected slaves (for UT) *3	4		
			Cascade control, 5 connected slaves (for UT) *3	5		
			Simple PID control, 2 connected slaves (for UP)	11		
			Simple PID control, 4 connected slaves (for UP)	12		
			Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		
			Cascade control, 2 connected slaves (for UP) *4	14		
			Cascade control, 1 connected slavee, with proguram pattern setting (for UP) *4	15		
SCAN *2		Automatic rescan time	OFF	OFF		OFF
			1 minute	1M		
			10 minutes	10M		
			30 minutes	30M		
			60 minutes	60M		

*1: The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.
► "3.4.2 Types of Profile" in this manual

*2: This parameter may not be displayed depending on the parameter display level (LEVL) setting.
► UTAdvanced Operation Guide or User's Manual (in CD-ROM)

*3: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly.
These numbers cannot be set for UT35A.

*4: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.
These numbers cannot be set for UP35A.

Description

- Baud rate (BR for DeviceNet)
  Set the same baud rate as that for the DeviceNet master controller to be connected.
  The unit is bps (bits per second).

- Address
  This is the address of DeviceNet slave.
  An arbitrary number from 0 to 63 can be set. (A duplicate number cannot be set on the same network.)

- Baud rate (BPS for RS458 (Modbus/RTU) communication)
  Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).

- Profile number
  Set the profile number to be used.
  For UT55A: 0 to 5
  For UT35A: 0 to 3
  For UP55A: 0, 11 to 15
  For UP35A: 0, 11 to 13
  (0: user profile)

- Automatic rescan time
  Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

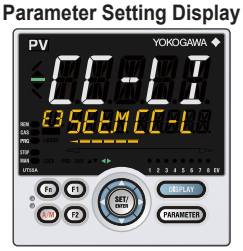
Note

The RS-485 communication address as a Modbus master is 1 (fixed).

2.1.3 Setting CC-Link Communication (for CC-Link Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > **PARAMETER** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**CC-L**] Menu Display in E3) > **SET/ENTER** key (The setting parameter is displayed.)
(E3 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value
BR	**E3**	Baud rate	156k bps	156K	CC-L	AUTO
			625k bps	625K		
			2.5M bps	2.5M		
			5M bps	5M		
			10M bps	10M		
ADR		Address	0 to 64 *1			1
BPS		Baud rate	9600 bps	9600		38400
			19200 bps	19200		
			38400 bps	38400		
FILE		Profile number	User profile *2	0		0
			Simple PID control, 3 connected slaves (for UT)	1		
			Simple PID control, 5 connected slaves (for UT)	2		
			Simple PID control, 8 connected slaves (for UT)	3		
			Cascade control, 3 connected slaves (for UT) *3	4		
			Cascade control, 5 connected slaves (for UT) *3	5		
			Simple PID control, 2 connected slaves (for UP)	11		
			Simple PID control, 4 connected slaves (for UP)	12		
			Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		
			Cascade control, 2 connected slaves (for UP) *5	14		
			Cascade control, 1 connected slavee, with proguram pattern setting (for UP) *5	15		
SCAN *3		Automatic rescan time	OFF	OFF		OFF
			1 minute	1M		
			10 minutes	10M		
			30 minutes	30M		
			60 minutes	60M		

*1: If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.

*2: The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.
► "4.4.2 Types of Profile" in this manual

*3: This parameter may not be displayed depending on the parameter display level (LEVL) setting.
► UTAdvanced Operation Guide or User's Manual (in CD-ROM)

*4: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly.
These numbers cannot be set for UT35A.

*5: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.
These numbers cannot be set for UP35A.

Description

- Baud rate (BR for CC-Link
  Set the same baud rate as that for the CC-Link master controller to be connected. The unit is bps (bits per second).

- Address
  This is the address of CC-Link slave.
  An arbitrary number from 0 to 64 can be set. (A duplicate number cannot be set on the same network.)

Note

If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.

- Baud rate (BPS for RS458 (Modbus/RTU) communication)
  Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).

- Profile number
  Set the profile number to be used.
  For UT55A: 0 to 5
  For UT35A: 0 to 3
  For UP55A: 0, 11 to 15
  For UP35A: 0, 11 to 13
  (0: user profile)

- Automatic rescan time
  Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

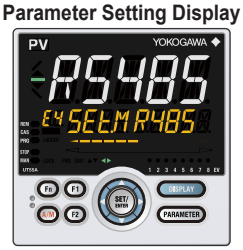
Note

The RS-485 communication address as a Modbus master is 1 (fixed).

2.1.4 Setting RS-485 Communication (Modbus Slave)

Setting Display

Parameter Setting Display



UT55A/UT35A/UP55A/UP35A:
Operation Display > **PARAMETER** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**R485**] Menu Display in E3 or E4) > **SET/ENTER** key (The setting parameter is displayed.) (E3 or E4 indicating the terminal area is displayed on Group display.)

UT52A/UT32A:
Operation Display > **PARA** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**R485**] Menu Display in E1) > **SET/ENTER** key (The setting parameter is displayed.) (E1 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value
PSL	UT55A/ UP55A: E3 or E4 UT35A/ UP35A: E3 UT52A/ UT32A: E1	Protocol selection	Modbus (RTU)	MBRTU	R485	MBRTU
BPS		Baud rate	9600 bps	9600		19200
			19200 bps	19200		
			38400 bps *1	38400		
PRI		Parity	Even	EVEN		EVEN
STP		Stop bit	1 bit	1		1
DLN		Data length	8 bit	8		8
ADR		Address	2 to 32	2 to 32		1

*1: 38400 bps can be specified when the UT55A/UT35A/UP55A/UP35A suffix code Type 3 = 1 or UT52A/ UT32A suffix code Type 2 = 1.

Description

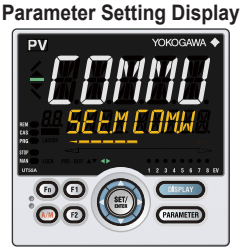
- Protocol selection
  Set the protocol to be MBRTU.
- Baud rate
  Set the same RS-485 communication baud rate as the UTAdvanced (Open Network slave/Modbus master) that the controller is to be connected to. The baud rate unit is bps (bits per second).
- Parity
  Set the EVEN.
- Stop bit
  Set the 1 bit.
- Data length
  Set the 8 bit.
- Address
  Set the 2 to 32.

2.2 Setting Write Enable for UTAdvanced

Writing to registers via all communication protocols can be permitted and prohibited. However, communication using the light-loader (on the front panel) is possible. Writing to registers via communication is possible only when the parameter COM.W (communication write enable/disable) in the KLOC menu is set to OFF (enable).

Setting Display

Parameter Setting Display



UT55A/UT35A/UP55A/UP35A:
Operation Display > **PARAMETER** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**KLOC**] Menu Display) > **SET/ENTER** key (The COM.W parameter is displayed.)

UT52A/UT32A:
Operation Display > **PARA** and **Left arrow** keys simultaneously for 3 seconds (to the [**CTL**] Menu Display) > **Right arrow** key (to the [**KLOC**] Menu Display) > **SET/ENTER** key (The COM.W parameter is displayed.)

Setting Details

Parameter symbol	Name	Setting range	Menu symbol	Initial value
COM.W	Communication write enable/ disable	OFF: Enable (0) ON: Disable (1)	KLOC	OFF (0)

Note: Figures in parentheses "( )" are values to be set when performing communication.

3.1 Overview

PROFIBUS/DeviceNet is an open field bus standard used in various applications for factory automation and process automation.

PROFIBUS-DP: IEC61158
DeviceNet: IEC62026

PROFIBUS-DP/DeviceNet (Decentralized Periphery) is used for communication between PLCs and remote I/O, enabling high-speed data transmission.

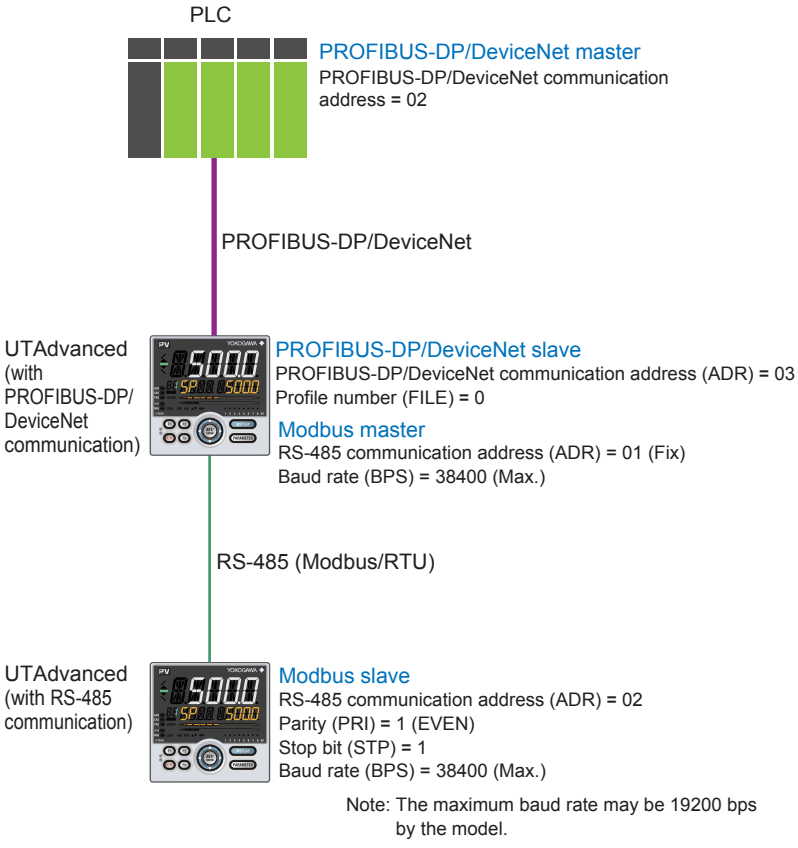
Note

For details of PROFIBUS specifications and information, see the documents published from the PROFIBUS Organization in respective regions.
PROFIBUS International: http://www.profibus.com/

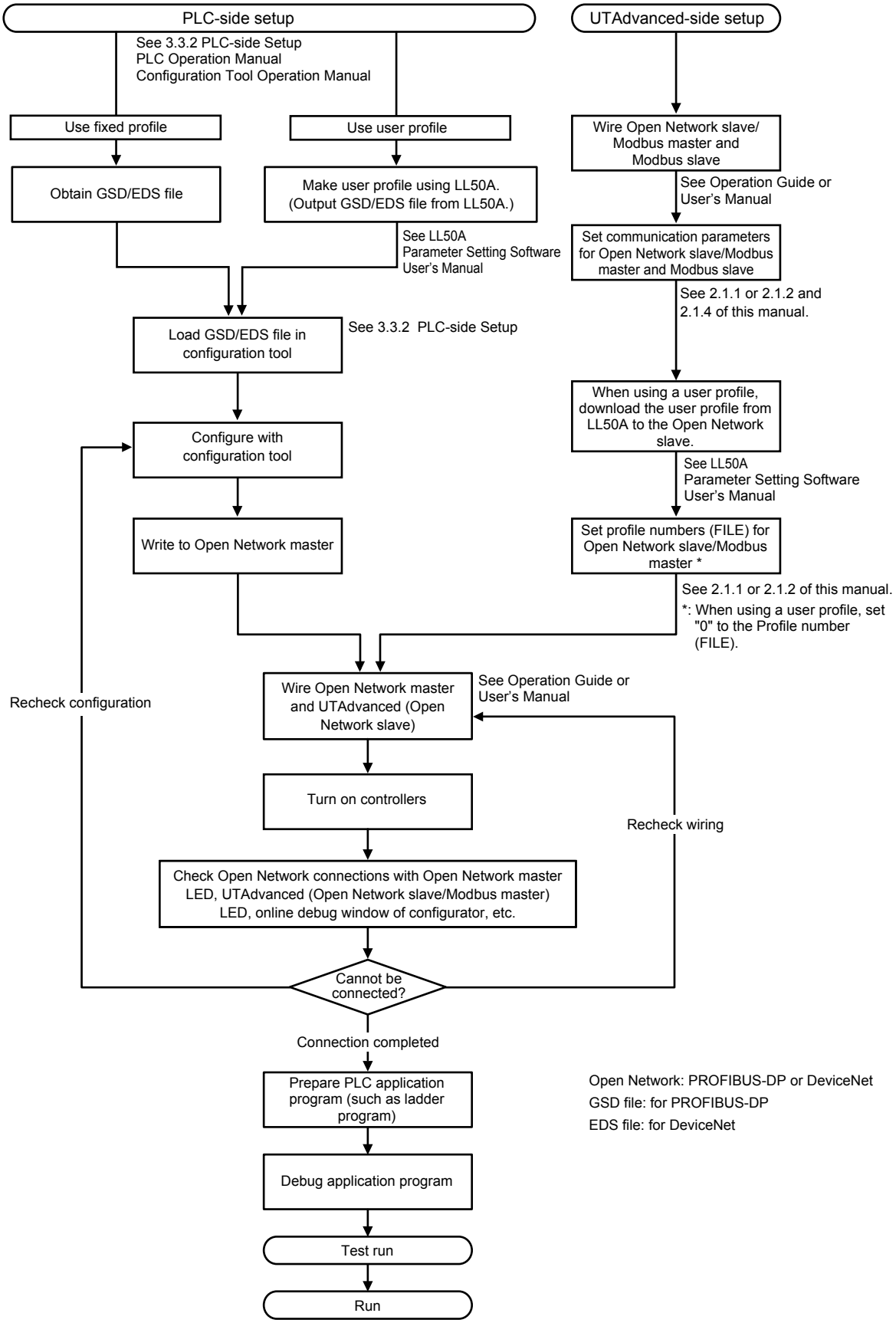
Note

For details of DeviceNet specifications and information, see the documents published from the ODVA Inc. .
ODVA Inc.: http://www.odva.org/

Example: PROFIBUS-DP/DeviceNet Communication Connection



3.2 Workflow


PLC-side setup
UTAdvanced-side setup
See 3.3.2 PLC-side Setup
PLC Operation Manual
Configuration Tool Operation Manual
Use fixed profile
Use user profile
Wire Open Network slave/ Modbus master and Modbus slave
Obtain GSD/EDS file
Make user profile using LL50A. (Output GSD/EDS file from LL50A.)
See Operation Guide or User's Manual
See LL50A Parameter Setting Software User's Manual
Set communication parameters for Open Network slave/Modbus master and Modbus slave
See 2.1.1 or 2.1.2 and 2.1.4 of this manual.
Load GSD/EDS file in configuration tool
See 3.3.2 PLC-side Setup
When using a user profile, download the user profile from LL50A to the Open Network slave.
Configure with configuration tool
See LL50A Parameter Setting Software User's Manual
Write to Open Network master
Set profile numbers (FILE) for Open Network slave/Modbus master *
See 2.1.1 or 2.1.2 of this manual.
*: When using a user profile, set "0" to the Profile number (FILE).
Recheck configuration
Wire Open Network master and UTAdvanced (Open Network slave)
See Operation Guide or User's Manual
Turn on controllers
Recheck wiring
Check Open Network connections with Open Network master LED, UTAdvanced (Open Network slave/Modbus master) LED, online debug window of configurator, etc.
Cannot be connected?
Connection completed
Prepare PLC application program (such as ladder program)
Debug application program
Test run
Run
Open Network: PROFIBUS-DP or DeviceNet
GSD file: for PROFIBUS-DP
EDS file: for DeviceNet

3.3 Setting Up Connection with Master

3.3.1 UTAdvanced-side Setup

Wiring

For wiring, see UTAdvanced Operation Guide or User's Manual.

Setting communication parameters

For setting parameters, see 2.1.1 or 2.1.2 and 2.1.4 of this manual.

Downloading User Profile

When using a user profile, download the user profile via LL50A.
For the procedure of download, see LL50A Parameter Setting Software User's Manual.

3.3.2 PLC-side Setup

GSD File (PROFIBUS-DP), EDS file (DeviceNet)

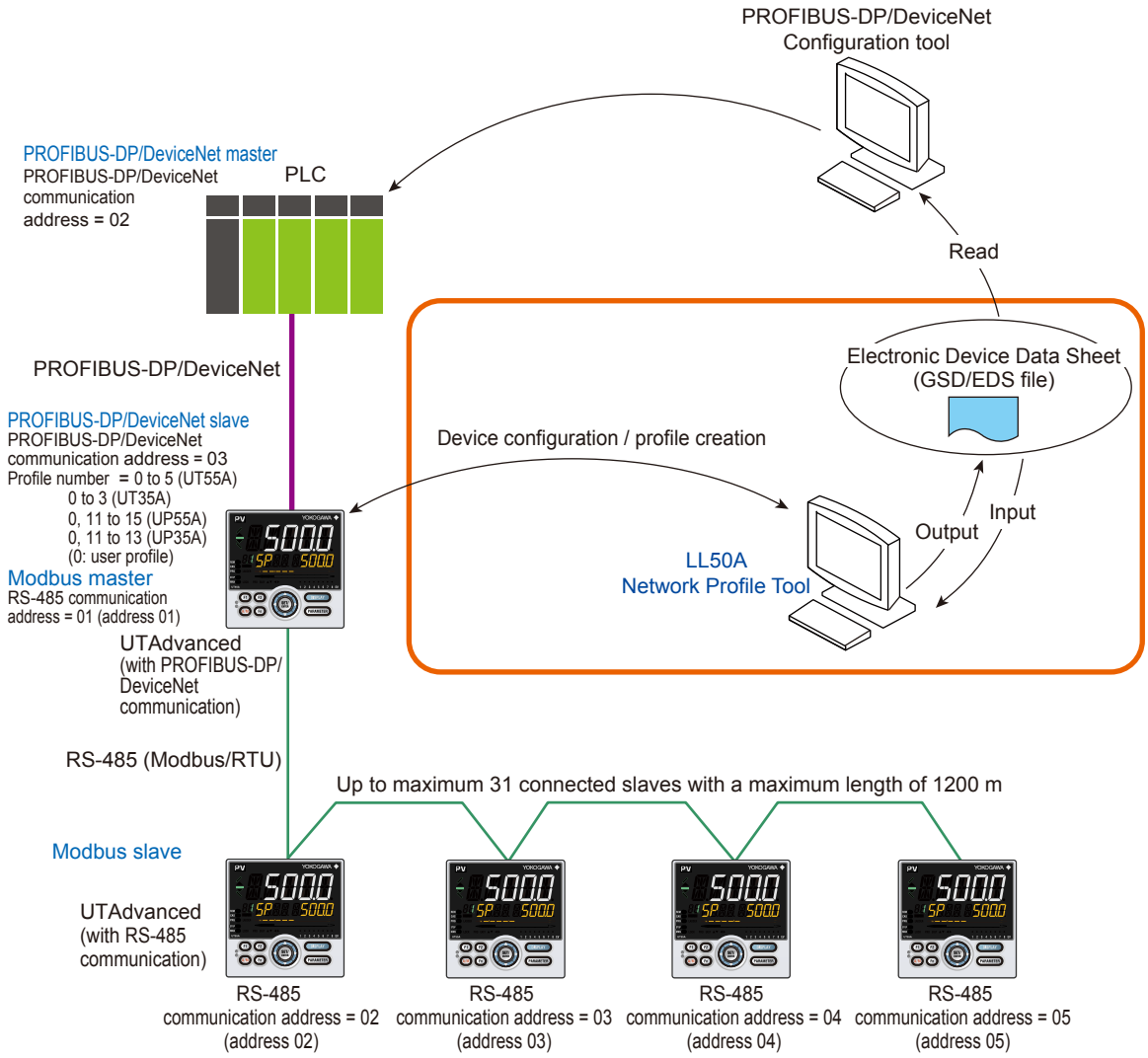
To connect UTAdvanced to a network as a PROFIBUS-DP/DeviceNet slave, first of all, the Electronic Device Data Sheet of UTAdvanced (PROFIBUS-DP/DeviceNet slave) needs to be installed in the configuration tool.

Electronic Device Data Sheet: GSD file (PROFIBUS-DP)
EDS file (DeviceNet)

The GSD/EDS file contains the device information on UTAdvanced.
PLC communicates with UTAdvanced (PROFIBUS-DP/DeviceNet slave) based on the information of the GSD/EDS file.
For how to obtain the configuration tool, contact the PROFIBUS-DP/DeviceNet master vendor.
For how to use the configuration tool, see the operation manual for the configuration tool.

Note

GSD/EDS file for UTAdvanced which can be installed in the configuration tool is one file only. When multiple user profiles are created using LL50A Network Profile Tool, set the IN/OUT area size to the maximum size.
If the IN/OUT area sizes are different between each user profile, UTAdvanced cannot be connected to PLC.



How to Obtain the GSD/EDS File

· Output the GSD/EDS file using the Network Profile Tool of LL50A.

► LL50A Parameter Setting Software User's Manual

· Obtain the GSD/EDS file from the Yokogawa Web site when using a fixed profile:

URL: www.yokogawa.com/ns/utadv/

	File name	Explanation
PROFUBUS-DP	YEC45F2.GSD	
DeviceNet	UTAdvanced_Profile0.eds	Profile number: 0 (for UT55A/UT35A/UP55A/UP35A)
	UTAdvanced_Profile1.eds	Profile number: 1 (for UT55A/UT35A)
	UTAdvanced_Profile2.eds	Profile number: 2 (for UT55A/UT35A)
	UTAdvanced_Profile3.eds	Profile number: 3 (for UT55A/UT35A)
	UTAdvanced_Profile4.eds	Profile number: 4 (for UT55A)
	UTAdvanced_Profile5.eds	Profile number: 5 (for UT55A)
	UTAdvanced_Profile11.eds	Profile number: 11 (for UP55A/UP35A)
	UTAdvanced_Profile12.eds	Profile number: 12 (for UP55A/UP35A)
	UTAdvanced_Profile13.eds	Profile number: 13 (for UP55A/UP35A)
	UTAdvanced_Profile14.eds	Profile number: 14 (for UP55A)
	UTAdvanced_Profile15.eds	Profile number: 15 (for UP55A)

Contents of the GSD/EDS File

UTAdvanced is displayed in the following way in the configurator window.

	Item	Description	Explanation
PROFIBUS-DP	File Name	YEC45F2.GSD	-
	Slave Family	5 (Controllers)	-
DeviceNet	Vendor	Yokogawa Electric Corp.[250]	-
	Type	Communication Adapter[12]	-
	Product	UTAdvanced Profile0[100]	Profile number: 0 (for UT55A/UT35A/UP55A/UP35A)
		UTAdvanced Profile1[101]	Profile number: 1 (for UT55A/UT35A)
		UTAdvanced Profile2[102]	Profile number: 2 (for UT55A/UT35A)
		UTAdvanced Profile3[103]	Profile number: 3 (for UT55A/UT35A)
		UTAdvanced Profile4[104]	Profile number: 4 (for UT55A)
		UTAdvanced Profile5[105]	Profile number: 5 (for UT55A)
		UTAdvanced Profile11[111]	Profile number: 11 (for UP55A/UP35A)
		UTAdvanced Profile12[112]	Profile number: 12 (for UP55A/UP35A)
		UTAdvanced Profile13[113]	Profile number: 13 (for UP55A/UP35A)
		UTAdvanced Profile14[114]	Profile number: 14 (for UP55A)
		UTAdvanced Profile15[115]	Profile number: 15 (for UP55A)

Note

Precaution in using SIEMENS PROFIBUS-DP master in transmission speed of 6M bps or 12M bps
In the configuration for PROFIBUS-DP for the following cases, select the "Constant bus cycle time" check box and set the DP constant bus cycle time to 2 ms or more: When using a fixed profile No. 3 or No.5, or when using a user profile with 50 words or more for IN or OUT area.

3.4 Profile

3.4.1 Contents of Profile

A profile consists of a predefined fixed-part and a data-part to which parameters are assigned.
Flags to switch pages of the data-part and flags to indicate the connection status of controllers are assigned to the fixed-part.
The data-part can be used by switching pages. The number of pages of a profile is 4 (1 to 4).

Note

Parameters are classified into each page of a profile, i.e. the profile is classified into pages of the more frequently used parameters for routine operation and the less frequently used parameters for startup and batch-start.
Classification for each page allows reducing the memory space occupied in the master. Furthermore, it allows optimizing the updating of the data (in the IN area) read from UTAdvanced.

Example: Profile number 0 (Page 1)

Profile number 0 (User Profile [initial value: simple PID control, 2 connected slaves]) Page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	⋮	⋮		⋮	⋮
	15	(Reserved)		15	(Reserved)
1	0	Normal connection slave (address 01)	1	0	Batch write request (address 01)
	1	Normal connection slave (address 02)		1	Batch write request (address 02)
	2	Normal connection slave (address 03)		2	Batch write request (address 03)
	⋮	⋮		⋮	⋮
	15	Normal connection slave (address 16)		15	Batch write request (address 04)
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 05)
	1	Normal connection slave (address 18)		1	Batch write request (address 06)
	2	Normal connection slave (address 19)		2	Batch write request (address 07)
	⋮	⋮		⋮	⋮
	15	Normal connection slave (address 32)		15	Batch write request (address 32)
3		Current profile number	3		(Unused)
4		Current page	4		Page change request
5		01 : PV_L1	5		(Unused)
6		02 : PV_L1	6		(Unused)
7		01 : CSP_L1	7		01 : SP_L1_1
8		02 : CSP_L1	8		02 : SP_L1_1
9		01 : OUT_L1	9		01 : MOUT_L1
10		02 : OUT_L1	10		02 : MOUT_L1
11	0	01 : A.M	11	0	01 : A.M
	1	01 : R.L_L1		1	01 : R.L_L1
	2	01 : S.R		2	01 : S.R
	⋮	⋮		⋮	⋮
	15	01 : ALM8_L1		15	(Unused)
12	⋮	⋮	12	⋮	⋮

(1) (2) (9) (3) (4) (5) (6) (8) (7) (10)

Fixed-part

Data-part (11)

Communication address

Register symbol
See UTAdvanced Series Communication Interface (RS-485, Ethernet) User's Manual.

Communication address

(1) Receive data valid flag (1: valid)
This flag allows checking whether the data in the IN area is valid.
If the flag is set to 1, the data in the IN area is valid. (However, this only applies to the slave data where the normal connection slave flag (address 01 to 32) is also set to 1.)
When the power is turned on or a rescan is requested, the flag is set to 0. When checking whether all slaves registered in the profile are ready for communication is finished, the flag is set to 1.

(2) During-write flag (0: write enable)
This flag allows checking whether writing to the OUT area is enabled.
If the flag is set to 0, writing to the OUT area is enabled.
When the power is turned on, the flag is set to 0. When the write communication is performed, the flag is set to 1. When a response is returned from the slave, or when the time is up, the flag returns to 0.

Note

When the flag is set to 1, a write request is not accepted and is ignored (not held).

(3) Normal connection slave flag (Address 01 to 32) (1: connected)
This flag allows checking whether each slave is connected.
The normal connection of slave flags have 32 bits in the fixed-part of the IN area.
Slave (address 01) in word position 1, and bit position 0 is UTAdvanced that runs as a PROFIBUS-DP/DeviceNet slave.

Note

If normal connection slave (address 01) is set to 0 in a profile in which the PROFIBUS-DP/DeviceNet slave (address 01) is registered, and rescan does not cause the flag to return to 1, it is a failure.

Word position 1 and bit positions 1 to 15, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32.
When slaves (address 01 to 32) are connected, each flag is set to 1. When the power is turned on, the flag is set to 0, and when communication becomes enabled, the flag is set to 1. When communication is disabled, the flag is set to 0. When communication becomes enabled upon a rescan request, the flag is set to 1.

(4) Current profile number
The currently used profile number is displayed.
For UT55A: 0 to 5
For UT35A: 0 to 3
For UP55A: 0, 11 to 15
For UP35A: 0, 11 to 13
(0: user profile)

(5) Current pager
The currently used profile page number (1 to 4) is displayed.

(6) Rescan request flag
A rescan request is made to attempt a retry to establish communication with unconnected slaves. (When connection is normally established with all slave controllers registered in the profile, a rescan is not performed.)
Change the flag in word position 0 and bit position 0 of the OUT area from 0 to 1. Thereafter, return it to 0 when the receive data valid flag has been set to 1.

(7) Write request flag (address 01 to 32)
This flag allows writing all parameters that are assigned to the OUT area together to the slave for each communication address.
Write request flags have 32 bits in the fixed-part of the OUT area. Slave (address 01) in word position 1 and bit position 0 is UTAdvanced that runs as a PROFIBUS-DP/ DeviceNet slave.
Word position 1 and bit positions 1 to 15, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32.
Change the write request flags from 0 to 1 for the slaves (address 01 to 32) to be written, while the during-write flag is set to 0. Thereafter, return them to 0 when the writing process is completed.

(8) Write request flag and (9) Write acknowledgment flag
When a write request is made, regardless of whether the writing is performed individually or all together, both the write acknowledgement flag and write request flag need to be used to reliably recognize that the writing is completed.
When the write request flag is set to 1, while the during-write flag and write acknowledgement flag are set to 0, the write acknowledgment flag is set to 1.
Set the data-part of the OUT area when the write acknowledgment flag is set to 1.
Thereafter, returning the write request flag to 0 prompts the writing to be performed.
When the writing is completed, the write acknowledgment flag is set to 0.
► “3.6 Reading and Writing UTAdvanced Data” in this manual

(10) Page change request
This request switches the currently used profile page.
Set the value in OUT area word position 4 to a value (any of 1 to 4) that is different from the current page (in IN area word position 4).
The receive data valid flag remains set to 0 until the page is switched upon the page change request and the data acquisition is completed.
► “3.7 Switching Pages” in this manual

(11) Data-part
The data format is the same as that of the displayed value of UTAdvanced.
Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.
► LL50A Parameter Setting Software User’s Manual)

3.4.2 Types of Profile

UT55A/UP55A provides one user profile and 5 fixed profiles.
UT35A/UP35A provides one user profile and 3 fixed profiles.
Set each profile numbers according to the configurations.
Profile numbers can be set with the FILE parameter in the PROFIBUS-DP Communication Settings menu (PROF) or DeviceNet Communication Settings menu (DNET).

► Setting FILE parameters: “2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)” or “2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)” in this manual

Example: UT55A/UT35A

For a simple PID control with 6 connected controllers, use “Profile number 3: 8 simple PID controllers”.
For a simple PID control with 10 connected controllers, set the connection devices using “Profile number 0: User profile” with Network Profile Tool of LL50A.

UT55A/UT35A

Profile number	Name	Page number	Item	Data length (byte)	Applicable control mode and control type	
					Control mode	Control type
0	User profile (Initial value: Simple PID Control, 2 connected slaves)	1	Process value, operation mode, alarm status	26	All modes except for Cascade Control (4: CAS)	All type
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
1	Simple PID Control, 3 connected slaves	1	Process value, operation mode, alarm status	46		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
2	Simple PID Control, 5 connected slaves	1	Process value, operation mode, alarm status	70		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
3	Simple PID Control, 8 connected slaves	1	Process value, operation mode, alarm status	106		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
4	Cascade Control, 3 connected slaves	1	Process value, operation mode, alarm status	70	Cascade Control (4: CAS)	
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
5	Cascade Control, 5 connected slaves	1	Process value, operation mode, alarm status	110		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			

UP55A/UP35A

Profile number	Name	Page number	Item	Data length (byte)	Applicable control mode and control type	
					Control mode	Control type
0	User profile (Initial value: Simple PID Control, 2 connected slaves)	1	Process value, operation mode, alarm status	26	All modes except for Cascade Control (4: CAS)	All type
		2	PID parameter (for address 1)			
		3	PID parameter (for address 2)			
		4	Local event-1 to -2 setpoint (for address 1, 2)			
11	Simple PID Control, 2 connected slaves	1	Process value, operation mode, alarm status	48		
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1)			
		4	Local event-1 to -7 setpoint (for address 2)			
12	Simple PID Control, 4 connected slaves	1	Process value, operation mode, alarm status	88		
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1, 2)			
		4	Local event-1 to -7 setpoint (for address 3, 4)			
13	Simple PID Control, 1 connected slave (with program pattern setting)	1	Process value, operation mode, alarm status	162		
		2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance			
		3	Pattern setting			
		4	Segment setting			
14	Cascade Control, 2 connected slaves	1	Process value, operation mode, alarm status	88	Cascade Control (4: CAS)	
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1, 2)			
		4	Local event-1 to -7 setpoint (for address 3, 4)			
15	Cascade Control, 1 connected slave (with program pattern setting)	1	Process value, operation mode, alarm status	162		
		2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance			
		3	Pattern setting			
		4	Segment setting			

User profile

As the default, a parameter for a simple PID control with 2 connected controllers is set. Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.

► LL50A Parameter Setting Software User's Manual

Fixed profile

UT55A/UT35A:

Parameters for a simple PID control with 3, 5, and 8 connected controllers and for a cascade control with 3 and 5 connected controllers are set.

UP55A/UP35A:

Parameters for a simple PID control with 2, 4, and 1 (with program setting function) connected controllers and for a cascade control with 2 and 1 (with program setting function) connected controllers are set.

However, the parameters for a cascade control (Profile numbers: 4 and 5) cannot be used for UT35A, and the parameters for a cascade control (Profile numbers: 14 and 15) cannot be used for UP35A.

3.5 Operation at the Time of Power-On

The following shows how the IN area of UTAdvanced looks like from the perspective of a PLC when UTAdvanced is turned on while the PLC power is already on.

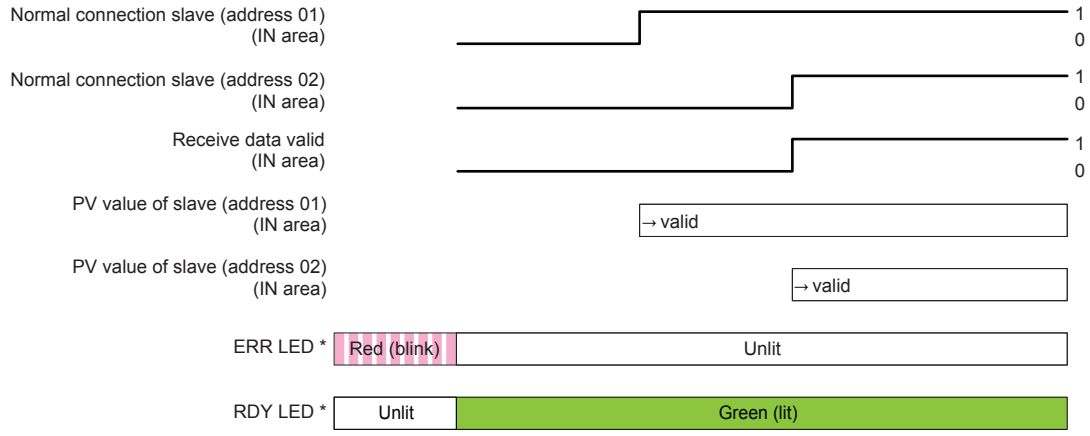
Note

The UTAdvanced data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("→valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1.

► 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual
► "3.9 Profile List" in this manual

3.5.1 Example at the Time of Power-On

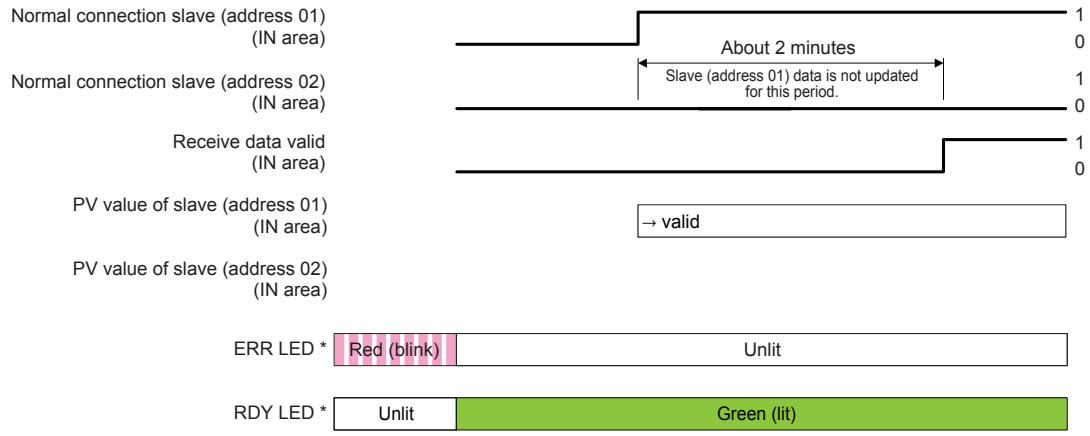
Example of connecting 2 slaves (address 01 and 02):



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.5.2 Example at the Time of Power-On (When Slave (address 02) is not Connected)

Example where slave (address 01) is connected, but slave (address 02) is not connected:



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.6 Reading and Writing UTAdvanced Data

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual
► “3.9 Profile List” in this manual

3.6.1 Reading

Data in the IN area that is always updated can be read.

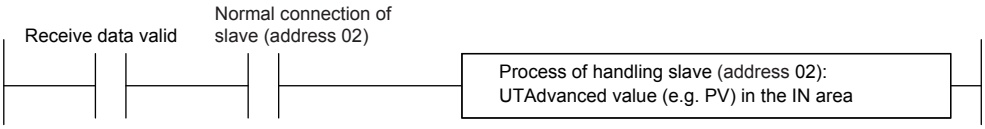
Procedure

1. Check that the receive data valid flag is set to **1**.
2. Check that the normal connection slave flag for a slave to be handled (address 01 to 32) is set to **1**.
3. Data for the corresponding slave (address 01 to 32) in the IN area can be handled.

Note

If both the receive data valid flag and normal connection slave flag are set to 1, the data in the IN area is valid.

Example of ladder program



3.6.2 Writing Individual Parameters

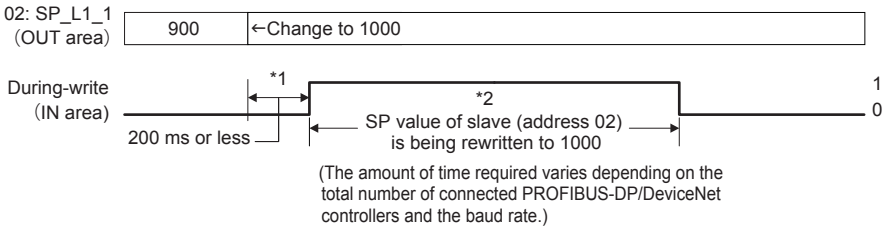
Only the parameter values to be changed in the OUT area can be written.

■ Simple procedure of writing individual parameters used when the write interval is long

Procedure

1. Check that the during-write flag is set to **0**.
2. Change the value in the OUT area to which the parameter to be written is assigned.

Example of rewriting the SP value for slave (address 02):



Note

*1 in the figure above

- If the write value is changed multiple times during the period*1, the last write value is valid.

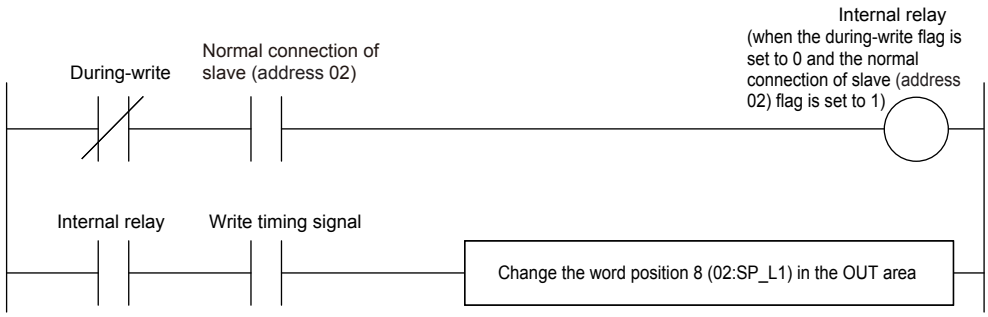
*2 in the figure above

- If the during-write flag is set to 1, a changed value in the OUT area is invalid. The change of the value is ignored (not held).
- Changing a value in the OUT area results in a request for writing the individual parameter.
- UTAdvanced holds the previous values in the OUT area in order to detect changes in the values in the OUT area. The previous values in the OUT area are set to 0 when the power is turned on, or when PROFIBUS-DP/DeviceNet is disconnected. If a value other than 0 is written in the OUT area of a PLC when the disconnected PROFIBUS-DP/DeviceNet is connected, UTAdvanced handles it as a request for writing the individual parameter.
  If you want to write 0 first after the disconnected PROFIBUS-DP/DeviceNet is connected, you need to use batch writing. Furthermore, the first writing after the power is turned on needs to use batch writing.
  ► Batch writing: “3.6.3 Batch writing for Each Communication Address” of this manual
- If the PROFIBUS-DP/DeviceNet baud rate is slow or the PLC scan cycle is long, the PLC program may be unable to detect that the during-write flag in the IN area has been set to 1. In order to reliably detect that the writing is completed, individual parameters need to be written using both the write request flag in the OUT area and the write acknowledgement flag in the OUT area (Procedure of reliably detecting that the writing is completed).

Note

For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User’s Manual.

Example of ladder program

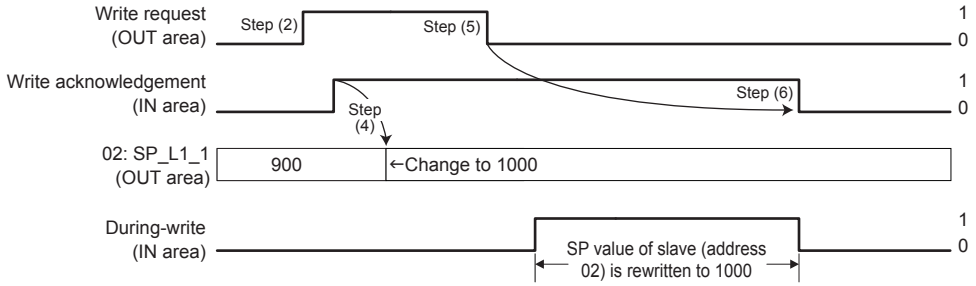


■ Writing individual parameters (Procedure to reliably detect that the writing is completed)

Procedure

1. Check that the during-write flag is set to **0**.
2. Change the write request flag from 0 to **1**.
3. Check that the write acknowledgment flag has been set to **1**.
4. Change the value in the OUT area to which the parameter to be written is assigned.
5. Return the write request flag from 1 to **0** (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that of step 4.
6. When the write acknowledgment flag has been set to 0, the writing is completed.

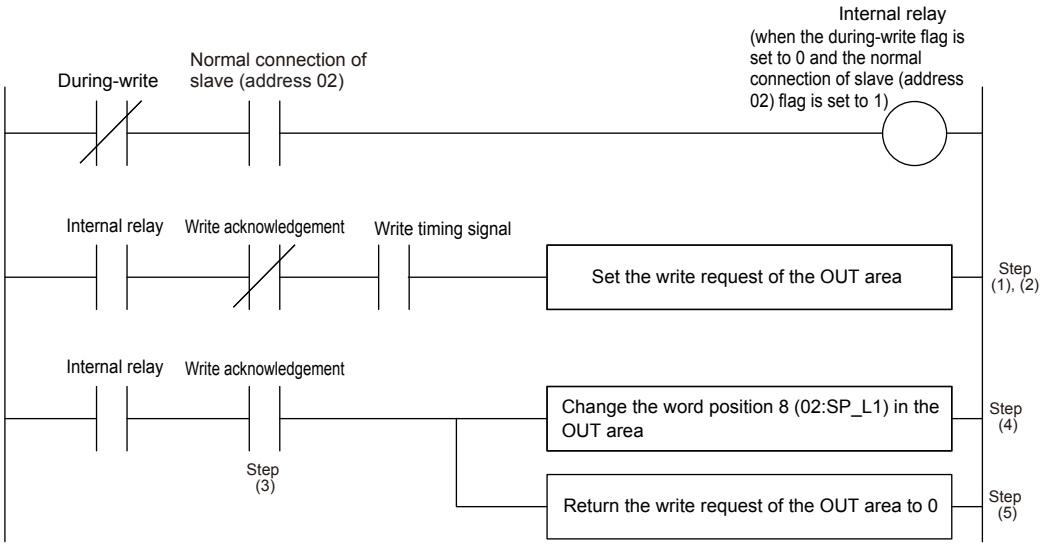
Example of rewriting the SP value of slave (address 02):



Note

- Changing the value in the OUT area results in a request for writing the individual parameter. If you want to write the current values in the OUT area, use batch writing.
- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



3.6.3 Batch Writing for Each Communication Address

Batch writing can be performed on parameter values assigned to the OUT area for each communication address.

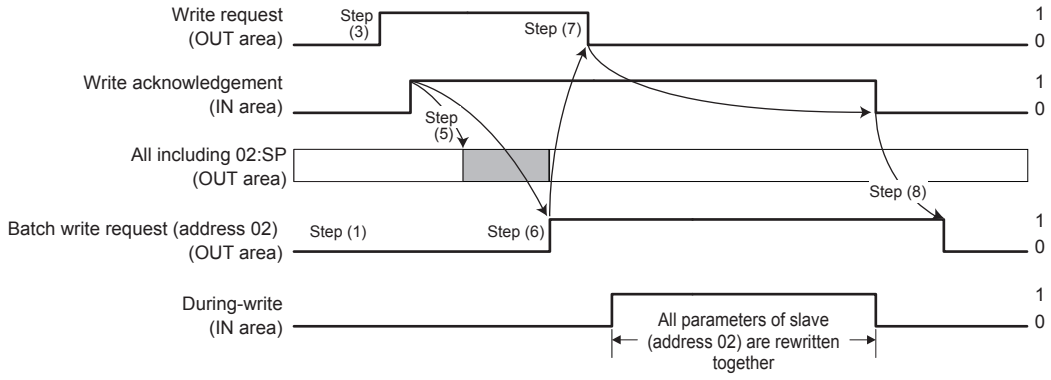
Procedure

1. Set the write request flag for the slaves to be written (address 01 to 32) to 0.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Set the values in the OUT area to which the parameters to be written are assigned. (The same values as the previously written values can also be written.)
6. Change the write request flag for the slaves to be written (address 01 to 32) from 0 to **1**.
7. Return the write request flag from 1 to **0** (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that in steps 5 and 6.
8. When the write acknowledgment flag has been set to 0, the writing is completed. Return the write request flag from 1 to **0**.

Note

- Batch writing writes the values in the OUT area at the point of step (7).
- Changing the write request flag from 0 to 1 (step 6) needs to be performed when the during-write flag is set to 0 and the write acknowledgment flag is set to 1. If these conditions are not met, the write request is invalid.

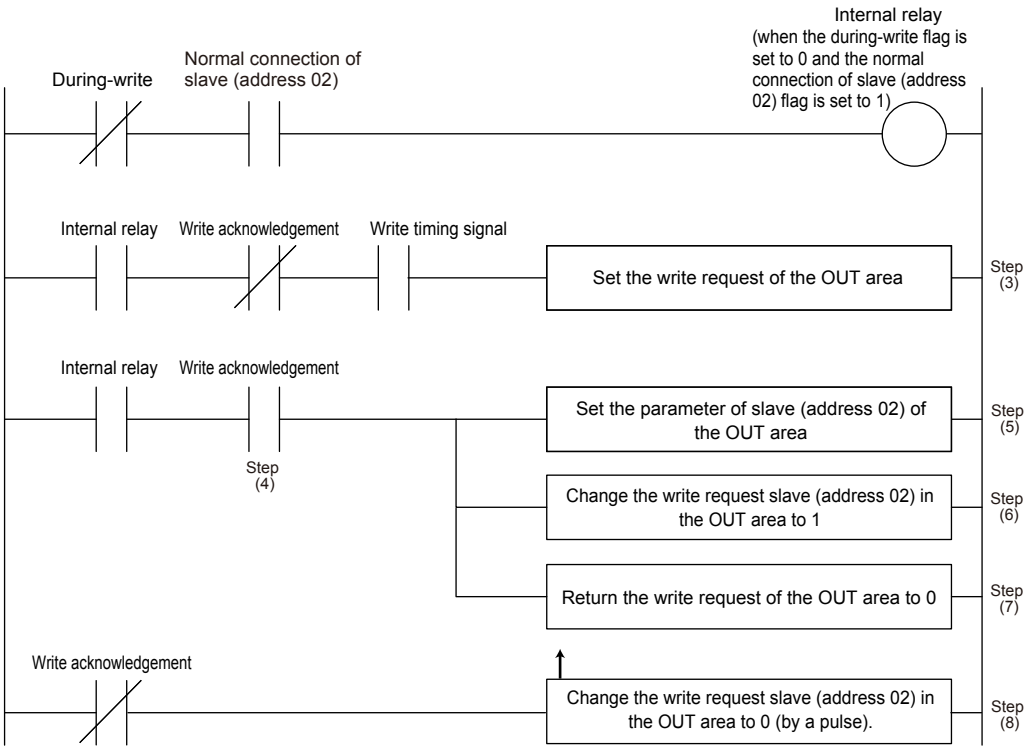
Example of changing the write request flag for slave (address 02) from 0 to 1:



Note

- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.
- When the PROFIBUS-DP/DeviceNet baud rate is slow, or the scan cycle of a PLC is long, the PLC program may be unable to detect that the during-write flag has been set to 0.

Example of ladder program



3.6.4 Reading Program Pattern

Procedure

1. Check that the receive data valid flag and the normal connection slave flag are set to **1**.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
6. Return the write request flag from 1 to **0**.
7. Check that the write acknowledgment flag has been set to **0**.
8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then confirm that is "0".
9. Check that the during-write flag is set to **0**.
10. Change the write request flag from 0 to **1**.
11. Check that the write acknowledgment flag has been set to **1**.
12. Write the required pattern number and the segment number to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
13. Return the write request flag from 1 to **0**.
14. Check that the write acknowledgment flag has been set to **0**.
15. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), the Segment number designation (SEGNO._C), and the Read/write error information (PTN.ERR).

    Confirm that the required pattern number and the segment number are set to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then the Read/write error information (PTN.ERR) has been set to "0".
16. Read the data corresponding to the segment parameters in the IN area; from the Final target setpoint (TSP_L1) to the Off time of time event 16 (T.OF16).

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

3.6.5 Writing Program Pattern

Procedure

1. Check that the receive data valid flag and the normal connection slave flag are set to **1**.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
6. Return the write request flag from 1 to **0**.
7. Check that the write acknowledgment flag has been set to **0**.
8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), and confirm that is "0".
9. Check that the during-write flag is set to **0**.
10. Change the write request flag from 0 to **1**.
11. Check that the write acknowledgment flag has been set to **1**.
12. Write the required pattern number and pattern data to the OUT area corresponding to the Program pattern number selection (PTNO._C) and Starting target setpoint (SSP_L1) to Program pattern name (P.NAME).
13. Return the write request flag from 0 to **1**.
14. Return the write request flag from 1 to **0**.
15. Check that the write acknowledgment flag has been set to **0**.
16. Return the write request flag from 1 to **0**.
17. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR).

    Confirm that the required pattern number is set to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR) has been set to "0".

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

3.7 Switching Pages

Pages can be switched by changing the value for the page change request (in word position 4 of the OUT area fixed-part).

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual
► “3.9 Profile List” in this manual

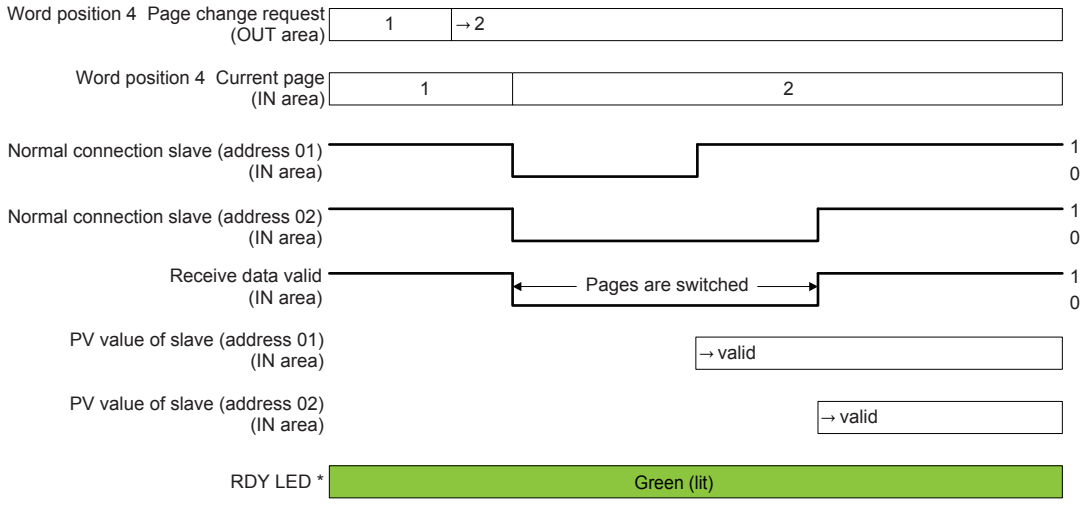
Procedure

1. Check that the during-write flag is set to **0**.
2. Change the value for the page change request (in word position 4 of the OUT area fixed-part) to a value (any of 1 to 4) that is different from the current page (in word position 4 of the IN area). The pages will be switched.

Note

- The page change request is accepted even when the during-write flag is set to 1. However, the page is actually changed when the writing is completed.
- PROFIBUS-DP/DeviceNet communication remains connected during the period when the page is being changed.
- The data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 (“→valid” in the figure below). However, it is recommended to handle them after the receive data valid flag has been set to 1.
- The value for the page change request needs to be held without change for 200 ms or longer. It is recommended that the next page change request is made after the receive data valid flag has been changed from 0 to 1.

Change of the flag when the page is switched from 1 to 2 when 2 slaves (address 01 and 02) are connected:



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

Checking page

The current page can be checked with the word position 4 of the IN area fixed-part.

3.8 Request for Rescanning

UTAdvanced that runs as a Modbus master attempts to establish communication with Modbus slaves registered in the profile, and if it cannot connect to a slave because of a wiring error or inconsistency in the communication conditions, it gives up the attempt to establish communication with that slave from the next time. It reduces the update cycle of the read data by reducing the time of communication with slaves that are disabled for communication.
A request for rescanning is made to attempt to start communication with slaves that were disabled for communication after errors with the wiring and communication conditions are fixed.
There are two types of request for rescanning: one type of request is made as needed, and the other is made at a constant frequency (automatic rescan time in SCAN). This section describes the type of rescan request that is made as needed.

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual
► “3.9 Profile List” in this manual

Procedure

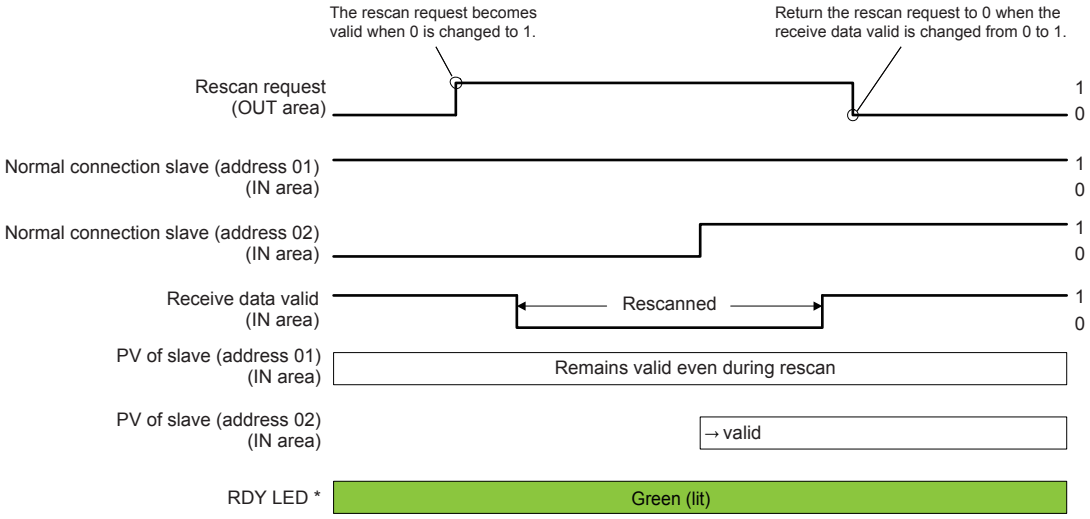
1. Change the rescan request flag (in work position 0 and bit position 0 in the OUT area) from 0 to **1**. Rescanning starts.
2. Return the rescan request flag from 1 to **0**.

Note

- A request for rescanning is accepted even when the during-write flag is set to 0. However, the rescan request process is actually performed after the writing is completed.
- The data and write request in the IN area become valid when the normal connection flag for each salve is set to 1 (“→valid” in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1. This is why if there are slaves to which connection cannot be established, the updating of the data of the salves to which connection can be established will be delayed by a time equaling the number of unconnected slaves multiplied by about 2 seconds. If there are many slaves that cannot be connected, it is recommended for the same reason to use the automatic rescan function.
- 0 of the rescan request flag needs to be held for 200 ms or longer before it is set to 1. Furthermore, after it is set to 1, 1 needs to be held for 200 ms or longer before it is set to 0. It is recommended to return the rescan request flag to 0 after the receive data valid flag is changed from 0 to 1.
- The rescan operation is performed on slaves that are not connected. If connection is normally established with all slaves registered in the profile, the receive data valid flag remains set to 1 even when a rescan request is made.

► “3.10 Changing Automatic Rescan Time (SCAN in PROF/DNET Menu)” in this manual

The operation of each flag when slave (address 01) is connected and slave (address 02) is not connected, and the rescan request flag is changed from 0 to 1 in order to establish a connection with the slave (address 02):



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

Intentionally blank

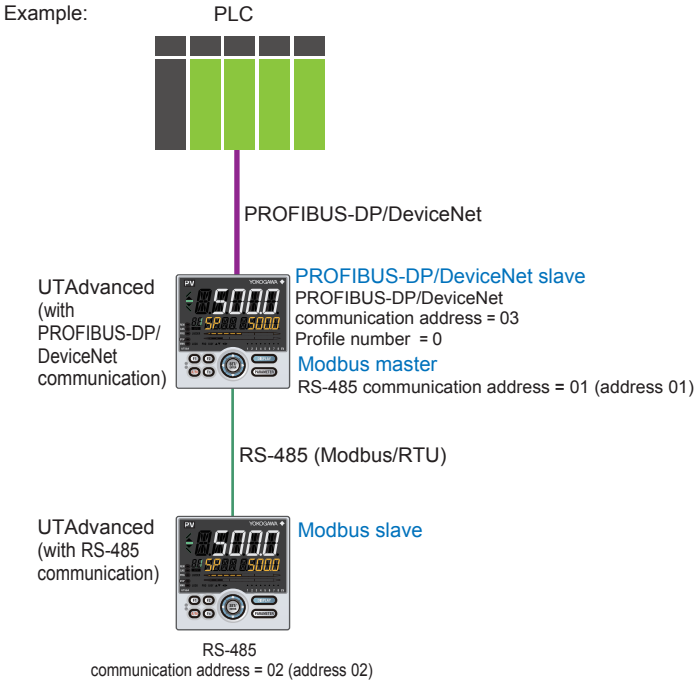
3.9 Profile List

For how to read the profile, see “3.4 Profile.”

3.9.1 Profile List for UT55A/UT35A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers])

UT55A
UT35A



Page 1

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
	8	(Reserved)		8	(Reserved)
	9	(Reserved)		9	(Reserved)
	10	(Reserved)		10	(Reserved)
	11	(Reserved)		11	(Reserved)
	12	(Reserved)		12	(Reserved)
	13	(Reserved)		13	(Reserved)
	14	(Reserved)		14	(Reserved)
	15	(Reserved)		15	(Reserved)
1	0	Normal connection slave (address 01)	1	0	Batch write request (address 01)
	1	Normal connection slave (address 02)		1	Batch write request (address 02)
	2	Normal connection slave (address 03)		2	Batch write request (address 03)
	3	Normal connection slave (address 04)		3	Batch write request (address 04)
	4	Normal connection slave (address 05)		4	Batch write request (address 05)
	5	Normal connection slave (address 06)		5	Batch write request (address 06)
	6	Normal connection slave (address 07)		6	Batch write request (address 07)
	7	Normal connection slave (address 08)		7	Batch write request (address 08)
	8	Normal connection slave (address 09)		8	Batch write request (address 09)
	9	Normal connection slave (address 10)		9	Batch write request (address 10)
	10	Normal connection slave (address 11)		10	Batch write request (address 11)
	11	Normal connection slave (address 12)		11	Batch write request (address 12)
	12	Normal connection slave (address 13)		12	Batch write request (address 13)
	13	Normal connection slave (address 14)		13	Batch write request (address 14)
	14	Normal connection slave (address 15)		14	Batch write request (address 15)
	15	Normal connection slave (address 16)		15	Batch write request (address 16)

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 17)
	1	Normal connection slave (address 18)		1	Batch write request (address 18)
	2	Normal connection slave (address 19)		2	Batch write request (address 19)
	3	Normal connection slave (address 20)		3	Batch write request (address 20)
	4	Normal connection slave (address 21)		4	Batch write request (address 21)
	5	Normal connection slave (address 22)		5	Batch write request (address 22)
	6	Normal connection slave (address 23)		6	Batch write request (address 23)
	7	Normal connection slave (address 24)		7	Batch write request (address 24)
	8	Normal connection slave (address 25)		8	Batch write request (address 25)
	9	Normal connection slave (address 26)		9	Batch write request (address 26)
	10	Normal connection slave (address 27)		10	Batch write request (address 27)
	11	Normal connection slave (address 28)		11	Batch write request (address 28)
	12	Normal connection slave (address 29)		12	Batch write request (address 29)
	13	Normal connection slave (address 30)		13	Batch write request (address 30)
	14	Normal connection slave (address 31)		14	Batch write request (address 31)
	15	Normal connection slave (address 32)		15	Batch write request (address 32)
3		Current profile number	3		(Unused)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		01: CSP_L1	7		01: SP_L1_1
8		02: CSP_L1	8		02: SP_L1_1
9		01: OUT_L1	9		01: MOUT_L1
10		02: OUT_L1	10		02: MOUT_L1
11	0	01: A.M	11	0	01: A.M
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1 } UT35A: unused		12	(Unused)
	13	01: ALM6_L1		13	(Unused)
	14	01: ALM7_L1		14	(Unused)
	15	01: ALM8_L1		15	(Unused)
12	0	02: A.M	12	0	02: A.M
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1 } UT35A: unused		12	(Unused)
	13	02: ALM6_L1		13	(Unused)
	14	02: ALM7_L1		14	(Unused)
	15	02: ALM8_L1		15	(Unused)

Page 2

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 2						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)		• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: P_L1_1		5		01: P_L1_1
6		02: P_L1_1		6		02: P_L1_1
7		01: I_L1_1		7		01: I_L1_1
8		02: I_L1_1		8		02: I_L1_1
9		01: D_L1_1		9		01: D_L1_1
10		02: D_L1_1		10		02: D_L1_1
11		01: SPNO.		11		01: SPNO.
12		02: SPNO.		12		02: SPNO.

Page 3

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 3					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: Pc_L1_1	5		01: Pc_L1_1
6		02: Pc_L1_1	6		02: Pc_L1_1
7		01: Ic_L1_1	7		01: Ic_L1_1
8		02: Ic_L1_1	8		02: Ic_L1_1
9		01: Dc_L1_1	9		01: Dc_L1_1
10		02: Dc_L1_1	10		02: Dc_L1_1
11		01: SPNO.	11		01: SPNO.
12		02: SPNO.	12		02: SPNO.

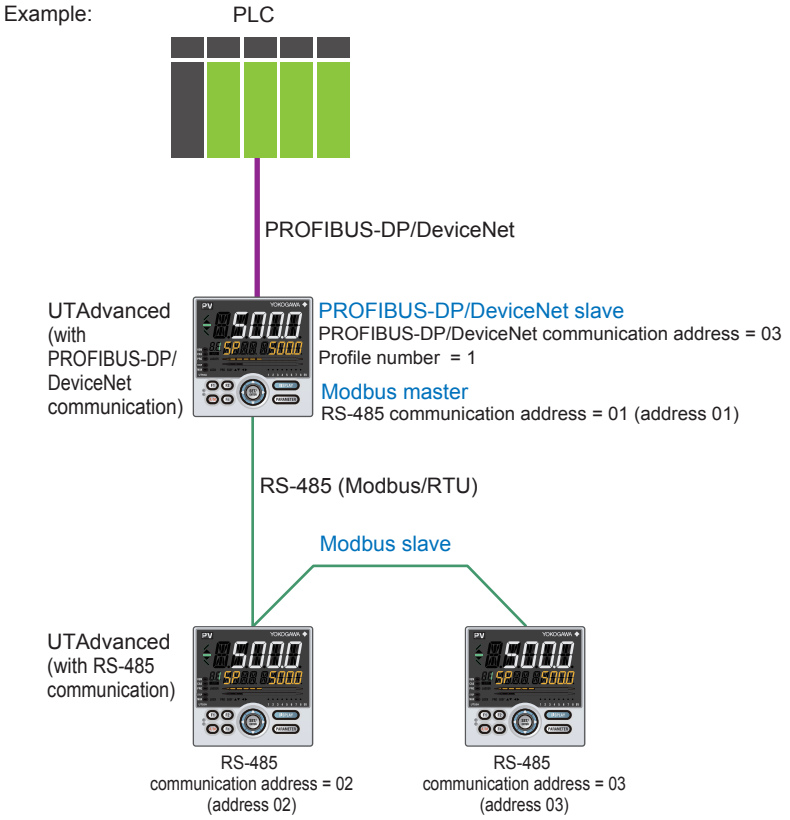
Page 4

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 4						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)		• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: A1_L1_1		5		01: A1_L1_1
6		02: A1_L1_1		6		02: A1_L1_1
7		01: A2_L1_1		7		01: A2_L1_1
8		02: A2_L1_1		8		02: A2_L1_1
9		01: A3_L1_1		9		01: A3_L1_1
10		02: A3_L1_1		10		02: A3_L1_1
11		01: A4_L1_1		11		01: A4_L1_1
12		02: A4_L1_1		12		02: A4_L1_1

Intentionally blank

Profile number 1 (Simple PID control with 3 connected controllers)

UT55A
UT35A



Page 1

Profile number 1 (Simple PID control with 3 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		03: PV_L1	7		(Unused)
8		01: CSP_L1	8		01: SP_L1_1
9		02: CSP_L1	9		02: SP_L1_1
10		03: CSP_L1	10		03: SP_L1_1
11		01: OUT_L1	11		01: MOUT_L1
12		02: OUT_L1	12		02: MOUT_L1

Profile number 1 (Simple PID control with 3 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
13		03: OUT_L1	13		03: MOUT_L1
14		01: H.OUT_L1	14		01: MOUT_L1
15		02: H.OUT_L1	15		02: MOUT_L1
16		03: H.OUT_L1	16		03: MOUT_L1
17		01: C.OUT_L1	17		01: MOUTc_L1
18		02: C.OUT_L1	18		02: MOUTc_L1
19		03: C.OUT_L1	19		03: MOUTc_L1
20	0	01: A.M	20	0	01: A.M
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1 } UT35A: unused		12	(Unused)
	13	01: ALM6_L1 } UT35A: unused		13	(Unused)
	14	01: ALM7_L1 } UT35A: unused		14	(Unused)
	15	01: ALM8_L1 } UT35A: unused		15	(Unused)
21	0	02: A.M	21	0	02: A.M
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1 } UT35A: unused		12	(Unused)
	13	02: ALM6_L1 } UT35A: unused		13	(Unused)
	14	02: ALM7_L1 } UT35A: unused		14	(Unused)
	15	02: ALM8_L1 } UT35A: unused		15	(Unused)
22	0	03: A.M	22	0	03: A.M
	1	03: R.L_L1		1	03: R.L_L1
	2	03: S.R		2	03: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L1		8	(Unused)
	9	03: ALM2_L1		9	(Unused)
	10	03: ALM3_L1		10	(Unused)
	11	03: ALM4_L1		11	(Unused)
	12	03: ALM5_L1 } UT35A: unused		12	(Unused)
	13	03: ALM6_L1 } UT35A: unused		13	(Unused)
	14	03: ALM7_L1 } UT35A: unused		14	(Unused)
	15	03: ALM8_L1 } UT35A: unused		15	(Unused)

Page 2

Profile number 1 (Simple PID control with 3 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		01: I_L1_1	8		01: I_L1_1
9		02: I_L1_1	9		02: I_L1_1
10		03: I_L1_1	10		03: I_L1_1
11		01: D_L1_1	11		01: D_L1_1
12		02: D_L1_1	12		02: D_L1_1
13		03: D_L1_1	13		03: D_L1_1
14		01: SPNO.	14		01: SPNO.
15		02: SPNO.	15		02: SPNO.
16		03: SPNO.	16		03: SPNO.
17		(Unused)	17		(Unused)
18		(Unused)	18		(Unused)
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)

Page 3

Profile number 1 (Simple PID control with 3 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: Pc_L1_1	5		01: Pc_L1_1
6		02: Pc_L1_1	6		02: Pc_L1_1
7		03: Pc_L1_1	7		03: Pc_L1_1
8		01: Ic_L1_1	8		01: Ic_L1_1
9		02: Ic_L1_1	9		02: Ic_L1_1
10		03: Ic_L1_1	10		03: Ic_L1_1
11		01: Dc_L1_1	11		01: Dc_L1_1
12		02: Dc_L1_1	12		02: Dc_L1_1
13		03: Dc_L1_1	13		03: Dc_L1_1
14		01: SPNO.	14		01: SPNO.
15		02: SPNO.	15		02: SPNO.
16		03: SPNO.	16		03: SPNO.
17		(Unused)	17		(Unused)
18		(Unused)	18		(Unused)
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)

Page 4

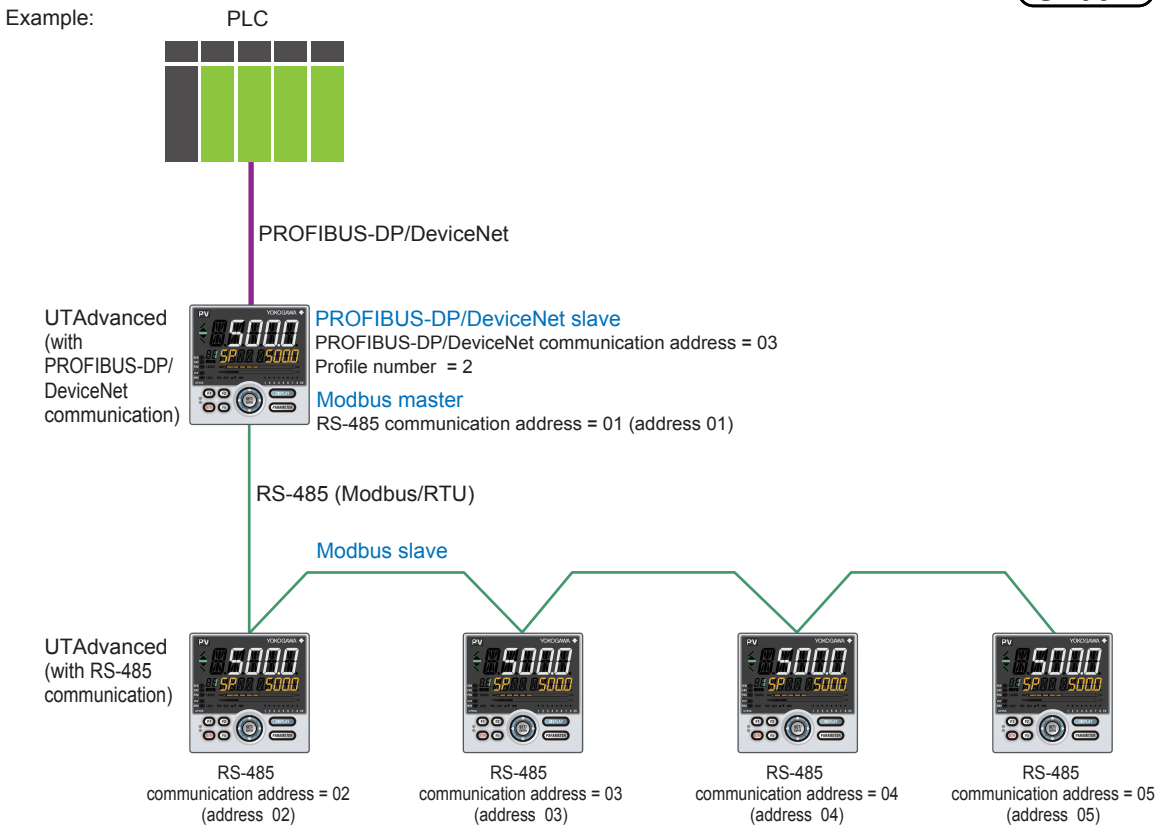
Profile number 1 (Simple PID control with 3 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		01: A2_L1_1	8		01: A2_L1_1
9		02: A2_L1_1	9		02: A2_L1_1
10		03: A2_L1_1	10		03: A2_L1_1
11		01: A3_L1_1	11		01: A3_L1_1
12		02: A3_L1_1	12		02: A3_L1_1
13		03: A3_L1_1	13		03: A3_L1_1
14		01: A4_L1_1	14		01: A4_L1_1
15		02: A4_L1_1	15		02: A4_L1_1
16		03: A4_L1_1	16		03: A4_L1_1
17		01: A5_L1_1 } UT35A: unused	17		01: A5_L1_1 } UT35A: unused
18		02: A5_L1_1	18		02: A5_L1_1
19		03: A5_L1_1	19		03: A5_L1_1
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)

Intentionally blank

Profile number 2 (Simple PID control with 5 connected controllers)

UT55A
UT35A



Page 1

Profile number 2 (Simple PID control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		03: PV_L1	7		(Unused)
8		04: PV_L1	8		(Unused)
9		05: PV_L1	9		(Unused)
10		01: CSP_L1	10		01: SP_L1_1
11		02: CSP_L1	11		02: SP_L1_1

Profile number 2 (Simple PID control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
12		03: CSP_L1	12		03: SP_L1_1
13		04: CSP_L1	13		04: SP_L1_1
14		05: CSP_L1	14		05: SP_L1_1
15		01: OUT_L1	15		01: MOUT_L1
16		02: OUT_L1	16		02: MOUT_L1
17		03: OUT_L1	17		03: MOUT_L1
18		04: OUT_L1	18		04: MOUT_L1
19		05: OUT_L1	19		05: MOUT_L1
20		01: H.OUT_L1	20		01: MOUT_L1
21		02: H.OUT_L1	21		02: MOUT_L1
22		03: H.OUT_L1	22		03: MOUT_L1
23		04: H.OUT_L1	23		04: MOUT_L1
24		05: H.OUT_L1	24		05: MOUT_L1
25		01: C.OUT_L1	25		01: MOUTc_L1
26		02: C.OUT_L1	26		02: MOUTc_L1
27		03: C.OUT_L1	27		03: MOUTc_L1
28		04: C.OUT_L1	28		04: MOUTc_L1
29		05: C.OUT_L1	29		05: MOUTc_L1
30	0	01: A.M	30	0	01: A.M
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1 } UT35A: unused		12	(Unused)
	13	01: ALM6_L1 } UT35A: unused		13	(Unused)
	14	01: ALM7_L1 } UT35A: unused		14	(Unused)
	15	01: ALM8_L1 } UT35A: unused		15	(Unused)
31	0	02: A.M	31	0	02: A.M
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1 } UT35A: unused		12	(Unused)
	13	02: ALM6_L1 } UT35A: unused		13	(Unused)
	14	02: ALM7_L1 } UT35A: unused		14	(Unused)
	15	02: ALM8_L1 } UT35A: unused		15	(Unused)

Profile number 2 (Simple PID control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
32	0	03: A.M	32	0	03: A.M
	1	03: R.L_L1		1	03: R.L_L1
	2	03: S.R		2	03: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L1		8	(Unused)
	9	03: ALM2_L1		9	(Unused)
	10	03: ALM3_L1		10	(Unused)
	11	03: ALM4_L1		11	(Unused)
	12	03: ALM5_L1 } UT35A: unused		12	(Unused)
	13	03: ALM6_L1 } UT35A: unused		13	(Unused)
	14	03: ALM7_L1 } UT35A: unused		14	(Unused)
	15	03: ALM8_L1 } UT35A: unused		15	(Unused)
33	0	04: A.M	33	0	04: A.M
	1	04: R.L_L1		1	04: R.L_L1
	2	04: S.R		2	04: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	04: ALM1_L1		8	(Unused)
	9	04: ALM2_L1		9	(Unused)
	10	04: ALM3_L1		10	(Unused)
	11	04: ALM4_L1		11	(Unused)
	12	04: ALM5_L1 } UT35A: unused		12	(Unused)
	13	04: ALM6_L1 } UT35A: unused		13	(Unused)
	14	04: ALM7_L1 } UT35A: unused		14	(Unused)
	15	04: ALM8_L1 } UT35A: unused		15	(Unused)
34	0	05: A.M	34	0	05: A.M
	1	05: R.L_L1		1	05: R.L_L1
	2	05: S.R		2	05: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	05: ALM1_L1		8	(Unused)
	9	05: ALM2_L1		9	(Unused)
	10	05: ALM3_L1		10	(Unused)
	11	05: ALM4_L1		11	(Unused)
	12	05: ALM5_L1 } UT35A: unused		12	(Unused)
	13	05: ALM6_L1 } UT35A: unused		13	(Unused)
	14	05: ALM7_L1 } UT35A: unused		14	(Unused)
	15	05: ALM8_L1 } UT35A: unused		15	(Unused)

Page 2

Profile number 2 (Simple PID control with 5 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		04: P_L1_1	8		04: P_L1_1
9		05: P_L1_1	9		05: P_L1_1
10		01: I_L1_1	10		01: I_L1_1
11		02: I_L1_1	11		02: I_L1_1
12		03: I_L1_1	12		03: I_L1_1
13		04: I_L1_1	13		04: I_L1_1
14		05: I_L1_1	14		05: I_L1_1
15		01: D_L1_1	15		01: D_L1_1
16		02: D_L1_1	16		02: D_L1_1
17		03: D_L1_1	17		03: D_L1_1
18		04: D_L1_1	18		04: D_L1_1
19		05: D_L1_1	19		05: D_L1_1
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25		(Unused)	25		(Unused)
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

Page 3

Profile number 2 (Simple PID control with 5 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: Pc_L1_1	5		01: Pc_L1_1
6		02: Pc_L1_1	6		02: Pc_L1_1
7		03: Pc_L1_1	7		03: Pc_L1_1
8		04: Pc_L1_1	8		04: Pc_L1_1
9		05: Pc_L1_1	9		05: Pc_L1_1
10		01: Ic_L1_1	10		01: Ic_L1_1
11		02: Ic_L1_1	11		02: Ic_L1_1
12		03: Ic_L1_1	12		03: Ic_L1_1
13		04: Ic_L1_1	13		04: Ic_L1_1
14		05: Ic_L1_1	14		05: Ic_L1_1
15		01: Dc_L1_1	15		01: Dc_L1_1
16		02: Dc_L1_1	16		02: Dc_L1_1
17		03: Dc_L1_1	17		03: Dc_L1_1
18		04: Dc_L1_1	18		04: Dc_L1_1
19		05: Dc_L1_1	19		05: Dc_L1_1
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25		(Unused)	25		(Unused)
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

Page 4

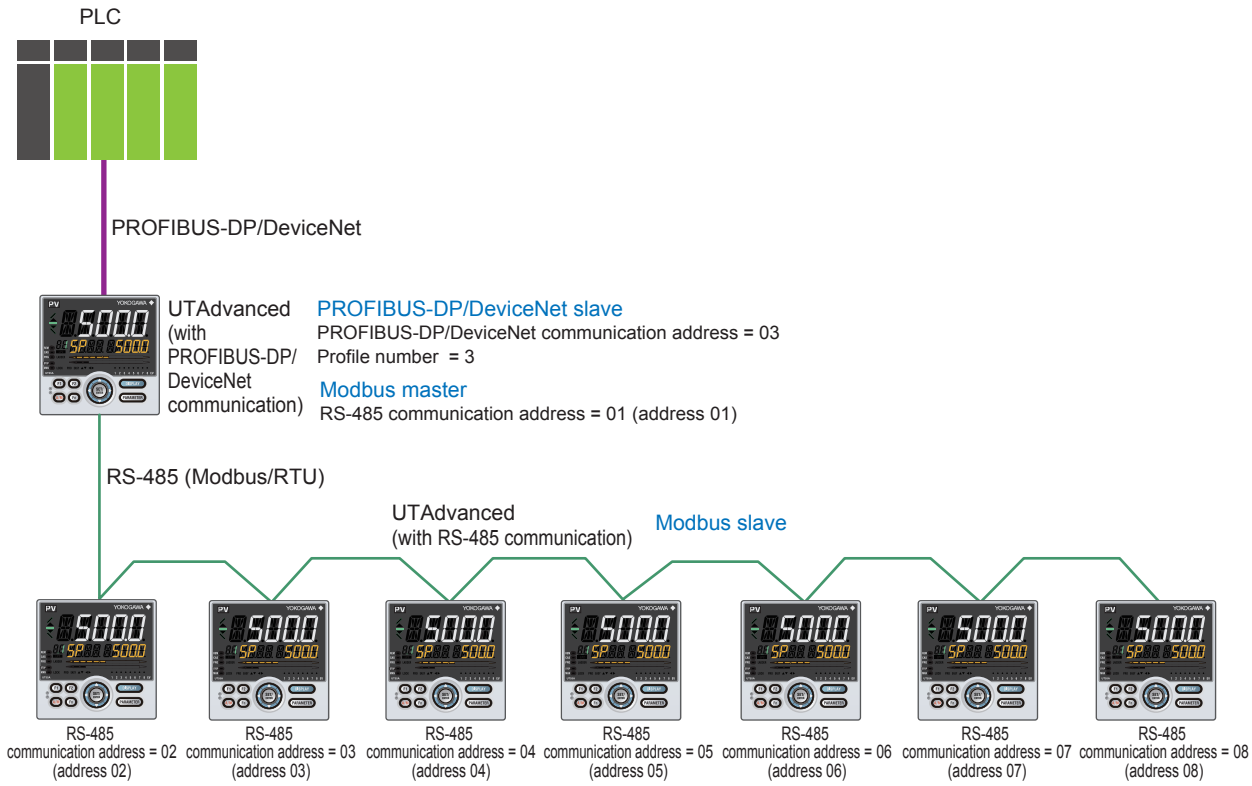
Profile number 2 (Simple PID control with 5 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		04: A1_L1_1	8		04: A1_L1_1
9		05: A1_L1_1	9		05: A1_L1_1
10		01: A2_L1_1	10		01: A2_L1_1
11		02: A2_L1_1	11		02: A2_L1_1
12		03: A2_L1_1	12		03: A2_L1_1
13		04: A2_L1_1	13		04: A2_L1_1
14		05: A2_L1_1	14		05: A2_L1_1
15		01: A3_L1_1	15		01: A3_L1_1
16		02: A3_L1_1	16		02: A3_L1_1
17		03: A3_L1_1	17		03: A3_L1_1
18		04: A3_L1_1	18		04: A3_L1_1
19		05: A3_L1_1	19		05: A3_L1_1
20		01: A4_L1_1	20		01: A4_L1_1
21		02: A4_L1_1	21		02: A4_L1_1
22		03: A4_L1_1	22		03: A4_L1_1
23		04: A4_L1_1	23		04: A4_L1_1
24		05: A4_L1_1	24		05: A4_L1_1
25		01: A5_L1_1	25		01: A5_L1_1
26		02: A5_L1_1	26		02: A5_L1_1
27		03: A5_L1_1 } UT35A: unused	27		03: A5_L1_1 } UT35A: unused
28		04: A5_L1_1	28		04: A5_L1_1
29		05: A5_L1_1	29		05: A5_L1_1
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

Profile number 3 (Simple PID control with 8 connected controllers)

UT55A
UT35A

Example:



Page 1

Profile number 3 (Simple PID control with 8 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		03: PV_L1	7		(Unused)
8		04: PV_L1	8		(Unused)
9		05: PV_L1	9		(Unused)
10		06: PV_L1	10		(Unused)
11		07: PV_L1	11		(Unused)

Profile number 3 (Simple PID control with 8 connected controllers) on page 1					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
12		08: PV_L1	12		(Unused)
13		01: CSP_L1	13		01: SP_L1_1
14		02: CSP_L1	14		02: SP_L1_1
15		03: CSP_L1	15		03: SP_L1_1
16		04: CSP_L1	16		04: SP_L1_1
17		05: CSP_L1	17		05: SP_L1_1
18		06: CSP_L1	18		06: SP_L1_1
19		07: CSP_L1	19		07: SP_L1_1
20		08: CSP_L1	20		08: SP_L1_1
21		01: OUT_L1	21		01: MOUT_L1
22		02: OUT_L1	22		02: MOUT_L1
23		03: OUT_L1	23		03: MOUT_L1
24		04: OUT_L1	24		04: MOUT_L1
25		05: OUT_L1	25		05: MOUT_L1
26		06: OUT_L1	26		06: MOUT_L1
27		07: OUT_L1	27		07: MOUT_L1
28		08: OUT_L1	28		08: MOUT_L1
29		01: H.OUT_L1	29		01: MOUT_L1
30		02: H.OUT_L1	30		02: MOUT_L1
31		03: H.OUT_L1	31		03: MOUT_L1
32		04: H.OUT_L1	32		04: MOUT_L1
33		05: H.OUT_L1	33		05: MOUT_L1
34		06: H.OUT_L1	34		06: MOUT_L1
35		07: H.OUT_L1	35		07: MOUT_L1
36		08: H.OUT_L1	36		08: MOUT_L1
37		01: C.OUT_L1	37		01: MOUTc_L1
38		02: C.OUT_L1	38		02: MOUTc_L1
39		03: C.OUT_L1	39		03: MOUTc_L1
40		04: C.OUT_L1	40		04: MOUTc_L1
41		05: C.OUT_L1	41		05: MOUTc_L1
42		06: C.OUT_L1	42		06: MOUTc_L1
43		07: C.OUT_L1	43		07: MOUTc_L1
44		08: C.OUT_L1	44		08: MOUTc_L1

Profile number 3 (Simple PID control with 8 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
45	0	01: A.M	45	0	01: A.M
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1		12	(Unused)
	13	01: ALM6_L1		13	(Unused)
	14	01: ALM7_L1		14	(Unused)
	15	01: ALM8_L1		15	(Unused)
46	0	02: A.M	46	0	02: A.M
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1		12	(Unused)
	13	02: ALM6_L1		13	(Unused)
	14	02: ALM7_L1		14	(Unused)
	15	02: ALM8_L1		15	(Unused)
47	0	03: A.M	47	0	03: A.M
	1	03: R.L_L1		1	03: R.L_L1
	2	03: S.R		2	03: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L1		8	(Unused)
	9	03: ALM2_L1		9	(Unused)
	10	03: ALM3_L1		10	(Unused)
	11	03: ALM4_L1		11	(Unused)
	12	03: ALM5_L1		12	(Unused)
	13	03: ALM6_L1		13	(Unused)
	14	03: ALM7_L1		14	(Unused)
	15	03: ALM8_L1		15	(Unused)
48	0	04: A.M	48	0	04: A.M
	1	04: R.L_L1		1	04: R.L_L1
	2	04: S.R		2	04: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	04: ALM1_L1		8	(Unused)
	9	04: ALM2_L1		9	(Unused)
	10	04: ALM3_L1		10	(Unused)
	11	04: ALM4_L1		11	(Unused)
	12	04: ALM5_L1		12	(Unused)
	13	04: ALM6_L1		13	(Unused)
	14	04: ALM7_L1		14	(Unused)
	15	04: ALM8_L1		15	(Unused)

UT35A: unused

Profile number 3 (Simple PID control with 8 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
49	0	05: A.M	49	0	05: A.M
	1	05: R.L_L1		1	05: R.L_L1
	2	05: S.R		2	05: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	05: ALM1_L1		8	(Unused)
	9	05: ALM2_L1		9	(Unused)
	10	05: ALM3_L1		10	(Unused)
	11	05: ALM4_L1		11	(Unused)
	12	05: ALM5_L1		12	(Unused)
	13	05: ALM6_L1		13	(Unused)
	14	05: ALM7_L1		14	(Unused)
	15	05: ALM8_L1		15	(Unused)
50	0	06: A.M	50	0	06: A.M
	1	06: R.L_L1		1	06: R.L_L1
	2	06: S.R		2	06: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	06: ALM1_L1		8	(Unused)
	9	06: ALM2_L1		9	(Unused)
	10	06: ALM3_L1		10	(Unused)
	11	06: ALM4_L1		11	(Unused)
	12	06: ALM5_L1		12	(Unused)
	13	06: ALM6_L1		13	(Unused)
	14	06: ALM7_L1		14	(Unused)
	15	06: ALM8_L1		15	(Unused)
51	0	07: A.M	51	0	07: A.M
	1	07: R.L_L1		1	07: R.L_L1
	2	07: S.R		2	07: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	07: ALM1_L1		8	(Unused)
	9	07: ALM2_L1		9	(Unused)
	10	07: ALM3_L1		10	(Unused)
	11	07: ALM4_L1		11	(Unused)
	12	07: ALM5_L1		12	(Unused)
	13	07: ALM6_L1		13	(Unused)
	14	07: ALM7_L1		14	(Unused)
	15	07: ALM8_L1		15	(Unused)
52	0	08: A.M	52	0	08: A.M
	1	08: R.L_L1		1	08: R.L_L1
	2	08: S.R		2	08: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	08: ALM1_L1		8	(Unused)
	9	08: ALM2_L1		9	(Unused)
	10	08: ALM3_L1		10	(Unused)
	11	08: ALM4_L1		11	(Unused)
	12	08: ALM5_L1		12	(Unused)
	13	08: ALM6_L1		13	(Unused)
	14	08: ALM7_L1		14	(Unused)
	15	08: ALM8_L1		15	(Unused)

UT35A: unused

Page 2

Profile number 3 (Simple PID control with 8 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		04: P_L1_1	8		04: P_L1_1
9		05: P_L1_1	9		05: P_L1_1
10		06: P_L1_1	10		06: P_L1_1
11		07: P_L1_1	11		07: P_L1_1
12		08: P_L1_1	12		08: P_L1_1
13		01: I_L1_1	13		01: I_L1_1
14		02: I_L1_1	14		02: I_L1_1
15		03: I_L1_1	15		03: I_L1_1
16		04: I_L1_1	16		04: I_L1_1
17		05: I_L1_1	17		05: I_L1_1
18		06: I_L1_1	18		06: I_L1_1
19		07: I_L1_1	19		07: I_L1_1
20		08: I_L1_1	20		08: I_L1_1
21		01: D_L1_1	21		01: D_L1_1
22		02: D_L1_1	22		02: D_L1_1
23		03: D_L1_1	23		03: D_L1_1
24		04: D_L1_1	24		04: D_L1_1
25		05: D_L1_1	25		05: D_L1_1
26		06: D_L1_1	26		06: D_L1_1
27		07: D_L1_1	27		07: D_L1_1
28		08: D_L1_1	28		08: D_L1_1
29		01: SPNO.	29		01: SPNO.
30		02: SPNO.	30		02: SPNO.
31		03: SPNO.	31		03: SPNO.

Profile number 3 (Simple PID control with 8 connected controllers) on page 2						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
32		04: SPNO.		32		04: SPNO.
33		05: SPNO.		33		05: SPNO.
34		06: SPNO.		34		06: SPNO.
35		07: SPNO.		35		07: SPNO.
36		08: SPNO.		36		08: SPNO.
37		(Unused)		45		(Unused)
38		(Unused)		46		(Unused)
39		(Unused)		47		(Unused)
40		(Unused)		48		(Unused)
41		(Unused)		49		(Unused)
42		(Unused)		50		(Unused)
43		(Unused)		51		(Unused)
44		(Unused)		52		(Unused)
45		(Unused)		53		(Unused)
46		(Unused)		46		(Unused)
47		(Unused)		47		(Unused)
48		(Unused)		48		(Unused)
49		(Unused)		49		(Unused)
50		(Unused)		50		(Unused)
51		(Unused)		51		(Unused)
52		(Unused)		52		(Unused)

Page 3

Profile number 3 (Simple PID control with 8 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: Pc_L1_1	5		01: Pc_L1_1
6		02: Pc_L1_1	6		02: Pc_L1_1
7		03: Pc_L1_1	7		03: Pc_L1_1
8		04: Pc_L1_1	8		04: Pc_L1_1
9		05: Pc_L1_1	9		05: Pc_L1_1
10		06: Pc_L1_1	10		06: Pc_L1_1
11		07: Pc_L1_1	11		07: Pc_L1_1
12		08: Pc_L1_1	12		08: Pc_L1_1
13		01: Ic_L1_1	13		01: Ic_L1_1
14		02: Ic_L1_1	14		02: Ic_L1_1
15		03: Ic_L1_1	15		03: Ic_L1_1
16		04: Ic_L1_1	16		04: Ic_L1_1
17		05: Ic_L1_1	17		05: Ic_L1_1
18		06: Ic_L1_1	18		06: Ic_L1_1
19		07: Ic_L1_1	19		07: Ic_L1_1
20		08: Ic_L1_1	20		08: Ic_L1_1
21		01: Dc_L1_1	21		01: Dc_L1_1
22		02: Dc_L1_1	22		02: Dc_L1_1
23		03: Dc_L1_1	23		03: Dc_L1_1
24		04: Dc_L1_1	24		04: Dc_L1_1
25		05: Dc_L1_1	25		05: Dc_L1_1
26		06: Dc_L1_1	26		06: Dc_L1_1
27		07: Dc_L1_1	27		07: Dc_L1_1
28		08: Dc_L1_1	28		08: Dc_L1_1
29		01: SPNO.	29		01: SPNO.
30		02: SPNO.	30		02: SPNO.
31		03: SPNO.	31		03: SPNO.

Profile number 3 (Simple PID control with 8 connected controllers) on page 3						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
32		04: SPNO.		32		04: SPNO.
33		05: SPNO.		33		05: SPNO.
34		06: SPNO.		34		06: SPNO.
35		07: SPNO.		35		07: SPNO.
36		08: SPNO.		36		08: SPNO.
37		(Unused)		37		(Unused)
38		(Unused)		38		(Unused)
39		(Unused)		39		(Unused)
40		(Unused)		40		(Unused)
41		(Unused)		41		(Unused)
42		(Unused)		42		(Unused)
43		(Unused)		43		(Unused)
44		(Unused)		44		(Unused)
45		(Unused)		45		(Unused)
46		(Unused)		46		(Unused)
47		(Unused)		47		(Unused)
48		(Unused)		48		(Unused)
49		(Unused)		49		(Unused)
50		(Unused)		50		(Unused)
51		(Unused)		51		(Unused)
52		(Unused)		52		(Unused)

Page 4

Profile number 3 (Simple PID control with 8 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		04: A1_L1_1	8		04: A1_L1_1
9		05: A1_L1_1	9		05: A1_L1_1
10		06: A1_L1_1	10		06: A1_L1_1
11		07: A1_L1_1	11		07: A1_L1_1
12		08: A1_L1_1	12		08: A1_L1_1
13		01: A2_L1_1	13		01: A2_L1_1
14		02: A2_L1_1	14		02: A2_L1_1
15		03: A2_L1_1	15		03: A2_L1_1
16		04: A2_L1_1	16		04: A2_L1_1
17		05: A2_L1_1	17		05: A2_L1_1
18		06: A2_L1_1	18		06: A2_L1_1
19		07: A2_L1_1	19		07: A2_L1_1
20		08: A2_L1_1	20		08: A2_L1_1
21		01: A3_L1_1	21		01: A3_L1_1
22		02: A3_L1_1	22		02: A3_L1_1
23		03: A3_L1_1	23		03: A3_L1_1
24		04: A3_L1_1	24		04: A3_L1_1
25		05: A3_L1_1	25		05: A3_L1_1
26		06: A3_L1_1	26		06: A3_L1_1
27		07: A3_L1_1	27		07: A3_L1_1
28		08: A3_L1_1	28		08: A3_L1_1
29		01: A4_L1_1	29		01: A4_L1_1
30		02: A4_L1_1	30		02: A4_L1_1
31		03: A4_L1_1	31		03: A4_L1_1
32		04: A4_L1_1	32		04: A4_L1_1
33		05: A4_L1_1	33		05: A4_L1_1

Profile number 3 (Simple PID control with 8 connected controllers) on page 4

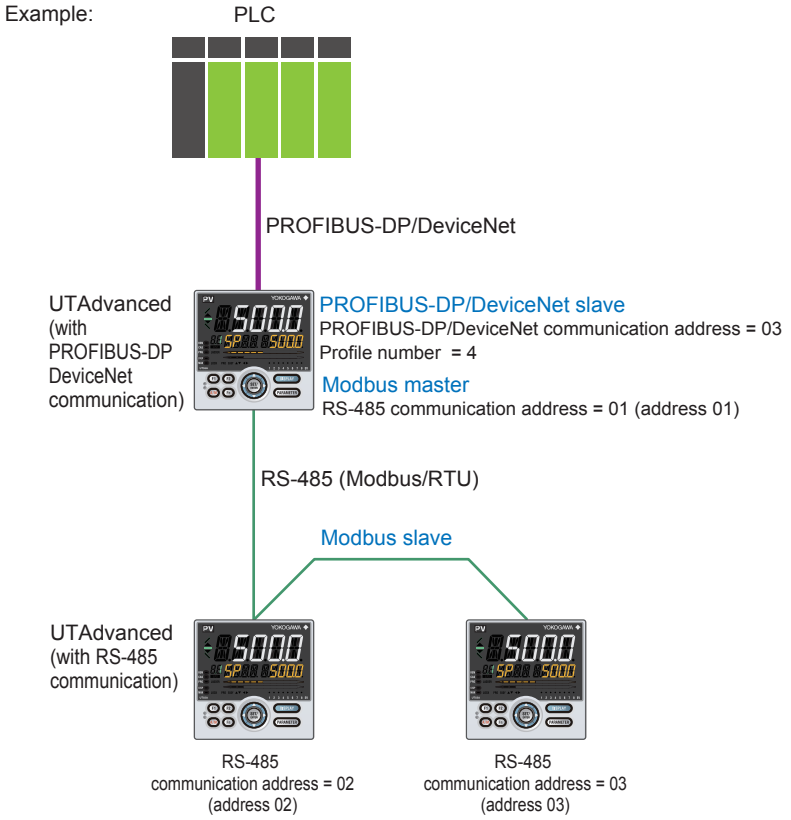
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
34		06: A4_L1_1	34		06: A4_L1_1
35		07: A4_L1_1	35		07: A4_L1_1
36		08: A4_L1_1	36		08: A4_L1_1
37		01: A5_L1_1	37		01: A5_L1_1
38		02: A5_L1_1	38		02: A5_L1_1
39		03: A5_L1_1	39		03: A5_L1_1
40		04: A5_L1_1	40		04: A5_L1_1
41		05: A5_L1_1	41		05: A5_L1_1
42		06: A5_L1_1	42		06: A5_L1_1
43		07: A5_L1_1	43		07: A5_L1_1
44		08: A5_L1_1	44		08: A5_L1_1
45		(Unused)	45		(Unused)
46		(Unused)	46		(Unused)
47		(Unused)	47		(Unused)
48		(Unused)	48		(Unused)
49		(Unused)	49		(Unused)
50		(Unused)	50		(Unused)
51		(Unused)	51		(Unused)
52		(Unused)	52		(Unused)

UT35A: unused

UT35A: unused

Profile number 4 (Cascade control with 3 connected controllers)

UT55A



Page 1

Profile number 4 (Cascade control with 3 connected controllers) on page 1

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		03: PV_L1	7		(Unused)
8		01: PV_L2	8		(Unused)
9		02: PV_L2	9		(Unused)
10		03: PV_L2	10		(Unused)
11		01: CSP_L1	11		01: SP_L1_1
12		02: CSP_L1	12		02: SP_L1_1

Profile number 4 (Cascade control with 3 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
13		03: CSP_L1	13		03: SP_L1_1
14		01: CSP_L2	14		01: SP_L2_1
15		02: CSP_L2	15		02: SP_L2_1
16		03: CSP_L2	16		03: SP_L2_1
17		01: C.A.M	17		01: C.A.M
18		02: C.A.M	18		02: C.A.M
19		03: C.A.M	19		03: C.A.M
20		01: OUT_L2	20		01: MOUT_L2
21		02: OUT_L2	21		02: MOUT_L2
22		03: OUT_L2	22		03: MOUT_L2
23		01: H.OUT_L2	23		01: MOUT_L2
24		02: H.OUT_L2	24		02: MOUT_L2
25		03: H.OUT_L2	25		03: MOUT_L2
26		01: C.OUT_L2	26		01: MOUTc_L2
27		02: C.OUT_L2	27		02: MOUTc_L2
28		03: C.OUT_L2	28		03: MOUTc_L2
29	0	(Unused)	29	0	(Unused)
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1		12	(Unused)
	13	01: ALM6_L1		13	(Unused)
	14	01: ALM7_L1		14	(Unused)
	15	01: ALM8_L1		15	(Unused)
30	0	(Unused)	30	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L2		8	(Unused)
	9	01: ALM2_L2		9	(Unused)
	10	01: ALM3_L2		10	(Unused)
	11	01: ALM4_L2		11	(Unused)
	12	01: ALM5_L2		12	(Unused)
	13	01: ALM6_L2		13	(Unused)
	14	01: ALM7_L2		14	(Unused)
	15	01: ALM8_L2		15	(Unused)

Profile number 4 (Cascade control with 3 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
31	0	(Unused)	31	0	(Unused)
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1		12	(Unused)
	13	02: ALM6_L1		13	(Unused)
	14	02: ALM7_L1		14	(Unused)
	15	02: ALM8_L1		15	(Unused)
32	0	(Unused)	32	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L2		8	(Unused)
	9	02: ALM2_L2		9	(Unused)
	10	02: ALM3_L2		10	(Unused)
	11	02: ALM4_L2		11	(Unused)
	12	02: ALM5_L2		12	(Unused)
	13	02: ALM6_L2		13	(Unused)
	14	02: ALM7_L2		14	(Unused)
	15	02: ALM8_L2		15	(Unused)
33	0	(Unused)	33	0	(Unused)
	1	03: R.L_L1		1	03: R.L_L1
	2	03: S.R		2	03: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L1		8	(Unused)
	9	03: ALM2_L1		9	(Unused)
	10	03: ALM3_L1		10	(Unused)
	11	03: ALM4_L1		11	(Unused)
	12	03: ALM5_L1		12	(Unused)
	13	03: ALM6_L1		13	(Unused)
	14	03: ALM7_L1		14	(Unused)
	15	03: ALM8_L1		15	(Unused)
34	0	(Unused)	34	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L2		8	(Unused)
	9	03: ALM2_L2		9	(Unused)
	10	03: ALM3_L2		10	(Unused)
	11	03: ALM4_L2		11	(Unused)
	12	03: ALM5_L2		12	(Unused)
	13	03: ALM6_L2		13	(Unused)
	14	03: ALM7_L2		14	(Unused)
	15	03: ALM8_L2		15	(Unused)

Page 2

Profile number 4 (Cascade control with 3 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		01: I_L1_1	8		01: I_L1_1
9		02: I_L1_1	9		02: I_L1_1
10		03: I_L1_1	10		03: I_L1_1
11		01: D_L1_1	11		01: D_L1_1
12		02: D_L1_1	12		02: D_L1_1
13		03: D_L1_1	13		03: D_L1_1
14		01: SPNO.	14		01: SPNO.
15		02: SPNO.	15		02: SPNO.
16		03: SPNO.	16		03: SPNO.
17		01: P_L2_1	17		01: P_L2_1
18		02: P_L2_1	18		02: P_L2_1
19		03: P_L2_1	19		03: P_L2_1
20		01: I_L2_1	20		01: I_L2_1
21		02: I_L2_1	21		02: I_L2_1
22		03: I_L2_1	22		03: I_L2_1
23		01: D_L2_1	23		01: D_L2_1
24		02: D_L2_1	24		02: D_L2_1
25		03: D_L2_1	25		03: D_L2_1
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

Page 3

Profile number 4 (Cascade control with 3 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		(Unused)	5		(Unused)
6		(Unused)	6		(Unused)
7		(Unused)	7		(Unused)
8		(Unused)	8		(Unused)
9		(Unused)	9		(Unused)
10		(Unused)	10		(Unused)
11		(Unused)	11		(Unused)
12		(Unused)	12		(Unused)
13		(Unused)	13		(Unused)
14		01: SPNO.	14		01: SPNO.
15		02: SPNO.	15		02: SPNO.
16		03: SPNO.	16		03: SPNO.
17		01: Pc_L2_1	17		01: Pc_L2_1
18		02: Pc_L2_1	18		02: Pc_L2_1
19		03: Pc_L2_1	19		03: Pc_L2_1
20		01: Ic_L2_1	20		01: Ic_L2_1
21		02: Ic_L2_1	21		02: Ic_L2_1
22		03: Ic_L2_1	22		03: Ic_L2_1
23		01: Dc_L2_1	23		01: Dc_L2_1
24		02: Dc_L2_1	24		02: Dc_L2_1
25		03: Dc_L2_1	25		03: Dc_L2_1
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

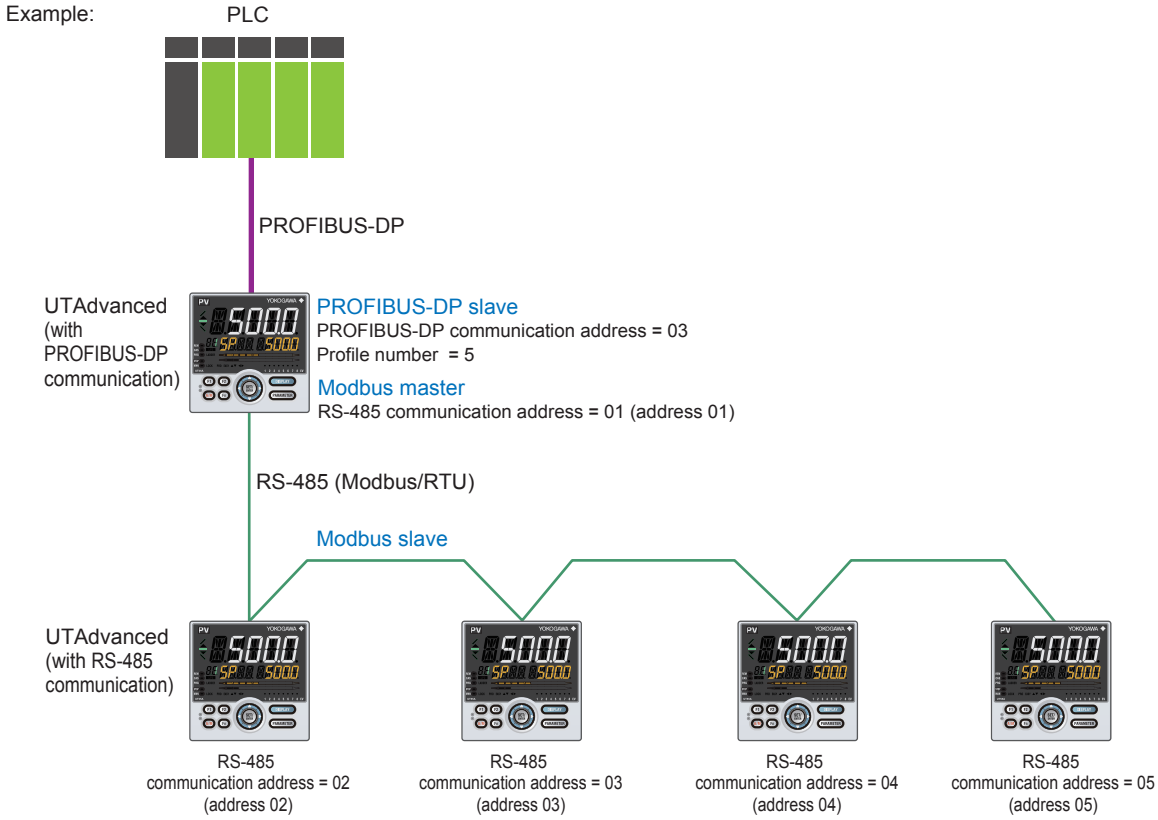
Page 4

Profile number 4 (Cascade control with 3 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		01: A2_L1_1	8		01: A2_L1_1
9		02: A2_L1_1	9		02: A2_L1_1
10		03: A2_L1_1	10		03: A2_L1_1
11		01: A3_L1_1	11		01: A3_L1_1
12		02: A3_L1_1	12		02: A3_L1_1
13		03: A3_L1_1	13		03: A3_L1_1
14		01: A4_L1_1	14		01: A4_L1_1
15		02: A4_L1_1	15		02: A4_L1_1
16		03: A4_L1_1	16		03: A4_L1_1
17		01: A5_L1_1	17		01: A5_L1_1
18		02: A5_L1_1	18		02: A5_L1_1
19		03: A5_L1_1	19		03: A5_L1_1
20		01: A1_L2_1	20		01: A1_L2_1
21		02: A1_L2_1	21		02: A1_L2_1
22		03: A1_L2_1	22		03: A1_L2_1
23		01: A2_L2_1	23		01: A2_L2_1
24		02: A2_L2_1	24		02: A2_L2_1
25		03: A2_L2_1	25		03: A2_L2_1
26		01: A3_L2_1	26		01: A3_L2_1
27		02: A3_L2_1	27		02: A3_L2_1
28		03: A3_L2_1	28		03: A3_L2_1
29		01: A4_L2_1	29		01: A4_L2_1
30		02: A4_L2_1	30		02: A4_L2_1
31		03: A4_L2_1	31		03: A4_L2_1
32		01: A5_L2_1	32		01: A5_L2_1
33		02: A5_L2_1	33		02: A5_L2_1
34		03: A5_L2_1	34		03: A5_L2_1

Profile number 5 (Cascade control with 5 connected controllers)

UT55A



Page 1

Profile number 5 (Cascade control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		03: PV_L1	7		(Unused)
8		04: PV_L1	8		(Unused)
9		05: PV_L1	9		(Unused)
10		01: PV_L2	10		(Unused)
11		02: PV_L2	11		(Unused)
12		03: PV_L2	12		(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
13		04: PV_L2	13		(Unused)
14		05: PV_L2	14		(Unused)
15		01: CSP_L1	15		01: SP_L1_1
16		02: CSP_L1	16		02: SP_L1_1
17		03: CSP_L1	17		03: SP_L1_1
18		04: CSP_L1	18		04: SP_L1_1
19		05: CSP_L1	19		05: SP_L1_1
20		01: CSP_L2	20		01: SP_L2_1
21		02: CSP_L2	21		02: SP_L2_1
22		03: CSP_L2	22		03: SP_L2_1
23		04: CSP_L2	23		04: SP_L2_1
24		05: CSP_L2	24		05: SP_L2_1
25		01: C.A.M	25		01: C.A.M
26		02: C.A.M	26		02: C.A.M
27		03: C.A.M	27		03: C.A.M
28		04: C.A.M	28		04: C.A.M
29		05: C.A.M	29		05: C.A.M
30		01: OUT_L2	30		01: MOUT_L2
31		02: OUT_L2	31		02: MOUT_L2
32		03: OUT_L2	32		03: MOUT_L2
33		04: OUT_L2	33		04: MOUT_L2
34		05: OUT_L2	34		05: MOUT_L2
35		01: H.OUT_L2	35		01: MOUT_L2
36		02: H.OUT_L2	36		02: MOUT_L2
37		03: H.OUT_L2	37		03: MOUT_L2
38		04: H.OUT_L2	38		04: MOUT_L2
39		05: H.OUT_L2	39		05: MOUT_L2
40		01: C.OUT_L2	40		01: MOUTc_L2
41		02: C.OUT_L2	41		02: MOUTc_L2
42		03: C.OUT_L2	42		03: MOUTc_L2
43		04: C.OUT_L2	43		04: MOUTc_L2
44		05: C.OUT_L2	44		05: MOUTc_L2

Profile number 5 (Cascade control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
45	0	(Unused)	45	0	(Unused)
	1	01: R.L_L1		1	01: R.L_L1
	2	01: S.R		2	01: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	01: ALM5_L1		12	(Unused)
	13	01: ALM6_L1		13	(Unused)
	14	01: ALM7_L1		14	(Unused)
	15	01: ALM8_L1		15	(Unused)
46	0	(Unused)	46	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L2		8	(Unused)
	9	01: ALM2_L2		9	(Unused)
	10	01: ALM3_L2		10	(Unused)
	11	01: ALM4_L2		11	(Unused)
	12	01: ALM5_L2		12	(Unused)
	13	01: ALM6_L2		13	(Unused)
	14	01: ALM7_L2		14	(Unused)
	15	01: ALM8_L2		15	(Unused)
47	0	(Unused)	47	0	(Unused)
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R		2	02: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11	02: ALM4_L1		11	(Unused)
	12	02: ALM5_L1		12	(Unused)
	13	02: ALM6_L1		13	(Unused)
	14	02: ALM7_L1		14	(Unused)
	15	02: ALM8_L1		15	(Unused)
48	0	(Unused)	48	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L2		8	(Unused)
	9	02: ALM2_L2		9	(Unused)
	10	02: ALM3_L2		10	(Unused)
	11	02: ALM4_L2		11	(Unused)
	12	02: ALM5_L2		12	(Unused)
	13	02: ALM6_L2		13	(Unused)
	14	02: ALM7_L2		14	(Unused)
	15	02: ALM8_L2		15	(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
49	0	(Unused)	49	0	(Unused)
	1	03: R.L_L1		1	03: R.L_L1
	2	03: S.R		2	03: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L1		8	(Unused)
	9	03: ALM2_L1		9	(Unused)
	10	03: ALM3_L1		10	(Unused)
	11	03: ALM4_L1		11	(Unused)
	12	03: ALM5_L1		12	(Unused)
	13	03: ALM6_L1		13	(Unused)
	14	03: ALM7_L1		14	(Unused)
	15	03: ALM8_L1		15	(Unused)
50	0	(Unused)	50	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	03: ALM1_L2		8	(Unused)
	9	03: ALM2_L2		9	(Unused)
	10	03: ALM3_L2		10	(Unused)
	11	03: ALM4_L2		11	(Unused)
	12	03: ALM5_L2		12	(Unused)
	13	03: ALM6_L2		13	(Unused)
	14	03: ALM7_L2		14	(Unused)
	15	03: ALM8_L2		15	(Unused)
51	0	(Unused)	51	0	(Unused)
	1	04: R.L_L1		1	04: R.L_L1
	2	04: S.R		2	04: S.R
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	04: ALM1_L1		8	(Unused)
	9	04: ALM2_L1		9	(Unused)
	10	04: ALM3_L1		10	(Unused)
	11	04: ALM4_L1		11	(Unused)
	12	04: ALM5_L1		12	(Unused)
	13	04: ALM6_L1		13	(Unused)
	14	04: ALM7_L1		14	(Unused)
	15	04: ALM8_L1		15	(Unused)
52	0	(Unused)	52	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	04: ALM1_L2		8	(Unused)
	9	04: ALM2_L2		9	(Unused)
	10	04: ALM3_L2		10	(Unused)
	11	04: ALM4_L2		11	(Unused)
	12	04: ALM5_L2		12	(Unused)
	13	04: ALM6_L2		13	(Unused)
	14	04: ALM7_L2		14	(Unused)
	15	04: ALM8_L2		15	(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
53	0	(Unused)		53	0	(Unused)
	1	05: R.L_L1			1	05: R.L_L1
	2	05: S.R			2	05: S.R
	3	(Unused)			3	(Unused)
	4	(Unused)			4	(Unused)
	5	(Unused)			5	(Unused)
	6	(Unused)			6	(Unused)
	7	(Unused)			7	(Unused)
	8	05: ALM1_L1			8	(Unused)
	9	05: ALM2_L1			9	(Unused)
	10	05: ALM3_L1			10	(Unused)
	11	05: ALM4_L1			11	(Unused)
	12	05: ALM5_L1			12	(Unused)
	13	05: ALM6_L1			13	(Unused)
	14	05: ALM7_L1			14	(Unused)
	15	05: ALM8_L1			15	(Unused)
54	0	(Unused)		54	0	(Unused)
	1	(Unused)			1	(Unused)
	2	(Unused)			2	(Unused)
	3	(Unused)			3	(Unused)
	4	(Unused)			4	(Unused)
	5	(Unused)			5	(Unused)
	6	(Unused)			6	(Unused)
	7	(Unused)			7	(Unused)
	8	05: ALM1_L2			8	(Unused)
	9	05: ALM2_L2			9	(Unused)
	10	05: ALM3_L2			10	(Unused)
	11	05: ALM4_L2			11	(Unused)
	12	05: ALM5_L2			12	(Unused)
	13	05: ALM6_L2			13	(Unused)
	14	05: ALM7_L2			14	(Unused)
	15	05: ALM8_L2			15	(Unused)

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Page 2

Profile number 5 (Cascade control with 5 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		04: P_L1_1	8		04: P_L1_1
9		05: P_L1_1	9		05: P_L1_1
10		01: I_L1_1	10		01: I_L1_1
11		02: I_L1_1	11		02: I_L1_1
12		03: I_L1_1	12		03: I_L1_1
13		04: I_L1_1	13		04: I_L1_1
14		05: I_L1_1	14		05: I_L1_1
15		01: D_L1_1	15		01: D_L1_1
16		02: D_L1_1	16		02: D_L1_1
17		03: D_L1_1	17		03: D_L1_1
18		04: D_L1_1	18		04: D_L1_1
19		05: D_L1_1	19		05: D_L1_1
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25		01: P_L2_1	25		01: P_L2_1
26		02: P_L2_1	26		02: P_L2_1
27		03: P_L2_1	27		03: P_L2_1
28		04: P_L2_1	28		04: P_L2_1
29		05: P_L2_1	29		05: P_L2_1
30		01: I_L2_1	30		01: I_L2_1
31		02: I_L2_1	31		02: I_L2_1
32		03: I_L2_1	32		03: I_L2_1
33		04: I_L2_1	33		04: I_L2_1

Profile number 5 (Cascade control with 5 connected controllers) on page 2						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
34		05: I_L2_1		34		05: I_L2_1
35		01: D_L2_1		35		01: D_L2_1
36		02: D_L2_1		36		02: D_L2_1
37		03: D_L2_1		37		03: D_L2_1
38		04: D_L2_1		38		04: D_L2_1
39		05: D_L2_1		39		05: D_L2_1
40		(Unused)		40		(Unused)
41		(Unused)		41		(Unused)
42		(Unused)		42		(Unused)
43		(Unused)		43		(Unused)
44		(Unused)		44		(Unused)
45		(Unused)		45		(Unused)
46		(Unused)		46		(Unused)
47		(Unused)		47		(Unused)
48		(Unused)		48		(Unused)
49		(Unused)		49		(Unused)
50		(Unused)		50		(Unused)
51		(Unused)		51		(Unused)
52		(Unused)		52		(Unused)
53		(Unused)		53		(Unused)
54		(Unused)		54		(Unused)

Page 3

Profile number 5 (Cascade control with 5 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		(Unused)	5		(Unused)
6		(Unused)	6		(Unused)
7		(Unused)	7		(Unused)
8		(Unused)	8		(Unused)
9		(Unused)	9		(Unused)
10		(Unused)	10		(Unused)
11		(Unused)	11		(Unused)
12		(Unused)	12		(Unused)
13		(Unused)	13		(Unused)
14		(Unused)	14		(Unused)
15		(Unused)	15		(Unused)
16		(Unused)	16		(Unused)
17		(Unused)	17		(Unused)
18		(Unused)	18		(Unused)
19		(Unused)	19		(Unused)
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25		01: Pc_L2_1	25		01: Pc_L2_1
26		02: Pc_L2_1	26		02: Pc_L2_1
27		03: Pc_L2_1	27		03: Pc_L2_1
28		04: Pc_L2_1	28		04: Pc_L2_1
29		05: Pc_L2_1	29		05: Pc_L2_1
30		01: Ic_L2_1	30		01: Ic_L2_1
31		02: Ic_L2_1	31		02: Ic_L2_1
32		03: Ic_L2_1	32		03: Ic_L2_1
33		04: Ic_L2_1	33		04: Ic_L2_1
34		05: Ic_L2_1	34		05: Ic_L2_1
35		01: Dc_L2_1	35		01: Dc_L2_1
36		02: Dc_L2_1	36		02: Dc_L2_1
37		03: Dc_L2_1	37		03: Dc_L2_1
38		04: Dc_L2_1	38		04: Dc_L2_1
39		05: Dc_L2_1	39		05: Dc_L2_1

Profile number 5 (Cascade control with 5 connected controllers) on page 3						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
40		(Unused)		40		(Unused)
41		(Unused)		41		(Unused)
42		(Unused)		42		(Unused)
43		(Unused)		43		(Unused)
44		(Unused)		44		(Unused)
45		(Unused)		45		(Unused)
46		(Unused)		46		(Unused)
47		(Unused)		47		(Unused)
48		(Unused)		48		(Unused)
49		(Unused)		49		(Unused)
50		(Unused)		50		(Unused)
51		(Unused)		51		(Unused)
52		(Unused)		52		(Unused)
53		(Unused)		53		(Unused)
54		(Unused)		54		(Unused)

Page 4

Profile number 5 (Cascade control with 5 connected controllers) on page 4

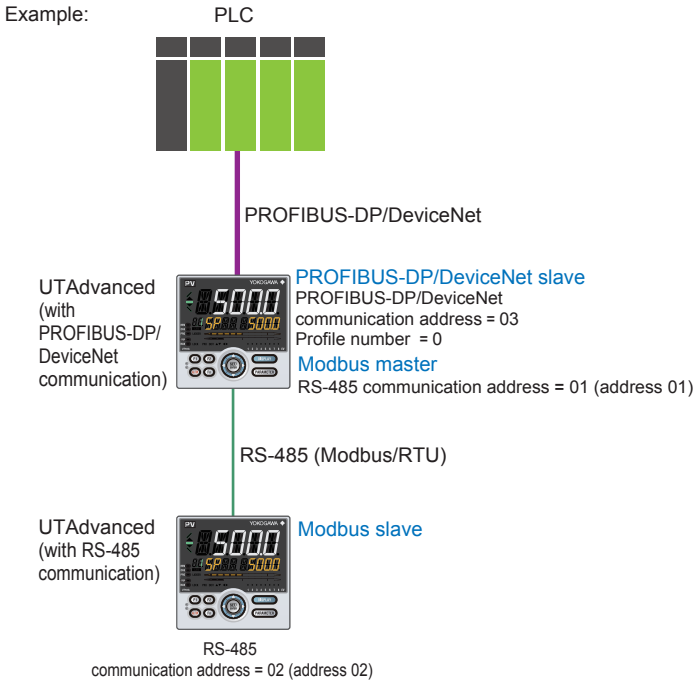
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		04: A1_L1_1	8		04: A1_L1_1
9		05: A1_L1_1	9		05: A1_L1_1
10		01: A2_L1_1	10		01: A2_L1_1
11		02: A2_L1_1	11		02: A2_L1_1
12		03: A2_L1_1	12		03: A2_L1_1
13		04: A2_L1_1	13		04: A2_L1_1
14		05: A2_L1_1	14		05: A2_L1_1
15		01: A3_L1_1	15		01: A3_L1_1
16		02: A3_L1_1	16		02: A3_L1_1
17		03: A3_L1_1	17		03: A3_L1_1
18		04: A3_L1_1	18		04: A3_L1_1
19		05: A3_L1_1	19		05: A3_L1_1
20		01: A4_L1_1	20		01: A4_L1_1
21		02: A4_L1_1	21		02: A4_L1_1
22		03: A4_L1_1	22		03: A4_L1_1
23		04: A4_L1_1	23		04: A4_L1_1
24		05: A4_L1_1	24		05: A4_L1_1
25		01: A5_L1_1	25		01: A5_L1_1
26		02: A5_L1_1	26		02: A5_L1_1
27		03: A5_L1_1	27		03: A5_L1_1
28		04: A5_L1_1	28		04: A5_L1_1
29		05: A5_L1_1	29		05: A5_L1_1
30		01: A1_L2_1	30		01: A1_L2_1
31		02: A1_L2_1	31		02: A1_L2_1
32		03: A1_L2_1	32		03: A1_L2_1
33		04: A1_L2_1	33		04: A1_L2_1

Profile number 5 (Cascade control with 5 connected controllers) on page 4						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
34		05: A1_L2_1		34		05: A1_L2_1
35		01: A2_L2_1		35		01: A2_L2_1
36		02: A2_L2_1		36		02: A2_L2_1
37		03: A2_L2_1		37		03: A2_L2_1
38		04: A2_L2_1		38		04: A2_L2_1
39		05: A2_L2_1		39		05: A2_L2_1
40		01: A3_L2_1		40		01: A3_L2_1
41		02: A3_L2_1		41		02: A3_L2_1
42		03: A3_L2_1		42		03: A3_L2_1
43		04: A3_L2_1		43		04: A3_L2_1
44		05: A3_L2_1		44		05: A3_L2_1
45		01: A4_L2_1		45		01: A4_L2_1
46		02: A4_L2_1		46		02: A4_L2_1
47		03: A4_L2_1		47		03: A4_L2_1
48		04: A4_L2_1		48		04: A4_L2_1
49		05: A4_L2_1		49		05: A4_L2_1
50		01: A5_L2_1		50		01: A5_L2_1
51		02: A5_L2_1		51		02: A5_L2_1
52		03: A5_L2_1		52		03: A5_L2_1
53		04: A5_L2_1		53		04: A5_L2_1
54		05: A5_L2_1		54		05: A5_L2_1

3.9.2 Profile List for UP55A/UP35A

UP55A
UP35A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers])



Page 1

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
	8	(Reserved)		8	(Reserved)
	9	(Reserved)		9	(Reserved)
	10	(Reserved)		10	(Reserved)
	11	(Reserved)		11	(Reserved)
	12	(Reserved)		12	(Reserved)
	13	(Reserved)		13	(Reserved)
	14	(Reserved)		14	(Reserved)
	15	(Reserved)		15	(Reserved)
1	0	Normal connection slave (address 01)	1	0	Batch write request (address 01)
	1	Normal connection slave (address 02)		1	Batch write request (address 02)
	2	Normal connection slave (address 03)		2	Batch write request (address 03)
	3	Normal connection slave (address 04)		3	Batch write request (address 04)
	4	Normal connection slave (address 05)		4	Batch write request (address 05)
	5	Normal connection slave (address 06)		5	Batch write request (address 06)
	6	Normal connection slave (address 07)		6	Batch write request (address 07)
	7	Normal connection slave (address 08)		7	Batch write request (address 08)
	8	Normal connection slave (address 09)		8	Batch write request (address 09)
	9	Normal connection slave (address 10)		9	Batch write request (address 10)
	10	Normal connection slave (address 11)		10	Batch write request (address 11)
	11	Normal connection slave (address 12)		11	Batch write request (address 12)
	12	Normal connection slave (address 13)		12	Batch write request (address 13)
	13	Normal connection slave (address 14)		13	Batch write request (address 14)
	14	Normal connection slave (address 15)		14	Batch write request (address 15)
	15	Normal connection slave (address 16)		15	Batch write request (address 16)

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 17)
	1	Normal connection slave (address 18)		1	Batch write request (address 18)
	2	Normal connection slave (address 19)		2	Batch write request (address 19)
	3	Normal connection slave (address 20)		3	Batch write request (address 20)
	4	Normal connection slave (address 21)		4	Batch write request (address 21)
	5	Normal connection slave (address 22)		5	Batch write request (address 22)
	6	Normal connection slave (address 23)		6	Batch write request (address 23)
	7	Normal connection slave (address 24)		7	Batch write request (address 24)
	8	Normal connection slave (address 25)		8	Batch write request (address 25)
	9	Normal connection slave (address 26)		9	Batch write request (address 26)
	10	Normal connection slave (address 27)		10	Batch write request (address 27)
	11	Normal connection slave (address 28)		11	Batch write request (address 28)
	12	Normal connection slave (address 29)		12	Batch write request (address 29)
	13	Normal connection slave (address 30)		13	Batch write request (address 30)
	14	Normal connection slave (address 31)		14	Batch write request (address 31)
	15	Normal connection slave (address 32)		15	Batch write request (address 32)
3		Current profile number	3		(Unused)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		02: PV_L1	8		02: H.TSP_L1
9		02: CSP_L1	9		02: H.SP_L1
10		02: SEG_RTIME	10		02: H.TM_L1
11	0	01: RST_ON	11	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD		3	01: HOLD
	4	(Unused)		4	01: ADV
	5	01: A.M_L1		5	01: A.M_L1
	6	01: PV_EV1		6	(Unused)
	7	01: PV_EV2		7	(Unused)
	8	01: TIME_EV1		8	(Unused)
	9	01: TIME_EV2		9	(Unused)
	10	01: TIME_EV3		10	(Unused)
	11	01: TIME_EV4		11	(Unused)
	12	01: TIME_EV5		12	(Unused)
	13	01: TIME_EV6		13	(Unused)
	14	01: TIME_EV7		14	(Unused)
	15	01: TIME_EV8		15	(Unused)
12	0	02: RST_ON	12	0	02: RST_ON
	1	02: PRG_ON		1	02: PRG_ON
	2	02: LOC_ON		2	02: LOC_ON
	3	02: HOLD		3	02: HOLD
	4	(Unused)		4	02: ADV
	5	02: A.M_L1		5	02: A.M_L1
	6	02: PV_EV1		6	(Unused)
	7	02: PV_EV2		7	(Unused)
	8	02: TIME_EV1		8	(Unused)
	9	02: TIME_EV2		9	(Unused)
	10	02: TIME_EV3		10	(Unused)
	11	02: TIME_EV4		11	(Unused)
	12	02: TIME_EV5		12	(Unused)
	13	02: TIME_EV6		13	(Unused)
	14	02: TIME_EV7		14	(Unused)
	15	02: TIME_EV8		15	(Unused)

UP35A: unused

UP35A: unused

Page 2

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 2						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)		. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: P_L1_1		5		01: P_L1_1
6		01: I_L1_1		6		01: I_L1_1
7		01: D_L1_1		7		01: D_L1_1
8		01: L.PID		8		01: L.PID
9		01: C.PTNO.		9		01: PTNO.
10		01: SEG.N		10		01: SST
11		(Unused)		11		(Unused)
12		(Unused)		12		(Unused)

Page 3

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 3					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		02: P_L1_1	5		02: P_L1_1
6		02: I_L1_1	6		02: I_L1_1
7		02: D_L1_1	7		02: D_L1_1
8		02: L.PID	8		02: L.PID
9		02: C.PTNO.	9		02: PTNO.
10		02: SEG.N	10		02: SST
11		(Unused)	11		(Unused)
12		(Unused)	12		(Unused)

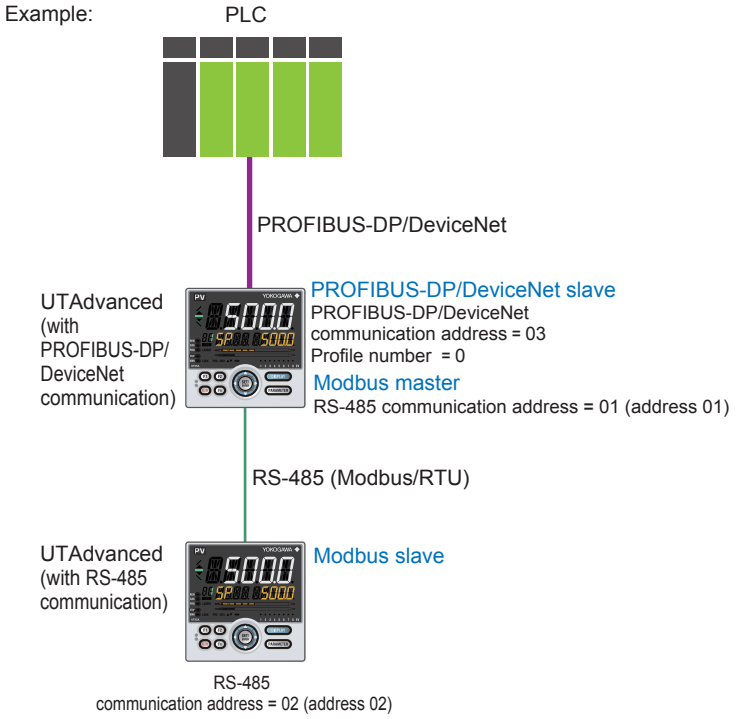
Page 4

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 4						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)		• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: L.TY1		5		01: L.TY1
6		01: L.EV1		6		01: L.EV1
7		01: L.TY2		7		01: L.TY2
8		01: L.EV2		8		01: L.EV2
9		02: L.TY1		9		02: L.TY1
10		02: L.EV1		10		02: L.EV1
11		02: L.TY2		11		02: L.TY2
12		02: L.EV2		12		02: L.EV2

Intentionally blank

Profile number 11 (Simple PID control with 2 connected controllers)

UP55A
UP35A



Page 1

Profile number 11 (Simple PID control with 2 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		01: LSP_L1	8		01: LSP_L1
9		01: OUT_L1	9		01: MOUT_L1
10		01: C.PTNO.	10		01: PTNO.
11		01: SEG.N	11		01: SST
12		(Unused)	12		(Unused)
13		02: PV_L1	13		02: H.TSP_L1
14		02: CSP_L1	14		02: H.SP_L1
15		02: SEG_RTIME	15		02: H.TM_L1

Profile number 11 (Simple PID control with 2 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
16		02: LSP_L1	16		02: LSP_L1
17		02: OUT_L1	17		02: MOUT_L1
18		02: C.PTNO.	18		02: PTNO.
19		02: SEG.N	19		02: SST
20	0	01: RST_ON	20	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD		3	01: HOLD
	4	(Unused)		4	01: ADV
	5	01: A.M_L1		5	01: A.M_L1
	6	01: ALM1_L1		6	(Unused)
	7	01: ALM2_L1		7	(Unused)
	8	01: PV_EV1		8	(Unused)
	9	01: PV_EV2		9	(Unused)
	10	01: PV_EV3		10	(Unused)
	11	01: PV_EV4		11	(Unused)
	12	01: PV_EV5		12	(Unused)
	13	01: PV_EV6		13	(Unused)
	14	01: PV_EV7		14	(Unused)
	15	01: PV_EV8		15	(Unused)
21	0	01: TIME_EV1	21	0	(Unused)
	1	01: TIME_EV2		1	(Unused)
	2	01: TIME_EV3		2	(Unused)
	3	01: TIME_EV4		3	(Unused)
	4	01: TIME_EV5		4	(Unused)
	5	01: TIME_EV6		5	(Unused)
	6	01: TIME_EV7		6	(Unused)
	7	01: TIME_EV8		7	(Unused)
	8	01: TIME_EV9		8	(Unused)
	9	01: TIME_EV10		9	(Unused)
	10	01: TIME_EV11		10	(Unused)
	11	01: TIME_EV12		11	(Unused)
	12	01: TIME_EV13		12	(Unused)
	13	01: TIME_EV14		13	(Unused)
	14	01: TIME_EV15		14	(Unused)
	15	01: TIME_EV16		15	(Unused)
22	0	02: RST_ON	22	0	02: RST_ON
	1	02: PRG_ON		1	02: PRG_ON
	2	02: LOC_ON		2	02: LOC_ON
	3	02: HOLD		3	02: HOLD
	4	(Unused)		4	02: ADV
	5	02: A.M_L1		5	02: A.M_L1
	6	02: ALM1_L1		6	(Unused)
	7	02: ALM2_L1		7	(Unused)
	8	02: PV_EV1		8	(Unused)
	9	02: PV_EV2		9	(Unused)
	10	02: PV_EV3		10	(Unused)
	11	02: PV_EV4		11	(Unused)
	12	02: PV_EV5		12	(Unused)
	13	02: PV_EV6		13	(Unused)
	14	02: PV_EV7		14	(Unused)
	15	02: PV_EV8		15	(Unused)

UP35A: unused

Profile number 11 (Simple PID control with 2 connected controllers) on page 1						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
23	0	02: TIME_EV1		21	0	(Unused)
	1	02: TIME_EV2			1	(Unused)
	2	02: TIME_EV3			2	(Unused)
	3	02: TIME_EV4			3	(Unused)
	4	02: TIME_EV5			4	(Unused)
	5	02: TIME_EV6			5	(Unused)
	6	02: TIME_EV7			6	(Unused)
	7	02: TIME_EV8			7	(Unused)
	8	02: TIME_EV9			8	(Unused)
	9	02: TIME_EV10			9	(Unused)
	10	02: TIME_EV11			10	(Unused)
	11	02: TIME_EV12			11	(Unused)
	12	02: TIME_EV13			12	(Unused)
	13	02: TIME_EV14			13	(Unused)
	14	02: TIME_EV15			14	(Unused)
	15	02: TIME_EV16			15	(Unused)

UP35A: unused

Page 2

Profile number 11 (Simple PID control with 2 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		01: I_L1_1	6		01: I_L1_1
7		01: D_L1_1	7		01: D_L1_1
8		01: L.PID	8		01: L.PID
9		01: A1_L1_1	9		01: A1_L1_1
10		01: A2_L1_1	10		01: A2_L1_1
11		01: A3_L1_1 UP35A: unused	11		01: A3_L1_1 UP35A: unused
12		(Unused)	12		(Unused)
13		02: P_L1_1	13		02: P_L1_1
14		02: I_L1_1	14		02: I_L1_1
15		02: D_L1_1	15		02: D_L1_1
16		02: L.PID	16		02: L.PID
17		02: A1_L1_1	17		02: A1_L1_1
18		02: A2_L1_1	18		02: A2_L1_1
19		02: A3_L1_1 UP35A: unused	19		02: A3_L1_1 UP35A: unused
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)

Page 3

Profile number 11 (Simple PID control with 2 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)		• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: L.TY1		5		01: L.TY1
6		01: L.EV1		6		01: L.EV1
7		01: L.TY2		7		01: L.TY2
8		01: L.EV2		8		01: L.EV2
9		01: L.TY3	UP35A: unused	9		01: L.TY3	UP35A: unused
10		01: L.EV3		10		01: L.EV3	
11		01: L.TY4		11		01: L.TY4	
12		01: L.EV4		12		01: L.EV4	
13		01: L.TY5		13		01: L.TY5	
14		01: L.EV5.		14		01: L.EV5.	
15		01: L.TY6		15		01: L.TY6	
16		01: L.EV6		16		01: L.EV6	
17		01: L.TY7		17		01: L.TY7	
18		01: L.EV7		18		01: L.EV7	
19		(Unused)		19		(Unused)	
20		(Unused)		20		(Unused)	
21		(Unused)		21		(Unused)	
22		(Unused)		22		(Unused)	

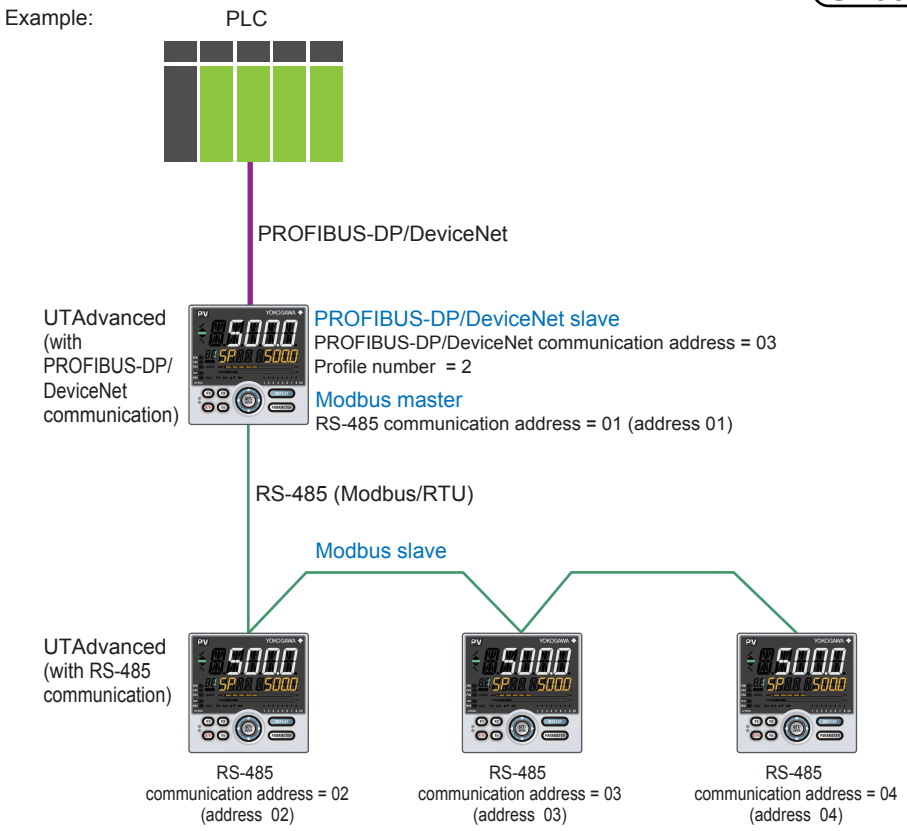
Page 4

Profile number 11 (Simple PID control with 2 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgement			2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5	(Reserved)			5	(Reserved)	
	6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)		. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page		4		Page change request	
5		02: L.TY1		5		02: L.TY1	
6		02: L.EV1		6		02: L.EV1	
7		02: L.TY2		7		02: L.TY2	
8		02: L.EV2		8		02: L.EV2	
9		02: L.TY3	UP35A: unused	9		02: L.TY3	UP35A: unused
10		02: L.EV3		10		02: L.EV3	
11		02: L.TY4		11		02: L.TY4	
12		02: L.EV4		12		02: L.EV4	
13		02: L.TY5		13		02: L.TY5	
14		02: L.EV5.		14		02: L.EV5.	
15		02: L.TY6		15		02: L.TY6	
16		02: L.EV6		16		02: L.EV6	
17		02: L.TY7		17		02: L.TY7	
18		02: L.EV7		18		02: L.EV7	
19		(Unused)		19		(Unused)	
20		(Unused)		20		(Unused)	
21		(Unused)		21		(Unused)	
22		(Unused)		22		(Unused)	

Profile number 12 (Simple PID control with 4 connected controllers)

UP55A
UP35A



Page 1

Profile number 12 (Simple PID control with 4 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		01: LSP_L1	8		01: LSP_L1
9		01: OUT_L1	9		01: MOUT_L1
10		01: C.PTNO.	10		01: PTNO.
11		01: SEG.N	11		01: SST
12		(Unused)	12		(Unused)
13		02: PV_L1	13		02: H.TSP_L1

Profile number 12 (Simple PID control with 4 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
14		02: CSP_L1	14		02: H.SP_L1
15		02: SEG_RTIME	15		02: H.TM_L1
16		02: LSP_L1	16		02: LSP_L1
17		02: OUT_L1	17		02: MOUT_L1
18		02: C.PTNO.	18		02: PTNO.
19		02: SEG.N	19		02: SST
20		(Unused)	20		(Unused)
21		03: PV_L1	21		03: H.TSP_L1
22		03: CSP_L1	22		03: H.SP_L1
23		03: SEG_RTIME	23		03: H.TM_L1
24		03: LSP_L1	24		03: LSP_L1
25		03: OUT_L1	25		03: MOUT_L1
26		03: C.PTNO.	26		03: PTNO.
27		03: SEG.N	27		03: SST
28		(Unused)	28		(Unused)
29		04: PV_L1	29		04: H.TSP_L1
30		04: CSP_L1	30		04: H.SP_L1
31		04: SEG_RTIME	31		04: H.TM_L1
32		04: LSP_L1	32		04: LSP_L1
33		04: OUT_L1	33		04: MOUT_L1
34		04: C.PTNO.	34		04: PTNO.
35		04: SEG.N	35		04: SST
36	0	01: RST_ON	36	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD		3	01: HOLD
	4	(Unused)		4	01: ADV
	5	01: A.M_L1		5	01: A.M_L1
	6	01: ALM1_L1		6	(Unused)
	7	01: ALM2_L1		7	(Unused)
	8	01: PV_EV1		8	(Unused)
	9	01: PV_EV2		9	(Unused)
	10	01: PV_EV3 } UP35A: unused		10	(Unused)
	11	01: PV_EV4		11	(Unused)
	12	01: PV_EV5		12	(Unused)
	13	01: PV_EV6		13	(Unused)
	14	01: PV_EV7		14	(Unused)
	15	01: PV_EV8		15	(Unused)
37	0	01: TIME_EV1	37	0	(Unused)
	1	01: TIME_EV2		1	(Unused)
	2	01: TIME_EV3		2	(Unused)
	3	01: TIME_EV4		3	(Unused)
	4	01: TIME_EV5		4	(Unused)
	5	01: TIME_EV6		5	(Unused)
	6	01: TIME_EV7		6	(Unused)
	7	01: TIME_EV8		7	(Unused)
	8	01: TIME_EV9		8	(Unused)
	9	01: TIME_EV10		9	(Unused)
	10	01: TIME_EV11 } UP35A: unused		10	(Unused)
	11	01: TIME_EV12		11	(Unused)
	12	01: TIME_EV13		12	(Unused)
	13	01: TIME_EV14		13	(Unused)
	14	01: TIME_EV15		14	(Unused)
	15	01: TIME_EV16		15	(Unused)

Profile number 12 (Simple PID control with 4 connected controllers) on page 1

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
38	0	02: RST_ON		38	0	02: RST_ON
	1	02: PRG_ON			1	02: PRG_ON
	2	02: LOC_ON			2	02: LOC_ON
	3	02: HOLD			3	02: HOLD
	4	(Unused)			4	02: ADV
	5	02: A.M_L1			5	02: A.M_L1
	6	02: ALM1_L1			6	(Unused)
	7	02: ALM2_L1			7	(Unused)
	8	02: PV_EV1			8	(Unused)
	9	02: PV_EV2			9	(Unused)
	10	02: PV_EV3	UP35A: unused		10	(Unused)
	11	02: PV_EV4			11	(Unused)
	12	02: PV_EV5			12	(Unused)
	13	02: PV_EV6			13	(Unused)
	14	02: PV_EV7			14	(Unused)
	15	02: PV_EV8			15	(Unused)
39	0	02: TIME_EV1		39	0	(Unused)
	1	02: TIME_EV2			1	(Unused)
	2	02: TIME_EV3			2	(Unused)
	3	02: TIME_EV4			3	(Unused)
	4	02: TIME_EV5	UP35A: unused		4	(Unused)
	5	02: TIME_EV6			5	(Unused)
	6	02: TIME_EV7			6	(Unused)
	7	02: TIME_EV8			7	(Unused)
	8	02: TIME_EV9			8	(Unused)
	9	02: TIME_EV10			9	(Unused)
	10	02: TIME_EV11			10	(Unused)
	11	02: TIME_EV12			11	(Unused)
	12	02: TIME_EV13			12	(Unused)
	13	02: TIME_EV14			13	(Unused)
	14	02: TIME_EV15			14	(Unused)
	15	02: TIME_EV16			15	(Unused)
40	0	03: RST_ON		40	0	03: RST_ON
	1	03: PRG_ON			1	03: PRG_ON
	2	03: LOC_ON			2	03: LOC_ON
	3	03: HOLD			3	03: HOLD
	4	(Unused)			4	03: ADV
	5	03: A.M_L1			5	03: A.M_L1
	6	03: ALM1_L1			6	(Unused)
	7	03: ALM2_L1			7	(Unused)
	8	03: PV_EV1			8	(Unused)
	9	03: PV_EV2			9	(Unused)
	10	03: PV_EV3	UP35A: unused		10	(Unused)
	11	03: PV_EV4			11	(Unused)
	12	03: PV_EV5			12	(Unused)
	13	03: PV_EV6			13	(Unused)
	14	03: PV_EV7			14	(Unused)
	15	03: PV_EV8			15	(Unused)

Profile number 12 (Simple PID control with 4 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
41	0	03: TIME_EV1		41	0	(Unused)
	1	03: TIME_EV2			1	(Unused)
	2	03: TIME_EV3			2	(Unused)
	3	03: TIME_EV4			3	(Unused)
	4	03: TIME_EV5	UP35A: unused		4	(Unused)
	5	03: TIME_EV6			5	(Unused)
	6	03: TIME_EV7			6	(Unused)
	7	03: TIME_EV8			7	(Unused)
	8	03: TIME_EV9			8	(Unused)
	9	03: TIME_EV10			9	(Unused)
	10	03: TIME_EV11			10	(Unused)
	11	03: TIME_EV12			11	(Unused)
	12	03: TIME_EV13			12	(Unused)
	13	03: TIME_EV14			13	(Unused)
	14	03: TIME_EV15			14	(Unused)
	15	03: TIME_EV16			15	(Unused)
42	0	04: RST_ON		42	0	04: RST_ON
	1	04: PRG_ON			1	04: PRG_ON
	2	04: LOC_ON			2	04: LOC_ON
	3	04: HOLD			3	04: HOLD
	4	(Unused)			4	04: ADV
	5	04: A.M_L1			5	04: A.M_L1
	6	04: ALM1_L1			6	(Unused)
	7	04: ALM2_L1			7	(Unused)
	8	04: PV_EV1			8	(Unused)
	9	04: PV_EV2			9	(Unused)
	10	04: PV_EV3	UP35A: unused		10	(Unused)
	11	04: PV_EV4			11	(Unused)
	12	04: PV_EV5			12	(Unused)
	13	04: PV_EV6			13	(Unused)
	14	04: PV_EV7			14	(Unused)
	15	04: PV_EV8			15	(Unused)
43	0	04: TIME_EV1		43	0	(Unused)
	1	04: TIME_EV2			1	(Unused)
	2	04: TIME_EV3			2	(Unused)
	3	04: TIME_EV4			3	(Unused)
	4	04: TIME_EV5	UP35A: unused		4	(Unused)
	5	04: TIME_EV6			5	(Unused)
	6	04: TIME_EV7			6	(Unused)
	7	04: TIME_EV8			7	(Unused)
	8	04: TIME_EV9			8	(Unused)
	9	04: TIME_EV10			9	(Unused)
	10	04: TIME_EV11			10	(Unused)
	11	04: TIME_EV12			11	(Unused)
	12	04: TIME_EV13			12	(Unused)
	13	04: TIME_EV14			13	(Unused)
	14	04: TIME_EV15			14	(Unused)
	15	04: TIME_EV16			15	(Unused)

Page 2

Profile number 12 (Simple PID control with 4 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		01: I_L1_1	6		01: I_L1_1
7		01: D_L1_1	7		01: D_L1_1
8		01: L.PID	8		01: L.PID
9		01: A1_L1_1	9		01: A1_L1_1
10		01: A2_L1_1	10		01: A2_L1_1
11		01: A3_L1_1 UP35A: unused	11		01: A3_L1_1 UP35A: unused
12		(Unused)	12		(Unused)
13		02: P_L1_1	13		02: P_L1_1
14		02: I_L1_1	14		02: I_L1_1
15		02: D_L1_1	15		02: D_L1_1
16		02: L.PID	16		02: L.PID
17		02: A1_L1_1	17		02: A1_L1_1
18		02: A2_L1_1	18		02: A2_L1_1
19		02: A3_L1_1 UP35A: unused	19		02: A3_L1_1 UP35A: unused
20		(Unused)	20		(Unused)
21		03: P_L1_1	21		03: P_L1_1
22		03: I_L1_1	22		03: I_L1_1
23		03: D_L1_1	23		03: D_L1_1
24		03: L.PID	24		03: L.PID
25		03: A1_L1_1	25		03: A1_L1_1
26		03: A2_L1_1	26		03: A2_L1_1
27		03: A3_L1_1 UP35A: unused	27		03: A3_L1_1 UP35A: unused
28		(Unused)	28		(Unused)
29		04: P_L1_1	29		04: P_L1_1
30		04: I_L1_1	30		04: I_L1_1
31		04: D_L1_1	31		04: D_L1_1
32		04: L.PID	32		04: L.PID
33		04: A1_L1_1	33		04: A1_L1_1
34		04: A2_L1_1	34		04: A2_L1_1
35		04: A3_L1_1 UP35A: unused	35		04: A3_L1_1 UP35A: unused
36		(Unused)	36		(Unused)
37		(Unused)	37		(Unused)
38		(Unused)	38		(Unused)
39		(Unused)	39		(Unused)
40		(Unused)	40		(Unused)
41		(Unused)	41		(Unused)
42		(Unused)	42		(Unused)
43		(Unused)	43		(Unused)

Page 3

Profile number 12 (Simple PID control with 4 connected controllers) on page 3

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: L.TY1	5		01: L.TY1
6		01: L.EV1	6		01: L.EV1
7		01: L.TY2	7		01: L.TY2
8		01: L.EV2	8		01: L.EV2
9		01: L.TY3	9		01: L.TY3
10		01: L.EV3	10		01: L.EV3
11		01: L.TY4	11		01: L.TY4
12		01: L.EV4	12		01: L.EV4
13		01: L.TY5	13		01: L.TY5
14		01: L.EV5.	14		01: L.EV5.
15		01: L.TY6	15		01: L.TY6
16		01: L.EV6	16		01: L.EV6
17		01: L.TY7	17		01: L.TY7
18		01: L.EV7	18		01: L.EV7
		UP35A: unused			UP35A: unused
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		02: L.TY1	21		02: L.TY1
22		02: L.EV1	22		02: L.EV1
23		02: L.TY2	23		02: L.TY2
24		02: L.EV2	24		02: L.EV2
25		02: L.TY3	25		02: L.TY3
26		02: L.EV3	26		02: L.EV3
27		02: L.TY4	27		02: L.TY4
28		02: L.EV4	28		02: L.EV4
29		02: L.TY5	29		02: L.TY5
30		02: L.EV5.	30		02: L.EV5.
31		02: L.TY6	31		02: L.TY6
32		02: L.EV6	32		02: L.EV6
33		02: L.TY7	33		02: L.TY7
34		02: L.EV7	34		02: L.EV7
		UP35A: unused			UP35A: unused
35		(Unused)	35		(Unused)
36		(Unused)	36		(Unused)
37		(Unused)	37		(Unused)
38		(Unused)	38		(Unused)
39		(Unused)	39		(Unused)
40		(Unused)	40		(Unused)
41		(Unused)	41		(Unused)
42		(Unused)	42		(Unused)
43		(Unused)	43		(Unused)

Page 4

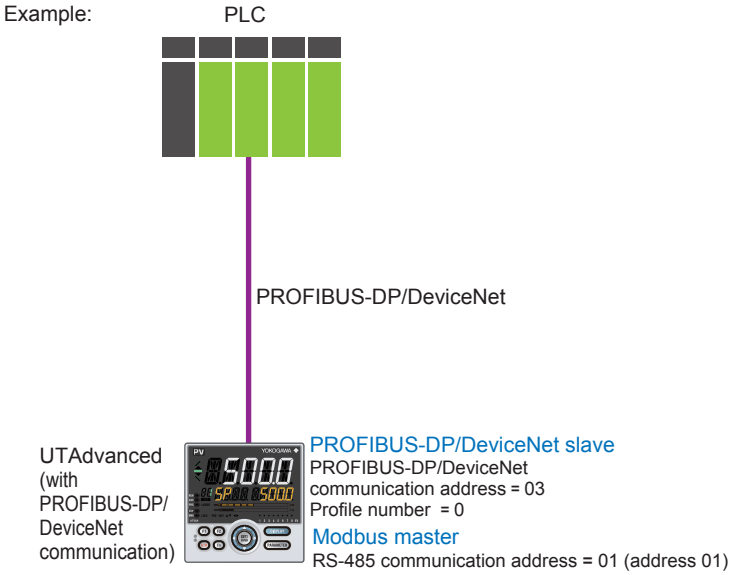
Profile number 12 (Simple PID control with 4 connected controllers) on page 4

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		03: L.TY1	5		03: L.TY1
6		03: L.EV1	6		03: L.EV1
7		03: L.EV1	7		03: L.EV1
8		03: L.EV1	8		03: L.EV1
9		03: L.TY3	9		03: L.TY3
10		03: L.EV3	10		03: L.EV3
11		03: L.TY4	11		03: L.TY4
12		03: L.EV4	12		03: L.EV4
13		03: L.TY5	13		03: L.TY5
14		03: L.EV5.	14		03: L.EV5.
15		03: L.TY6	15		03: L.TY6
16		03: L.EV6	16		03: L.EV6
17		03: L.TY7	17		03: L.TY7
18		03: L.EV7	18		03: L.EV7
		UP35A: unused			UP35A: unused
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		04: L.TY1	21		04: L.TY1
22		04: L.EV1	22		04: L.EV1
23		04: L.TY2	23		04: L.TY2
24		04: L.EV2	24		04: L.EV2
25		04: L.TY3	25		04: L.TY3
26		04: L.EV3	26		04: L.EV3
27		04: L.TY4	27		04: L.TY4
28		04: L.EV4	28		04: L.EV4
29		04: L.TY5	29		04: L.TY5
30		04: L.EV5.	30		04: L.EV5.
31		04: L.TY6	31		04: L.TY6
32		04: L.EV6	32		04: L.EV6
33		04: L.TY7	33		04: L.TY7
34		04: L.EV7	34		04: L.EV7
		UP35A: unused			UP35A: unused
35		(Unused)	35		(Unused)
36		(Unused)	36		(Unused)
37		(Unused)	37		(Unused)
38		(Unused)	38		(Unused)
39		(Unused)	39		(Unused)
40		(Unused)	40		(Unused)
41		(Unused)	41		(Unused)
42		(Unused)	42		(Unused)
43		(Unused)	43		(Unused)

Intentionally blank

Profile number 13 (Simple PID control with program patern setting for 1 connected controller)

UP55A
UP35A



Page 1

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: CSP_L2 UP35A: unused	7		01: H.SP_L2 UP35A: unused
8		01: SEG.RTIME	8		01: H.TM_L1
9		01: OUT_L1	9		01: LSP_L1
10		01: LSP_L2 UP35A: unused	10		01: LSP_L2 UP35A: unused
11		01: OUT_L1	11		01:MOUT_L1
12		01: H.OUT_L1	12		01:MOUT_L1
13		01: C.OUT_L1	13		01: MOUTc_L1
14		01: C.PTNO.	14		01: PTNO.
15		01: SEG.N	15		01: SST
16		(Unused)	16		(Unused)
• • •		• • •	• • •		• • •
77		(Unused)	77		(Unused)

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			
Word position	Bit position	Contents of assignment	
78	0	01: RST_ON	
	1	01: PRG_ON	
	2	01: LOC_ON	
	3	01: HOLD	
	4	(Unused)	
	5	01: A.M_L1	
	6	(Unused)	
	7	(Unused)	
	8	(Unused)	
	9	(Unused)	
	10	(Unused)	
	11	(Unused)	
	12	(Unused)	
	13	(Unused)	
	14	(Unused)	
	15	(Unused)	
79	0	01: PV_EV1	
	1	01: PV_EV2	
	2	01: PV_EV3	UP35A: unused
	3	01: PV_EV4	
	4	01: PV_EV5	
	5	01: PV_EV6	
	6	01: PV_EV7	
	7	01: PV_EV8	
	8	01: ALM1_L1	
	9	01: ALM2_L1	
	10	01: ALM3_L1	UP35A: unused
	11	01: ALM4_L1	UP35A: unused
	12	(Unused)	
	13	(Unused)	
	14	(Unused)	
	15	(Unused)	
80	0	01: TIME_EV1	
	1	01: TIME_EV2	
	2	01: TIME_EV3	
	3	01: TIME_EV4	
	4	01: TIME_EV5	UP35A: unused
	5	01: TIME_EV6	
	6	01: TIME_EV7	
	7	01: TIME_EV8	
	8	01: TIME_EV9	
	9	01: TIME_EV10	
	10	01: TIME_EV11	
	11	01: TIME_EV12	
	12	01: TIME_EV13	
	13	01: TIME_EV14	
	14	01: TIME_EV15	
	15	01: TIME_EV16	

OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
78	0	01: RST_ON
	1	01: PRG_ON
	2	01: LOC_ON
	3	01: HOLD
	4	01: ADV
	5	01: A.M_L1
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12	(Unused)
	13	(Unused)
	14	(Unused)
	15	(Unused)
79	0	(Unused)
	1	(Unused)
	2	(Unused)
	3	(Unused)
	4	(Unused)
	5	(Unused)
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12	(Unused)
	13	(Unused)
	14	(Unused)
	15	(Unused)
80	0	(Unused)
	1	(Unused)
	2	(Unused)
	3	(Unused)
	4	(Unused)
	5	(Unused)
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12	(Unused)
	13	(Unused)
	14	(Unused)
	15	(Unused)

Page 2

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
⋮	⋮	The fixed-part is omitted (See profile number 0 on page 1)	⋮	⋮	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		01: I_L1_1	6		01: I_L1_1
7		01: D_L1_1	7		01: D_L1_1
8		01: Pc_L1_1	8		01: Pc_L1_1
9		01: Ic_L1_1	9		01: Ic_L1_1
10		01: Dc_L1_1	10		01: Dc_L1_1
11		01: L.PID	11		01: L.PID
12		01: A1_L1_1	12		01: A1_L1_1
13		01: A2_L1_1	13		01: A2_L1_1
14		01: A3_L1_1 UP35A: unused	14		01: A3_L1_1 UP35A: unused
15		01: A4_L1_1 UP35A: unused	15		01: A4_L1_1 UP35A: unused
16		(Unused)	16		(Unused)
17		(Unused)	17		(Unused)
18		(Unused)	18		(Unused)
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)
23		(Unused)	23		(Unused)
24		(Unused)	24		(Unused)
25		01: L.TY1	25		01: L.TY1
26		01: L.EV1	26		01: L.EV1
27		01: L.TY2	27		01: L.TY2
28		01: L.EV2	28		01: L.EV2
29		01: L.TY3	29		01: L.TY3
30		01: L.EV3	30		01: L.EV3
31		01: L.TY4	31		01: L.TY4
32		01: L.EV4	32		01: L.EV4
33		01: L.TY5	33		01: L.TY5
34		01: L.EV5.	34		01: L.EV5.
35		01: L.TY6 } UP35A: unused	35		01: L.TY6 } UP35A: unused
36		01: L.EV6	36		01: L.EV6
37		01: L.TY7	37		01: L.TY7
38		01: L.EV7	38		01: L.EV7
39		01: L.TY8	39		01: L.TY8
40		01: L.EV8	40		01: L.EV8
41		(Unused)	41		(Unused)
⋮		⋮	⋮		⋮
74		(Unused)	74		(Unused)
75		01: LR.P	75		01: CLR.P
76		01: CLR.TRG	76		01: CLR.TRG
77		01: PTN.ERR	77		(Unused)
78		(Unused)	78		(Unused)
79		(Unused)	79		(Unused)
80		(Unused)	80		(Unused)

Page 3

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PTNO._C	5		01: PTNO._C
6		01: PTN.SEG	6		01: PTN.SEG
7		01: SSP_L1	7		01: SSP_L1
8		01: SSP_L2 UP35A: unused	8		01: SSP_L2 UP35A: unused
9		01: STC	9		01: STC
10		01: WT.SW1	10		01: WT.SW1
11		01: WZ.UP1	11		01: WZ.UP1
12		01: WZ.LO1	12		01: WZ.LO1
13		01: WT.TM1	13		01: WT.TM1
14		01: WT.SW2	14		01: WT.SW2
15		01: WZ.UP2	15		01: WZ.UP2
16		01: WZ.LO2	16		01: WZ.LO2
17		01: WT.TM2	17		01: WT.TM2
18		01: WT.SW3	18		01: WT.SW3
19		01: WZ.UP3	19		01: WZ.UP3
20		01: WZ.LO3	20		01: WZ.LO3
21		01: WT.TM3	21		01: WT.TM3
22		01: WT.SW4 } UP35A: unused	22		01: WT.SW4 } UP35A: unused
23		01: WZ.UP4	23		01: WZ.UP4
24		01: WZ.LO4	24		01: WZ.LO4
25		01: WT.TM4	25		01: WT.TM4
26		01: WT.SW5	26		01: WT.SW5
27		01: WZ.UP5	27		01: WZ.UP5
28		01: WZ.LO5	28		01: WZ.LO5
29		01: WT.TM5	29		01: WT.TM5
30		01: R.CYCL	30		01: R.CYCL
31		01: R.STRT	31		01: R.STRT
32		01: R.END	32		01: R.END
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)
35		01: P.NAME	35		01: P.NAME
36		01: P.NAME	36		01: P.NAME
37		01: P.NAME	37		01: P.NAME
38		01: P.NAME	38		01: P.NAME
39		01: P.NAME	39		01: P.NAME
40		01: P.NAME	40		01: P.NAME
41		01: P.NAME	41		01: P.NAME
42		01: P.NAME	42		01: P.NAME
43		01: P.NAME	43		01: P.NAME
44		01: P.NAME	44		01: P.NAME
45		01: P.NAME	45		01: P.NAME
46		01: PTN.ERR	46		(Unused)
47		(Unused)	47		(Unused)
• • •		• • •	• • •		• • •
80		(Unused)	80		(Unused)

Page 4

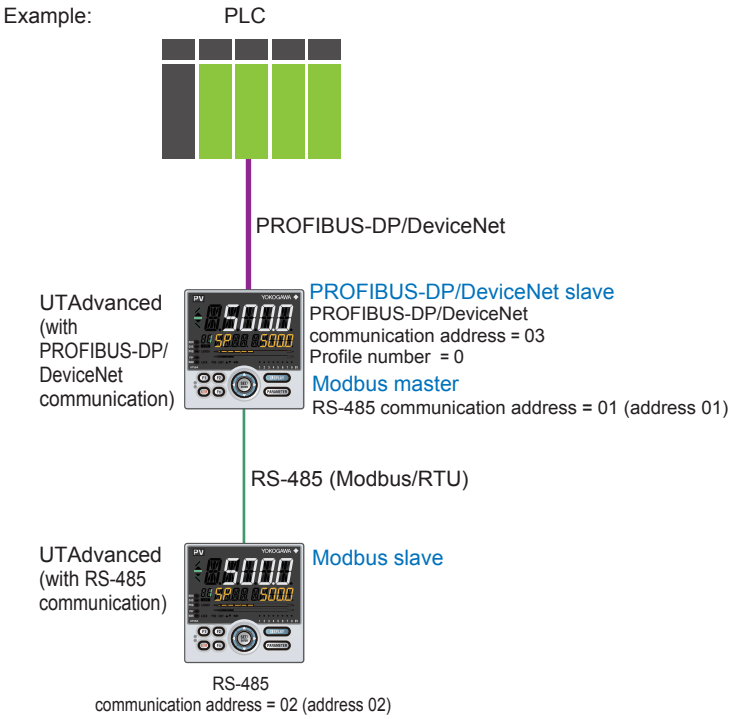
Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PTNO._C	5		01: PTNO._C
6		01: PTN.SEG	6		01: PTN.SEG
7		01: TSP_L1	7		01: TSP_L1
8		01: TSP_L2 UP35A: unused	8		01: TSP_L2 UP35A: unused
9		01: TIME	9		01: TIME
10		01: TM.RT	10		01: TM.RT
11		01: S.PID	11		01: S.PID
12		01: JC	12		01: JC
13		01: PV.TY1	13		01: PV.TY1
14		01: PV.EV1	14		01: PV.EV1
15		01: PV.TY2	15		01: PV.TY2
16		01: PV.EV2	16		01: PV.EV2
17		01: PV.TY3	17		01: PV.TY3
18		01: PV.EV3	18		01: PV.EV3
19		01: PV.TY4	19		01: PV.TY4
20		01: PV.EV4	20		01: PV.EV4
21		01: PV.TY5	21		01: PV.TY5
22		01: PV.EV5	22		01: PV.EV5
23		01: PV.TY6	23		01: PV.TY6
24		01: PV.EV6	24		01: PV.EV6
25		01: PV.TY7	25		01: PV.TY7
26		01: PV.EV7	26		01: PV.EV7
27		01: PV.TY8	27		01: PV.TY8
28		01: PV.EV8	28		01: PV.EV8
		UP35A: unused			UP35A: unused
29		01: TME1	29		01: TME1
30		01: T.ON1	30		01: T.ON1
31		01: T.OF1	31		01: T.OF1
32		01: TME2	32		01: TME2
33		01: T.ON2	33		01: T.ON2
34		01: T.OF2	34		01: T.OF2
35		01: TME3	35		01: TME3
36		01: T.ON3	36		01: T.ON3

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 4					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
37		01: T.OF3		37		01: T.OF3
38		01: TME4		38		01: TME4
39		01: T.ON4		39		01: T.ON4
40		01: T.OF4		40		01: T.OF4
41		01: TME5		41		01: TME5
42		01: T.ON5		42		01: T.ON5
43		01: T.OF5		43		01: T.OF5
44		01: TME6		44		01: TME6
45		01: T.ON6		45		01: T.ON6
46		01: T.OF6		46		01: T.OF6
47		01: TME7		47		01: TME7
48		01: T.ON7		48		01: T.ON7
49		01: T.OF7		49		01: T.OF7
50		01: TME8		50		01: TME8
51		01: T.ON8		51		01: T.ON8
52		01: T.OF8		52		01: T.OF8
53		01: TME9		53		01: TME9
54		01: T.ON9		54		01: T.ON9
55		01: T.OF9		55		01: T.OF9
56		01: TME10		56		01: TME10
57		01: T.ON10		57		01: T.ON10
58		01: T.OF10		58		01: T.OF10
59		01: TME11		59		01: TME11
60		01: T.ON11		60		01: T.ON11
61		01: T.OF11		61		01: T.OF11
62		01: TME12		62		01: TME12
63		01: T.ON12		63		01: T.ON12
64		01: T.OF12		64		01: T.OF12
65		01: TME13		65		01: TME13
66		01: T.ON13		66		01: T.ON13
67		01: T.OF13		67		01: T.OF13
68		01: TME14		68		01: TME14
69		01: T.ON14		69		01: T.ON14
70		01: T.OF14		70		01: T.OF14
71		01: TME15		71		01: TME15
72		01: T.ON15		72		01: T.ON15
73		01: T.OF15		73		01: T.OF15
74		01: TME16		74		01: TME16
75		01: T.ON16		75		01: T.ON16
76		01: T.OF16		76		01: T.OF16
41 to 76		UP35A: unused		41 to 76		UP35A: unused
77		01: PTN.ERR		77		(Unused)
78		(Unused)		78		(Unused)
79		(Unused)		79		(Unused)
80		(Unused)		80		(Unused)

Profile number 14 (Cascade control with 2 connected controllers)

UP55A



Page 1

Profile number 14 (Cascade control with 2 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		01: LSP_L1	8		01: LSP_L1
9		01: OUT_L2	9		01: MOUT_L2
10		(Unused)	10		(Unused)
11		(Unused)	11		(Unused)
12		01: C.PTNO.	12		01: PTNO.
13		01: SEG.N	13		01: SST
14		(Unused)	14		(Unused)
15		01: PV_L2	15		(Unused)

Profile number 14 (Cascade control with 2 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
16		01: CSP_L2	16		01: LSP_L2
17		01: OUT_L2	17		01: MOUT_L2
18		(Unused)	18		(Unused)
19		(Unused)	19		(Unused)
20		(Unused)	20		(Unused)
21		02: PV_L1	21		02: H.TSP_L1
22		02: CSP_L1	22		02: H.SP_L1
23		02: SEG_RTIME	23		02: H.TM_L1
24		02: LSP_L1	24		02: LSP_L1
25		02: OUT_L2	25		02: MOUT_L2
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		02: C.PTNO.	28		02: PTNO.
29		02: SEG.N	29		02: SST
30		(Unused)	30		(Unused)
31		02: PV_L2	31		(Unused)
32		02: CSP_L2	32		02: LSP_L2
33		02: OUT_L2	33		02: MOUT_L2
34		(Unused)	34		(Unused)
35		(Unused)	35		(Unused)
36	0	01: RST_ON	36	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD		3	01: HOLD
	4	(Unused)		4	01: ADV
	5	01: A.M_L2		5	01: A.M_L2
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: L.C		8	01: L.C
	9	(Unused)		9	(Unused)
	10	(Unused)		10	(Unused)
	11	(Unused)		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)
37	0	01: PV_EV1	37	0	(Unused)
	1	01: PV_EV2		1	(Unused)
	2	01: PV_EV3		2	(Unused)
	3	01: PV_EV4		3	(Unused)
	4	01: PV_EV5		4	(Unused)
	5	01: PV_EV6		5	(Unused)
	6	01: PV_EV7		6	(Unused)
	7	01: PV_EV8		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)

Profile number 14 (Cascade control with 2 connected controllers) on page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
38	0	01: TIME_EV1	38	0	(Unused)
	1	01: TIME_EV2		1	(Unused)
	2	01: TIME_EV3		2	(Unused)
	3	01: TIME_EV4		3	(Unused)
	4	01: TIME_EV5		4	(Unused)
	5	01: TIME_EV6		5	(Unused)
	6	01: TIME_EV7		6	(Unused)
	7	01: TIME_EV8		7	(Unused)
	8	01: TIME_EV9		8	(Unused)
	9	01: TIME_EV10		9	(Unused)
	10	01: TIME_EV11		10	(Unused)
	11	01: TIME_EV12		11	(Unused)
	12	01: TIME_EV13		12	(Unused)
	13	01: TIME_EV14		13	(Unused)
	14	01: TIME_EV15		14	(Unused)
	15	01: TIME_EV16		15	(Unused)
39	0	(Unused)	39	0	(Unused)
	1	(Unused)		1	(Unused)
	2	(Unused)		2	(Unused)
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	(Unused)		8	(Unused)
	9	(Unused)		9	(Unused)
	10	(Unused)		10	(Unused)
	11	(Unused)		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)
40	0	02: RST_ON	40	0	02: RST_ON
	1	02: PRG_ON		1	02: PRG_ON
	2	02: LOC_ON		2	02: LOC_ON
	3	02: HOLD		3	02: HOLD
	4	(Unused)		4	02: ADV
	5	02: A.M_L2		5	02: A.M_L2
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: L.C		8	02: L.C
	9	(Unused)		9	(Unused)
	10	(Unused)		10	(Unused)
	11	(Unused)		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)

Profile number 14 (Cascade control with 2 connected controllers) on page 1					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
41	0	02: PV_EV1		41	0	(Unused)
	1	02: PV_EV2			1	(Unused)
	2	02: PV_EV3			2	(Unused)
	3	02: PV_EV4			3	(Unused)
	4	02: PV_EV5			4	(Unused)
	5	02: PV_EV6			5	(Unused)
	6	02: PV_EV7			6	(Unused)
	7	02: PV_EV8			7	(Unused)
	8	02: ALM1_L1			8	(Unused)
	9	02: ALM2_L1			9	(Unused)
	10	02: ALM3_L1			10	(Unused)
	11	02: ALM4_L1			11	(Unused)
	12	(Unused)			12	(Unused)
	13	(Unused)			13	(Unused)
	14	(Unused)			14	(Unused)
	15	(Unused)			15	(Unused)
42	0	02: TIME_EV1		42	0	(Unused)
	1	02: TIME_EV2			1	(Unused)
	2	02: TIME_EV3			2	(Unused)
	3	02: TIME_EV4			3	(Unused)
	4	02: TIME_EV5			4	(Unused)
	5	02: TIME_EV6			5	(Unused)
	6	02: TIME_EV7			6	(Unused)
	7	02: TIME_EV8			7	(Unused)
	8	02: TIME_EV9			8	(Unused)
	9	02: TIME_EV10			9	(Unused)
	10	02: TIME_EV11			10	(Unused)
	11	02: TIME_EV12			11	(Unused)
	12	02: TIME_EV13			12	(Unused)
	13	02: TIME_EV14			13	(Unused)
	14	02: TIME_EV15			14	(Unused)
	15	02: TIME_EV16			15	(Unused)
43	0	(Unused)		43	0	(Unused)
	1	(Unused)			1	(Unused)
	2	(Unused)			2	(Unused)
	3	(Unused)			3	(Unused)
	4	(Unused)			4	(Unused)
	5	(Unused)			5	(Unused)
	6	(Unused)			6	(Unused)
	7	(Unused)			7	(Unused)
	8	(Unused)			8	(Unused)
	9	(Unused)			9	(Unused)
	10	(Unused)			10	(Unused)
	11	(Unused)			11	(Unused)
	12	(Unused)			12	(Unused)
	13	(Unused)			13	(Unused)
	14	(Unused)			14	(Unused)
	15	(Unused)			15	(Unused)

Page 2

Profile number 14 (Cascade control with 2 connected controllers) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
0	0	Receive data valid		0	0	Rescan request
	1	During-write			1	(Reserved)
	2	Write acknowledgement			2	Write request
	3	(Reserved)			3	(Reserved)
	4	(Reserved)			4	(Reserved)
	5	(Reserved)			5	(Reserved)
	6	(Reserved)			6	(Reserved)
	7	(Reserved)			7	(Reserved)
⋮	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	⋮	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page		4		Page change request
5		01: P_L1_1		5		01: P_L1_1
6		01: I_L1_1		6		01: I_L1_1
7		01: D_L1_1		7		01: D_L1_1
8		01: L.PID		8		01: L.PID
9		01: A1_L1_1		9		01: A1_L1_1
10		01: A2_L1_1		10		01: A2_L1_1
11		01: A3_L1_1		11		01: A3_L1_1
12		01: A4_L1_1		12		01: A4_L1_1
13		01: P_L2_1		13		01: P_L2_1
14		01: I_L2_1		14		01: I_L2_1
15		01: D_L2_1		15		01: D_L2_1
16		01: A1_L2_1		16		01: A1_L2_1
17		01: A2_L2_1		17		01: A2_L2_1
18		01: A3_L2_1		18		01: A3_L2_1
19		01: A4_L2_1		19		01: A4_L2_1
20		(Unused)		20		(Unused)
21		02: P_L1_1		21		02: P_L1_1
22		03: I_L1_1		22		03: I_L1_1
23		02: D_L1_1		23		02: D_L1_1
24		02: L.PID		24		02: L.PID
25		02: A1_L1_1		25		02: A1_L1_1
26		02: A2_L1_1		26		02: A2_L1_1
27		02: A3_L1_1		27		02: A3_L1_1
28		02: A4_L1_1		28		02: A4_L1_1
29		02: P_L2_1		29		02: P_L2_1
30		02: I_L2_1		30		02: I_L2_1
31		02: D_L2_1		31		02: D_L2_1
32		02: A1_L2_1		32		02: A1_L2_1
33		02: A2_L2_1		33		02: A2_L2_1
34		02: A3_L2_1		34		02: A3_L2_1
35		02: A4_L2_1		35		02: A4_L2_1
36		(Unused)		36		(Unused)
⋮		⋮		⋮		⋮
43		(Unused)		43		(Unused)

Page 3

Profile number 14 (Cascade control with 2 connected controllers) on page 3

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: L.TY1	5		01: L.TY1
6		01: L.EV1	6		01: L.EV1
7		01: L.TY2	7		01: L.TY2
8		01: L.EV2	8		01: L.EV2
9		01: L.TY3	9		01: L.TY3
10		01: L.EV3	10		01: L.EV3
11		01: L.TY4	11		01: L.TY4
12		01: L.EV4	12		01: L.EV4
13		01: L.TY5	13		01: L.TY5
14		01: L.EV5.	14		01: L.EV5.
15		01: L.TY6	15		01: L.TY6
16		01: L.EV6	16		01: L.EV6
17		01: L.TY7	17		01: L.TY7
18		01: L.EV7	18		01: L.EV7
19		01: L.TY8	19		01: L.TY8
20		01: L.EV8	20		01: L.EV8
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)
23		(Unused)	23		(Unused)
24		(Unused)	24		(Unused)
25		(Unused)	25		(Unused)
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)
35		(Unused)	35		(Unused)
36		(Unused)	36		(Unused)
37		(Unused)	37		(Unused)
38		(Unused)	38		(Unused)
39		(Unused)	39		(Unused)
40		(Unused)	40		(Unused)
41		(Unused)	41		(Unused)
42		(Unused)	42		(Unused)
43		(Unused)	43		(Unused)

Page 4

Profile number 14 (Cascade control with 2 connected controllers) on page 4

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		02: L.TY1	5		02: L.TY1
6		02: L.EV1	6		02: L.EV1
7		02: L.TY2	7		02: L.TY2
8		02: L.EV2	8		02: L.EV2
9		02: L.TY3	9		02: L.TY3
10		02: L.EV3	10		02: L.EV3
11		02: L.TY4	11		02: L.TY4
12		02: L.EV4	12		02: L.EV4
13		02: L.TY5	13		02: L.TY5
14		02: L.EV5.	14		02: L.EV5.
15		02: L.TY6	15		02: L.TY6
16		02: L.EV6	16		02: L.EV6
17		02: L.TY7	17		02: L.TY7
18		02: L.EV7	18		02: L.EV7
19		02: L.TY8	19		02: L.TY8
20		02: L.EV8	20		02: L.EV8
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)
23		(Unused)	23		(Unused)
24		(Unused)	24		(Unused)
25		(Unused)	25		(Unused)
26		(Unused)	26		(Unused)
27		(Unused)	27		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)
35		(Unused)	35		(Unused)
36		(Unused)	36		(Unused)
37		(Unused)	37		(Unused)
38		(Unused)	38		(Unused)
39		(Unused)	39		(Unused)
40		(Unused)	40		(Unused)
41		(Unused)	41		(Unused)
42		(Unused)	42		(Unused)
43		(Unused)	43		(Unused)

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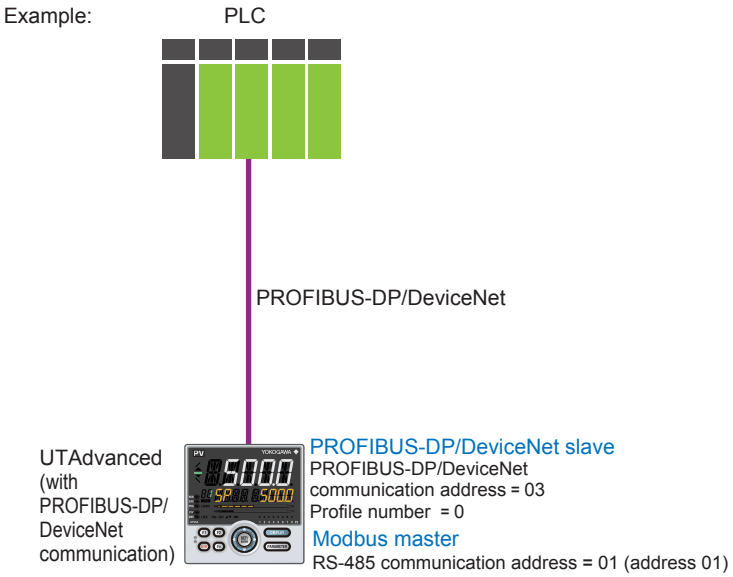
Profile number 15 (Cascade control with program patern setting for 1 connected controller)

UP55A



Page 1

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 1

IN area: PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area: PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Receive data valid
	1	During-write		1	During-write
	2	Write acknowledgement		2	Write acknowledgement
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)	. . .	. . .	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Current page
5		01: PV_L1	5		(Unused)
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		01: LSP_L1	8		01: LSP_L1
9		01: OUT_L2	9		01: MOUT_L2
10		(Unused)	10		(Unused)
11		(Unused)	11		(Unused)
12		01: C.PTNO.	12		01: PTNO.
13		01: SEG.N	13		01: SST
14		(Unused)	14		(Unused)
15		01: PV_L2	15		(Unused)
16		01: CSP_L2	16		01: LSP_L2
17		01: OUT_L2	17		01: MOUT_L2
18		(Unused)	18		(Unused)
. . .		. . .	. . .		. . .

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 1					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
77		(Unused)	77		(Unused)
78	0	01: RST_ON	78	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD		3	01: HOLD
	4	(Unused)		4	01: ADV
	5	01: A.M_L2		5	01: A.M_L2
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: L.C		8	01: L.C
	9	(Unused)		9	(Unused)
	10	(Unused)		10	(Unused)
	11	(Unused)		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)
79	0	01: PV_EV1	79	0	(Unused)
	1	01: PV_EV2		1	(Unused)
	2	01: PV_EV3		2	(Unused)
	3	01: PV_EV4		3	(Unused)
	4	01: PV_EV5		4	(Unused)
	5	01: PV_EV6		5	(Unused)
	6	01: PV_EV7		6	(Unused)
	7	01: PV_EV8		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12	(Unused)		12	(Unused)
	13	(Unused)		13	(Unused)
	14	(Unused)		14	(Unused)
	15	(Unused)		15	(Unused)
80	0	01: TIME_EV1	80	0	(Unused)
	1	01: TIME_EV2		1	(Unused)
	2	01: TIME_EV3		2	(Unused)
	3	01: TIME_EV4		3	(Unused)
	4	01: TIME_EV5		4	(Unused)
	5	01: TIME_EV6		5	(Unused)
	6	01: TIME_EV7		6	(Unused)
	7	01: TIME_EV8		7	(Unused)
	8	01: TIME_EV9		8	(Unused)
	9	01: TIME_EV10		9	(Unused)
	10	01: TIME_EV11		10	(Unused)
	11	01: TIME_EV12		11	(Unused)
	12	01: TIME_EV13		12	(Unused)
	13	01: TIME_EV14		13	(Unused)
	14	01: TIME_EV15		14	(Unused)
	15	01: TIME_EV16		15	(Unused)

Page 2

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 2

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		01: I_L1_1	6		01: I_L1_1
7		01: D_L1_1	7		01: D_L1_1
8		01: L.PID	8		01: L.PID
9		01: A1_L1_1	9		01: A1_L1_1
10		01: A2_L1_1	10		01: A2_L1_1
11		01: A3_L1_1	11		01: A3_L1_1
12		01: A4_L1_1	12		01: A4_L1_1
13		01: P_L2_1	13		01: P_L2_1
14		01: I_L2_1	14		01: I_L2_1
15		01: D_L2_1	15		01: D_L2_1
16		01: A1_L2_1	16		01: A1_L2_1
17		01: A2_L2_1	17		01: A2_L2_1
18		01: A3_L2_1	18		01: A3_L2_1
19		01: A4_L2_1	19		01: A4_L2_1
20		(Unused)	20		(Unused)
21		(Unused)	21		(Unused)
22		(Unused)	22		(Unused)
23		(Unused)	23		(Unused)
24		(Unused)	24		(Unused)
25		01: L.TY1	25		01: L.TY1
26		01: L.EV1	26		01: L.EV1
27		01: L.TY2	27		01: L.TY2
28		01: L.EV2	28		01: L.EV2
29		01: L.TY3	29		01: L.TY3
30		01: L.EV3	30		01: L.EV3
31		01: L.TY4	31		01: L.TY4
32		01: L.EV4	32		01: L.EV4
33		01: L.TY5	33		01: L.TY5
34		01: L.EV5.	34		01: L.EV5.
35		01: L.TY6	35		01: L.TY6
36		01: L.EV6	36		01: L.EV6

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 2						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
37		01: L.TY7		37		01: L.TY7
38		01: L.EV7		38		01: L.EV7
39		01: L.TY8		39		01: L.TY8
40		01: L.EV8		40		01: L.EV8
41		(Unused)		41		(Unused)
⋮		⋮		⋮		⋮
74		(Unused)		74		(Unused)
75		01: CLR.P		75		01: CLR.P
76		01: CLR.TRG		76		01: CLR.TRG
77		01: PTN.ERR		77		(Unused)
78		(Unused)		78		(Unused)
79		(Unused)		79		(Unused)
80		(Unused)		80		(Unused)

Page 3

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Receive data valid
	1	During-write		1	During-write
	2	Write acknowledgement		2	Write acknowledgement
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Current page
5		01: PTNO._C	5		01: PTNO._C
6		01: SEGNO._C	6		01: SEGNO._C
7		01: SSP_L1	7		01: SSP_L1
8		(Unused)	8		(Unused)
9		01: STC	9		01: STC
10		01: WT.SW1	10		01: WT.SW1
11		01: WZ.UP1	11		01: WZ.UP1
12		01: WZ.LO1	12		01: WZ.LO1
13		01: WT.TM1	13		01: WT.TM1
14		01: WT.SW2	14		01: WT.SW2
15		01: WZ.UP2	15		01: WZ.UP2
16		01: WZ.LO2	16		01: WZ.LO2
17		01: WT.TM2	17		01: WT.TM2
18		01: WT.SW3	18		01: WT.SW3
19		01: WZ.UP3	19		01: WZ.UP3
20		01: WZ.LO3	20		01: WZ.LO3
21		01: WT.TM3	21		01: WT.TM3
22		01: WT.SW4	22		01: WT.SW4
23		01: WZ.UP4	23		01: WZ.UP4
24		01: WZ.LO4	24		01: WZ.LO4
25		01: WT.TM4	25		01: WT.TM4
26		01: WT.SW5	26		01: WT.SW5
27		01: WZ.UP5	27		01: WZ.UP5
28		01: WZ.LO5	28		01: WZ.LO5
29		01: WT.TM5	29		01: WT.TM5
30		01: R.CYCL	30		01: R.CYCL
31		01: R.STRT	31		01: R.STRT
32		01: R.END	32		01: R.END
33		(Unused)	33		(Unused)
34		(Unused)	34		(Unused)
35		01: P.NAME	35		01: P.NAME
36		01: P.NAME	36		01: P.NAME
37		01: P.NAME	37		01: P.NAME

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
38		01: P.NAME		38		01: P.NAME
39		01: P.NAME		39		01: P.NAME
40		01: P.NAME		40		01: P.NAME
41		01: P.NAME		41		01: P.NAME
42		01: P.NAME		42		01: P.NAME
43		01: P.NAME		43		01: P.NAME
44		01: P.NAME		44		01: P.NAME
45		01: P.NAME		45		01: P.NAME
46		01: PTN.ERR		46		(Unused)
47		(Unused)		47		(Unused)
⋮		⋮		⋮		⋮
80		(Unused)		80		(Unused)

Page 4

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 4					
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)	• • •	• • •	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: PTNO._C	5		01: PTNO._C
6		01: SEGNO._C	6		01: SEGNO._C
7		01: TSP_L1	7		01: TSP_L1
8		(Unused)	8		(Unused)
9		01: TIME	9		01: TIME
10		01: TM.RT	10		01: TM.RT
11		01: S.PID	11		01: S.PID
12		01: JC	12		01: JC
13		01: PV.TY1	13		01: PV.TY1
14		01: PV.EV1	14		01: PV.EV1
15		01: PV.TY2	15		01: PV.TY2
16		01: PV.EV2	16		01: PV.EV2
17		01: PV.TY3	17		01: PV.TY3
18		01: PV.EV3	18		01: PV.EV3
19		01: PV.TY4	19		01: PV.TY4
20		01: PV.EV4	20		01: PV.EV4
21		01: PV.TY5	21		01: PV.TY5
22		01: PV.EV5	22		01: PV.EV5
23		01: PV.TY6	23		01: PV.TY6
24		01: PV.EV6	24		01: PV.EV6
25		01: PV.TY7	25		01: PV.TY7
26		01: PV.EV7	26		01: PV.EV7
27		01: PV.TY8	27		01: PV.TY8
28		01: PV.EV8	28		01: PV.EV8
29		01: TME1	29		01: TME1
30		01: T.ON1	30		01: T.ON1
31		01: T.OF1	31		01: T.OF1
32		01: TME2	32		01: TME2
33		01: T.ON2	33		01: T.ON2
34		01: T.OF2	34		01: T.OF2
35		01: TME3	35		01: TME3
36		01: T.ON3	36		01: T.ON3

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 4						
IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
37		01: T.OF3		37		01: T.OF3
38		01: TME4		38		01: TME4
39		01: T.ON4		39		01: T.ON4
40		01: T.OF4		40		01: T.OF4
41		01: TME5		41		01: TME5
42		01: T.ON5		42		01: T.ON5
43		01: T.OF5		43		01: T.OF5
44		01: TME6		44		01: TME6
45		01: T.ON6		45		01: T.ON6
46		01: T.OF6		46		01: T.OF6
47		01: TME7		47		01: TME7
48		01: T.ON7		48		01: T.ON7
49		01: T.OF7		49		01: T.OF7
50		01: TME8		50		01: TME8
51		01: T.ON8		51		01: T.ON8
52		01: T.OF8		52		01: T.OF8
53		01: TME9		53		01: TME9
54		01: T.ON9		54		01: T.ON9
55		01: T.OF9		55		01: T.OF9
56		01: TME10		56		01: TME10
57		01: T.ON10		57		01: T.ON10
58		01: T.OF10		58		01: T.OF10
59		01: TME11		59		01: TME11
60		01: T.ON11		60		01: T.ON11
61		01: T.OF11		61		01: T.OF11
62		01: TME12		62		01: TME12
63		01: T.ON12		63		01: T.ON12
64		01: T.OF12		64		01: T.OF12
65		01: TME13		65		01: TME13
66		01: T.ON13		66		01: T.ON13
67		01: T.OF13		67		01: T.OF13
68		01: TME14		68		01: TME14
69		01: T.ON14		69		01: T.ON14
70		01: T.OF14		70		01: T.OF14
71		01: TME15		71		01: TME15
72		01: T.ON15		72		01: T.ON15
73		01: T.OF15		73		01: T.OF15
74		01: TME16		74		01: TME16
75		01: T.ON16		75		01: T.ON16
76		01: T.OF16		76		01: T.OF16
77		01: PTN.ERR		77		(Unused)
78		(Unused)		78		(Unused)
79		(Unused)		79		(Unused)
80		(Unused)		80		(Unused)

3.10 Changing Automatic Rescan Time (SCAN in PROF/DNET Menu)

When the automatic rescan time setting is changed, UTAdvanced will perform operation in the following ways.

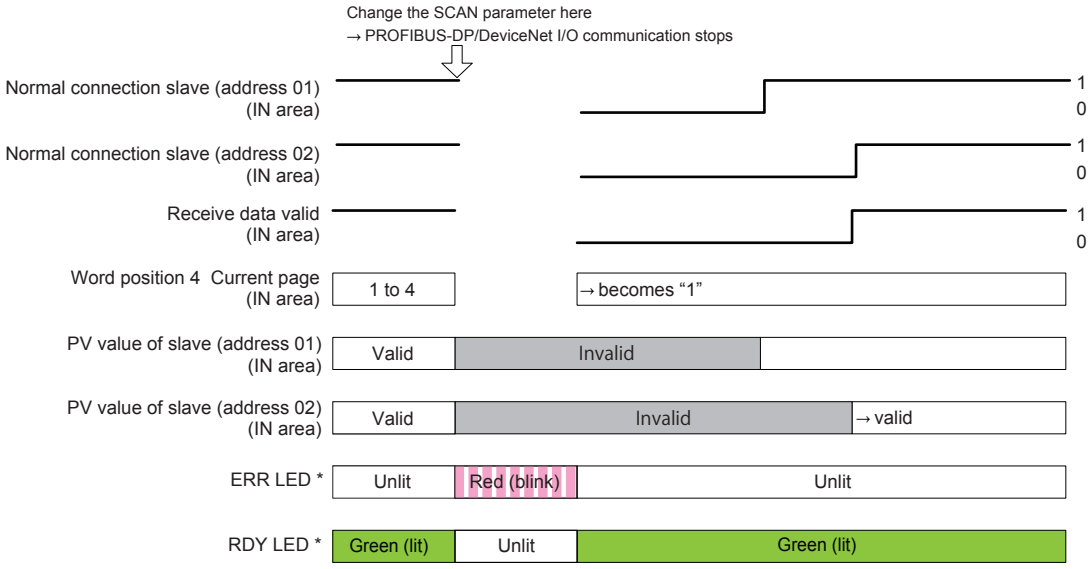
(1) Stops the PROFIBUS-DP/DeviceNet I/O communication.
(2) Sets the timer according to the SCAN parameter value.
(3) Restarts the PROFIBUS-DP/DeviceNet I/O communication.
(4) Sets the current page of the profile to 1 and restarts Modbus communication.

The automatic rescan time is set by the SCAN parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

► Setting SCAN parameters: “2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)” or “2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)”in this manual

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual

Example of connecting 2 slaves (address 01 and 02):



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.11 Changing Profile Number (FILE in PROF/DNET Menu)

The profile number is set by the FILE parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

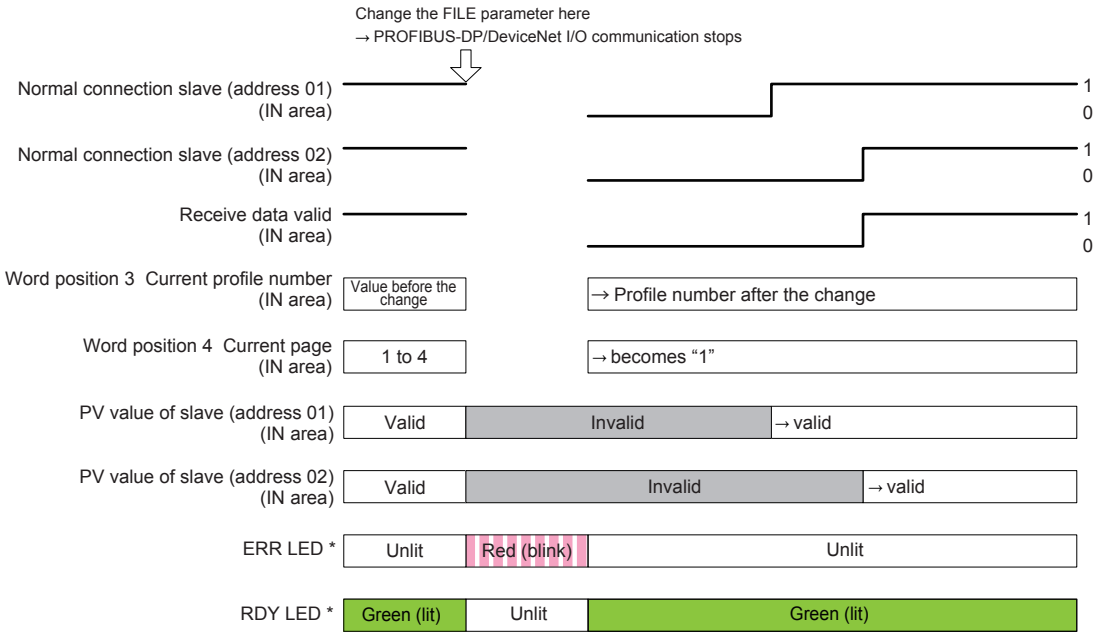
► Setting SCAN parameters: “2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)” or “2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)”in this manual

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual

When the profile number is changed, the PROFIBUS-DP/DeviceNet I/O size changes. If the configured profile number (I/O size) matches the PROFIBUS-DP/DeviceNet master, a connection can be established with the PROFIBUS-DP/DeviceNet master after the change is made. If it does not match, a connection cannot be established.

The following figure shows a case where the I/O size does not change after the profile number is changed. Since the I/O size usually changes, the PROFIBUS-DP/DeviceNet communication is disconnected either before or after, or both before and after the change (ERR LED*: blinking red).
*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

Example of connecting 2 slaves (address 01 and 02):



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.12 Changing RS-485 Baud Rate (BPS in PROF/DENT Menu)

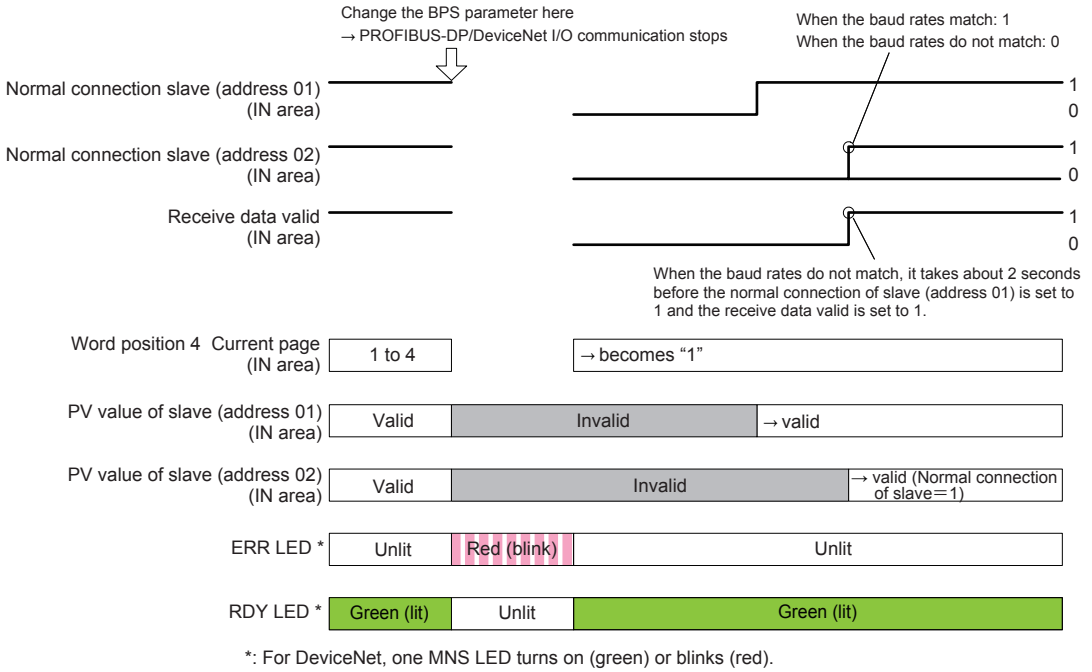
Make sure that the RS-485 baud rates of the Modbus master and Modbus slaves are identical.

The RS-485 baud rate of the Modbus master is set by the BPS parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

► Setting SCAN parameters: “2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)” or “2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)”in this manual

► 3.1 Overview: “Example: PROFIBUS-DP/DeviceNet Communication Connection” in this manual

Example of connecting 2 slaves address 01 and 02:



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.13 PLC Memory Space

UTAdvanced that serves a PROFIBUS-DP/DeviceNet slave occupies area of the memory space of the PROFIBUS-DP/DeviceNet master. The user needs to know from the perspective of a PLC where the data of UTAdvanced is assigned in the memory space. Be careful because the size of the occupied memory space of the master varies depending on the profile number of UTAdvanced that serves as a PROFIBUS-DP/DeviceNet slave.

Blank Page

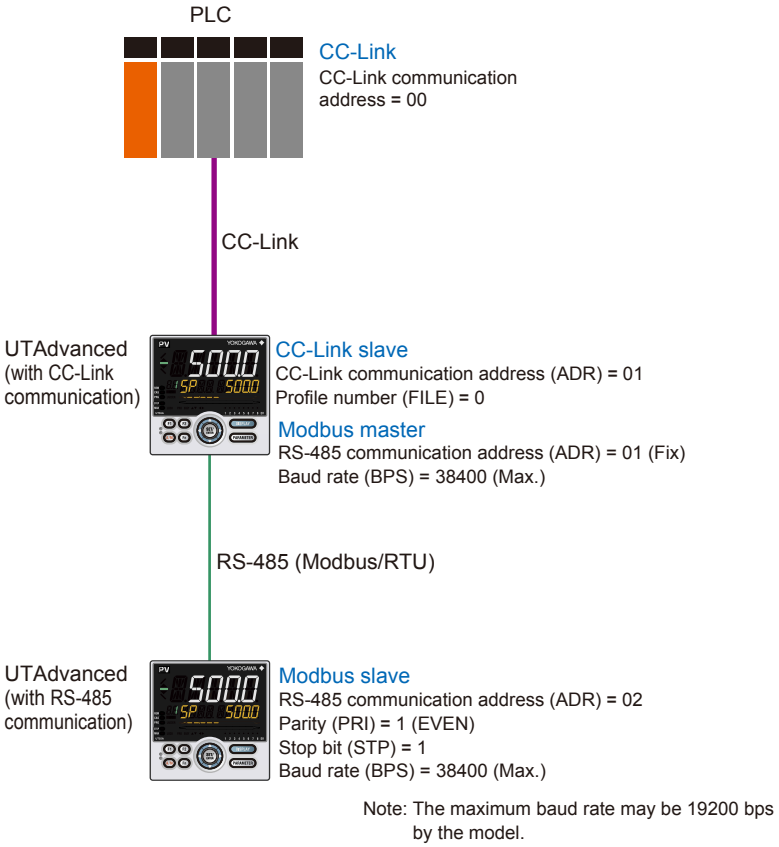
4.1 Overview

CC-Link is an open field bus used in various applications for factory automation and process automation.
CC-Link is used for communication between PLCs and remote I/O, enabling high-speed data transmission.

Note

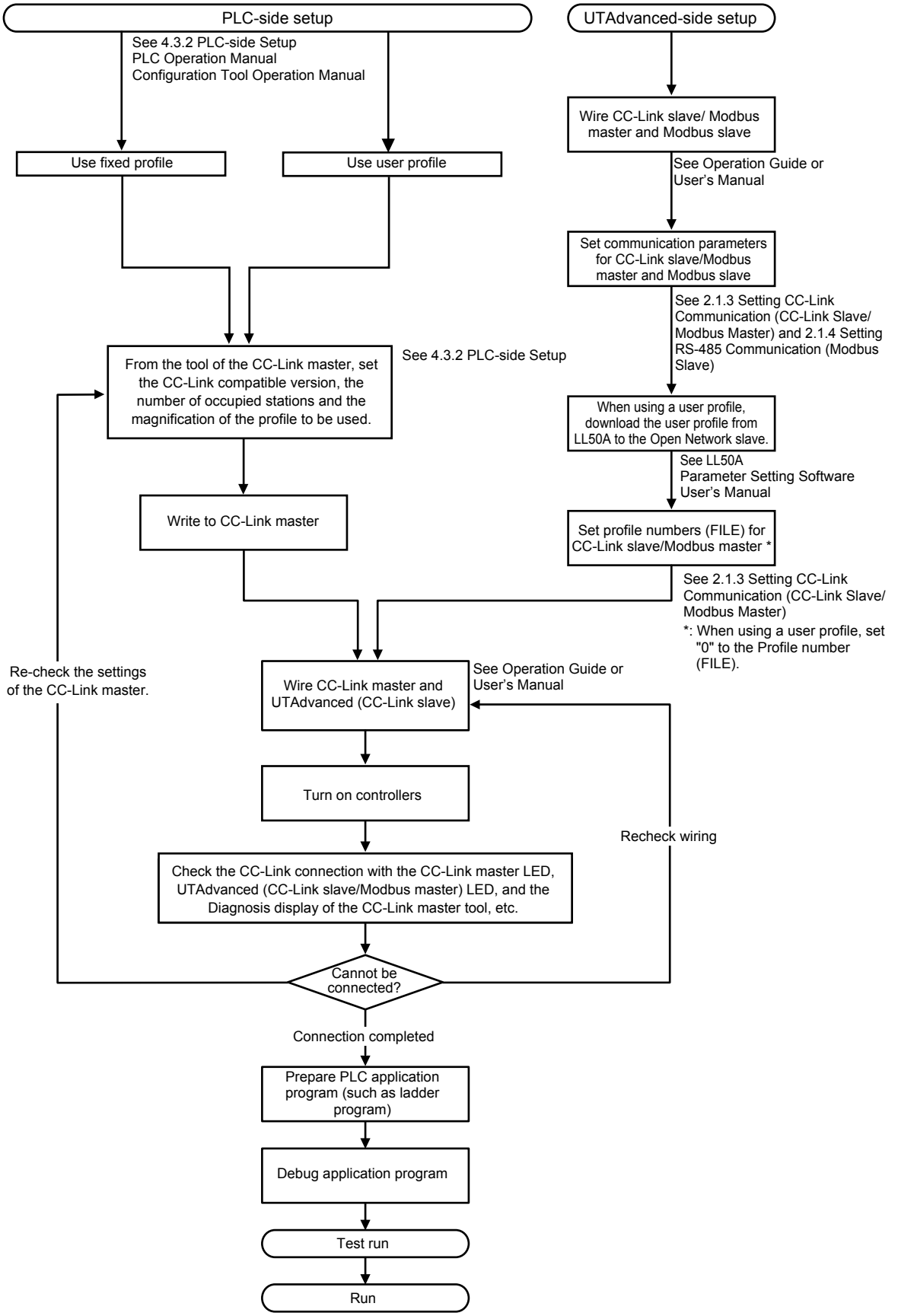
For details of CC-Link specifications and information, see the documents published from the CC-Link Partner Association in respective regions.
CC-Link Partner Association: http://www.CC-Link.org/

Example: CC-Link Communication Connection



Note: The maximum baud rate may be 19200 bps by the model.

4.2 Workflow


PLC-side setup
See 4.3.2 PLC-side Setup
PLC Operation Manual
Configuration Tool Operation Manual
Use fixed profile
Use user profile
From the tool of the CC-Link master, set the CC-Link compatible version, the number of occupied stations and the magnification of the profile to be used.
See 4.3.2 PLC-side Setup
Write to CC-Link master
UTAdvanced-side setup
Wire CC-Link slave/ Modbus master and Modbus slave
See Operation Guide or User's Manual
Set communication parameters for CC-Link slave/Modbus master and Modbus slave
See 2.1.3 Setting CC-Link Communication (CC-Link Slave/ Modbus Master) and 2.1.4 Setting RS-485 Communication (Modbus Slave)
When using a user profile, download the user profile from LL50A to the Open Network slave.
See LL50A Parameter Setting Software User's Manual
Set profile numbers (FILE) for CC-Link slave/Modbus master *
See 2.1.3 Setting CC-Link Communication (CC-Link Slave/ Modbus Master)
*: When using a user profile, set "0" to the Profile number (FILE).
Re-check the settings of the CC-Link master.
Wire CC-Link master and UTAdvanced (CC-Link slave)
See Operation Guide or User's Manual
Turn on controllers
Recheck wiring
Check the CC-Link connection with the CC-Link master LED, UTAdvanced (CC-Link slave/Modbus master) LED, and the Diagnosis display of the CC-Link master tool, etc.
Cannot be connected?
Connection completed
Prepare PLC application program (such as ladder program)
Debug application program
Test run
Run

4.3 Setting Up Connection with Master

4.3.1 UTAdvanced-side Setup

Wiring

For wiring, see UTAdvanced Operation Guide or User's Manual.

Setting communication parameters

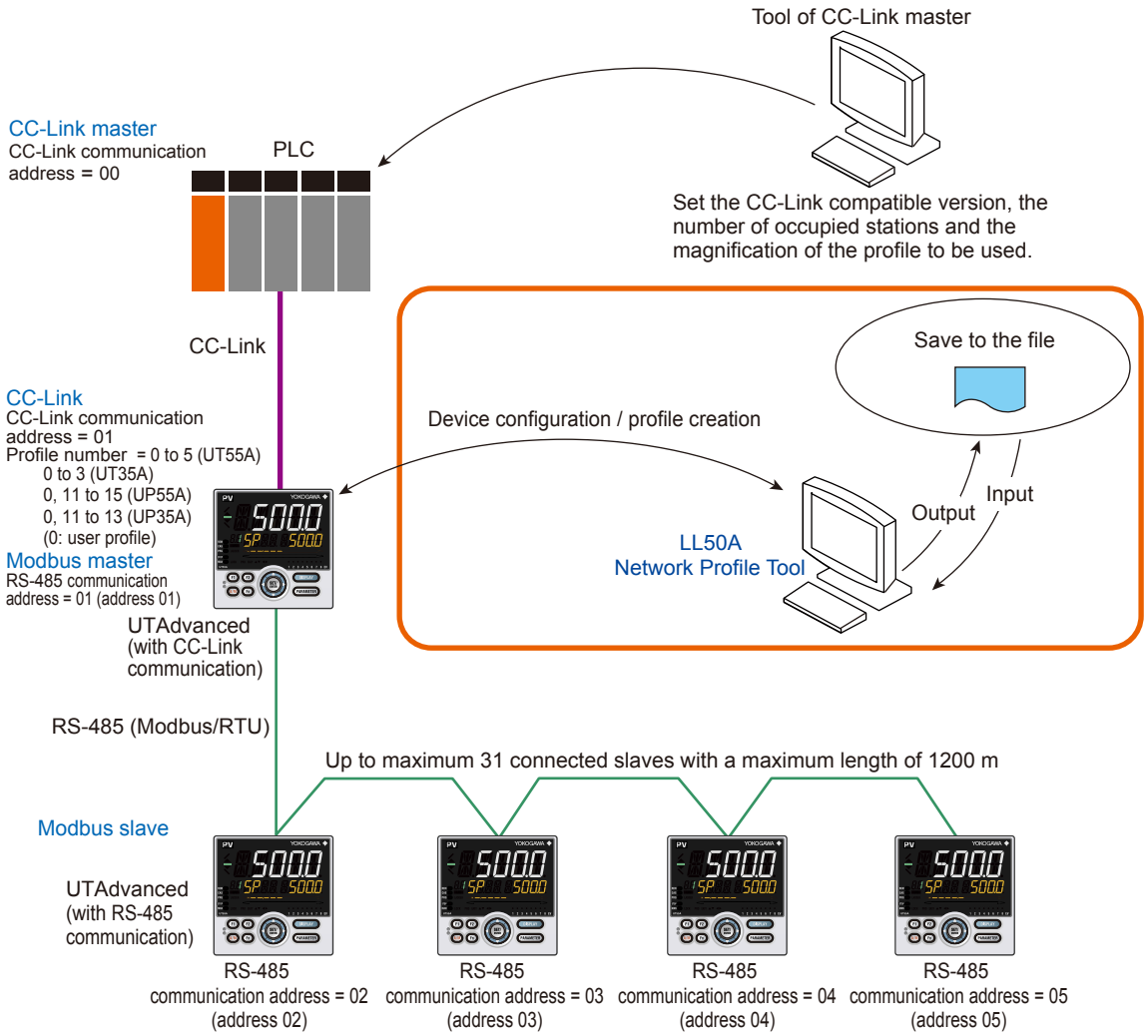
For setting parameters, see 2.1.2 and 2.1.3 of this manual.

Downloading User Profile

When using a user profile, download the user profile via LL50A.
For the procedure of download, see LL50A Parameter Setting Software User's Manual.

4.3.2 PLC-side Setup

Set the CC-Link compatible version, the number of occupied stations and the magnification of the profile to be used.



4.4 Profile

4.4.1 Contents of Profile

The UT55A/UT35A fixed profile contains 3-station occupation and 4-station occupation for Version 1.10 of the CC-Link master and 1-station occupation and 2-station occupation in the x8 setting for Version 2.00.
The UP55A/UP35A fixed profile contains 3-station occupation and 4-station occupation for Version 1.10 of the CC-Link master and 3-station occupation in the x8 setting and 2-station occupation in the x4 setting for Version 2.00.

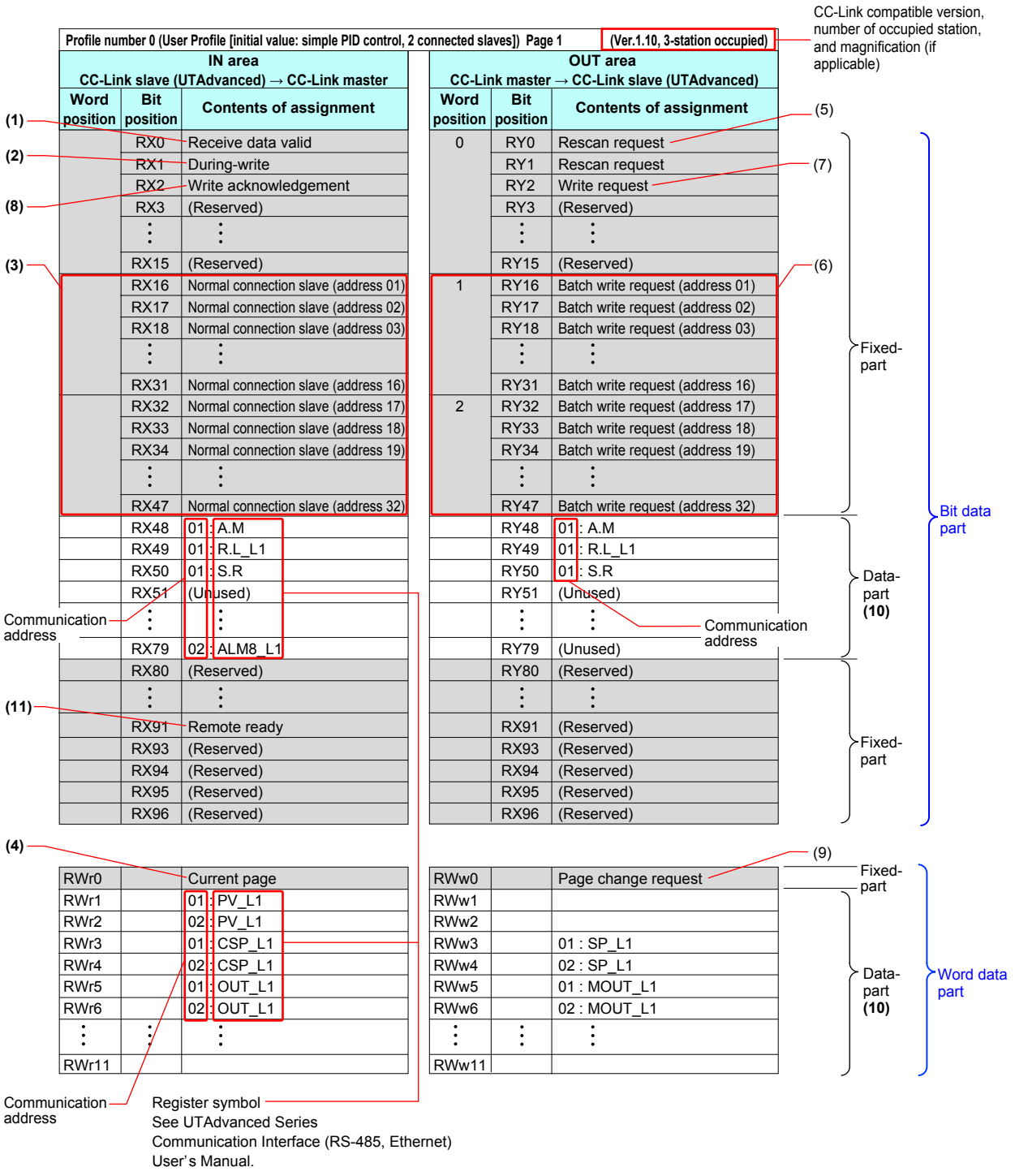
► “4.4.2 Type of Profile” in this manual

The profile contains a bit data area and a word data area, both of which consists of a predefined fixed area and a data area to which a parameter is assigned.
Flags to switch pages of the data-part and flags to indicate the connection status of controllers are assigned to the fixed-part.
The data-part can be used by switching pages. The number of pages of a profile is 4 (1 to 4).
When creating a user profile with LL50A Network Profile Tool, the settings can be selected from the combination of the followings: Ver 1.10, Ver 2.00, 1-station to 4-station occupation, and x1 to x8 speed.

Note

Parameters are classified into each page of a profile, i.e. the profile is classified into pages of the more frequently used parameters for routine operation and the less frequently used parameters for startup and batch-start.
Classification for each page allows reducing the memory space occupied in the master. Furthermore, it allows optimizing the updating of the data (in the IN area) read from UTAdvanced.

Example: Profile number 0 (Page 1)



Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid	0	RY0	Rescan request
	RX1	During-write		RY1	Rescan request
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	⋮	⋮		⋮	⋮
	RX15	(Reserved)		RY15	(Reserved)
	RX16	Normal connection slave (address 01)	1	RY16	Batch write request (address 01)
	RX17	Normal connection slave (address 02)		RY17	Batch write request (address 02)
	RX18	Normal connection slave (address 03)		RY18	Batch write request (address 03)
	⋮	⋮		⋮	⋮
	RX31	Normal connection slave (address 16)		RY31	Batch write request (address 16)
	RX32	Normal connection slave (address 17)	2	RY32	Batch write request (address 17)
	RX33	Normal connection slave (address 18)		RY33	Batch write request (address 18)
	RX34	Normal connection slave (address 19)		RY34	Batch write request (address 19)
	⋮	⋮		⋮	⋮
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01 : A.M		RY48	01 : A.M
	RX49	01 : R.L_L1		RY49	01 : R.L_L1
	RX50	01 : S.R		RY50	01 : S.R
	RX51	(Unused)		RY51	(Unused)
	⋮	⋮		⋮	⋮
	RX79	02 : ALM8_L1		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮	⋮		⋮	⋮
	RX91	Remote ready		RX91	(Reserved)
	RX93	(Reserved)		RX93	(Reserved)
	RX94	(Reserved)		RX94	(Reserved)
	RX95	(Reserved)		RX95	(Reserved)
	RX96	(Reserved)		RX96	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01 : PV_L1	RWw1		
RWr2		02 : PV_L1	RWw2		
RWr3		01 : CSP_L1	RWw3		01 : SP_L1
RWr4		02 : CSP_L1	RWw4		02 : SP_L1
RWr5		01 : OUT_L1	RWw5		01 : MOUT_L1
RWr6		02 : OUT_L1	RWw6		02 : MOUT_L1
⋮	⋮	⋮	⋮	⋮	⋮
RWr11			RWw11		

(1) Receive data valid flag (1: valid)
This flag allows checking whether the data in the IN area is valid.
If the flag is set to 1, the data in the IN area is valid. (However, this only applies to the slave data where the normal connection slave flag (address 01 to 32) is also set to 1.)
When the power is turned on or a rescan is requested, the flag is set to 0. When checking whether all slaves registered in the profile are ready for communication is finished, the flag is set to 1.

(2) During-write flag (0: write enable)
This flag allows checking whether writing to the OUT area is enabled.
If the flag is set to 0, writing to the OUT area is enabled.
When the power is turned on, the flag is set to 0. When the write communication is performed, the flag is set to 1. When a response is returned from the slave, or when the time is up, the flag returns to 0.

Note

When the flag is set to 1, a write request is not accepted and is ignored (not held).

(3) Normal connection slave flag (Address 01 to 32) (1: connected)
This flag allows checking whether each slave is connected.
The normal connection of slave flags have 32 bits in the fixed-part of the IN area.
Slave (address 01) in word position 1, and bit position 0 is UTAdvanced that runs as a CC-Link slave.

Note

If normal connection slave (address 01) is set to 0 in a profile in which the CC-Link slave (address 01) is registered, and rescan does not cause the flag to return to 1, it is a failure.

Word position 1 and bit positions RX16 to RX47, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32.
When slaves (address 01 to 32) are connected, each flag is set to 1. When the power is turned on, the flag is set to 0, and when communication becomes enabled, the flag is set to 1. When communication is disabled, the flag is set to 0. When communication becomes enabled upon a rescan request, the flag is set to 1.

(4) Current pager
The currently used profile page number (1 to 4) is displayed.

(5) Rescan request flag
A rescan request is made to attempt a retry to establish communication with unconnected slaves. (When connection is normally established with all slave controllers registered in the profile, a rescan is not performed.)
Change the flag in RY0 of the OUT area from 0 to 1. Thereafter, return it to 0 when the receive data valid flag has been set to 1.

(6) Write request flag (address 01 to 32)
This flag allows writing all parameters that are assigned to the OUT area together to the slave for each communication address.
Write request flags have 32 bits in the fixed-part of the OUT area. Slave (address 01) in RY16 is UTAdvanced that runs as a CC-Link slave.
RY16 to RY14, correspond to the Modbus slaves with communication addresses 2 to 32.
Change the write request flags from 0 to 1 for the slaves (address 01 to 32) to be written, while the during-write flag is set to 0. Thereafter, return them to 0 when the writing process is completed.

(7) Write request flag and (8) Write acknowledgment flag
When a write request is made, regardless of whether the writing is performed individually or all together, both the write acknowledgement flag and write request flag need to be used to reliably recognize that the writing is completed.
When the write request flag is set to 1, while the during-write flag and write acknowledgement flag are set to 0, the write acknowledgment flag is set to 1.
Set the data-part of the OUT area when the write acknowledgment flag is set to 1.
Thereafter, returning the write request flag to 0 prompts the writing to be performed.
When the writing is completed, the write acknowledgment flag is set to 0.

► "4.6 Reading and Writing UTAdvanced Data" in this manual

(9) Page change request
This request switches the currently used profile page.
Set the value in OUT area RWw0 to a value (any of 1 to 4) that is different from the current page (in IN area RWr0).
The receive data valid flag remains set to 0 until the page is switched upon the page change request and the data acquisition is completed.

► "4.7 Switching Pages" in this manual

(10) Data-part
The data format is the same as that of the displayed value of UTAdvanced.

(11) Remote ready (1: valid)
The function and operation are same as those of the receive data valid flag.
The bit position varies depending on the profile.

4.4.2 Types of Profile

UT55A provides one user profile and 5 fixed profiles.
UT35A provides one user profile and 3 fixed profiles.
Set each profile numbers according to the configurations.
Profile numbers can be set with the FILE parameter in the CC-Link Communication Settings menu (CC-L).

► Setting FILE parameters: “2.1.3 Setting CC-Link Communication (CC-Link Slave/Modbus Master)” in this manual

Example: UT55A/UT35A

For a simple PID control with 6 connected controllers, use “Profile number 3: 8 simple PID controllers”.
For a simple PID control with 10 connected controllers, set the connection devices using “Profile number 0: User profile” with Network Profile Tool of LL50A.

UT55A/UT35A

Profile number	Name	Page number	Item	CC-Link version Number of occupied stations and magnification I/O size (RX/RY: bit, RWr/RWw: word)	Applicable control mode and control type	
					Control mode	Control type
0	User profile (Initial value: Simple PID Control, 2 connected slaves)	1	Process value, operation mode, alarm status	Ver.1.10 3-station occupation 96/96, 12/12	All modes except for Cascade Control (4: CAS)	All type
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
1	Simple PID Control, 3 connected slaves	1	Process value, operation mode, alarm status	Ver.1.10 4-station occupation 128/128, 16/16		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
2	Simple PID Control, 5 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 1-station occupation x8 setting 128/128, 32/32		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
3	Simple PID Control, 8 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 2-station occupation x8 setting 384/384, 64/64		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
4	Cascade Control, 3 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 1-station occupation x8 setting 128/128, 32/32	Cascade Control (4: CAS)	
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			
5	Cascade Control, 5 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 2-station occupation x8 setting 384/384, 64/64		
		2	PID parameter			
		3	Heating/cooling PID parameter			
		4	Alarm setpoint			

UP55A/UP35A

Profile number	Name	Page number	Item	CC-Link version Number of occupied stations and magnification I/O size (RX/RY: bit, RWr/RWw: word)	Applicable control mode and control type	
					Control mode	Control type
0	User profile (Initial value: Simple PID Control, 2 connected slaves)	1	Process value, operation mode, alarm status	Ver.1.10 3-station occupation 96/96, 12/12	All modes except for Cascade Control (4: CAS)	All type
		2	PID parameter (for address 1)			
		3	PID parameter (for address 2)			
		4	Local event-1 to -2 setpoint (for address 1, 2)			
11	Simple PID Control, 2 connected slaves	1	Process value, operation mode, alarm status	Ver.1.10 4-station occupation 128/128, 16/16		
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1)			
		4	Local event-1 to -7 setpoint (for address 2)			
12	Simple PID Control, 4 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 2-station occupation x4 setting 192/192, 32/32		
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1, 2)			
		4	Local event-1 to -7 setpoint (for address 3, 4)			
13	Simple PID Control, 1 connected slave (with program pattern setting)	1	Process value, operation mode, alarm status	Ver.2.00 3-station occupation x8 setting 640/640, 96/96		
		2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance			
		3	Pattern setting			
		4	Segment setting			
14	Cascade Control, 2 connected slaves	1	Process value, operation mode, alarm status	Ver.2.00 2-station occupation x4 setting 192/192, 32/32	Cascade Control (4: CAS)	
		2	PID parameter, Alarm setpoint			
		3	Local event-1 to -7 setpoint (for address 1, 2)			
		4	Local event-1 to -7 setpoint (for address 3, 4)			
15	Cascade Control, 1 connected slave (with program pattern setting)	1	Process value, operation mode, alarm status	Ver.2.00 3-station occupation x8 setting 640/640, 96/96		
		2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance			
		3	Pattern setting			
		4	Segment setting			

User profile

As the default, a parameter for a simple PID control with 2 connected controllers is set. Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.

► LL50A Parameter Setting Software User's Manual

Fixed profile

UT55A/UT35A:

Parameters for a simple PID control with 3, 5, and 8 connected controllers and for a cascade control with 3 and 5 connected controllers are set.

UP55A/UP35A:

Parameters for a simple PID control with 2, 4, and 1 (with program setting function) connected controllers and for a cascade control with 2 and 1 (with program setting function) connected controllers are set.

However, the parameters for a cascade control (Profile numbers: 4 and 5) cannot be used for UT35A, and the parameters for a cascade control (Profile numbers: 14 and 15) cannot be used for UP35A.

4.5 Operation at the Time of Power-On

The following shows how the IN area of UTAdvanced looks like from the perspective of a PLC when UTAdvanced is turned on while the PLC power is already on.

Note

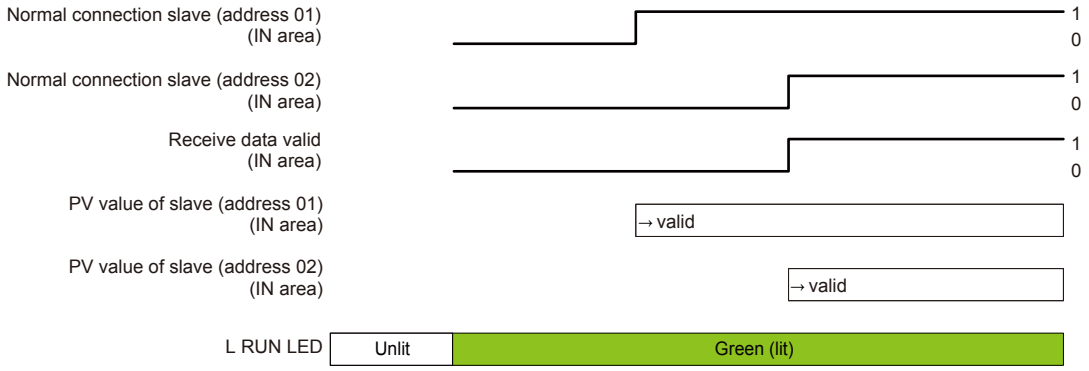
The UTAdvanced data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("→valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1.

► 4.1 Overview: "Example: CC-Link Communication Connection" in this manual

► "4.9 Profile List" in this manual

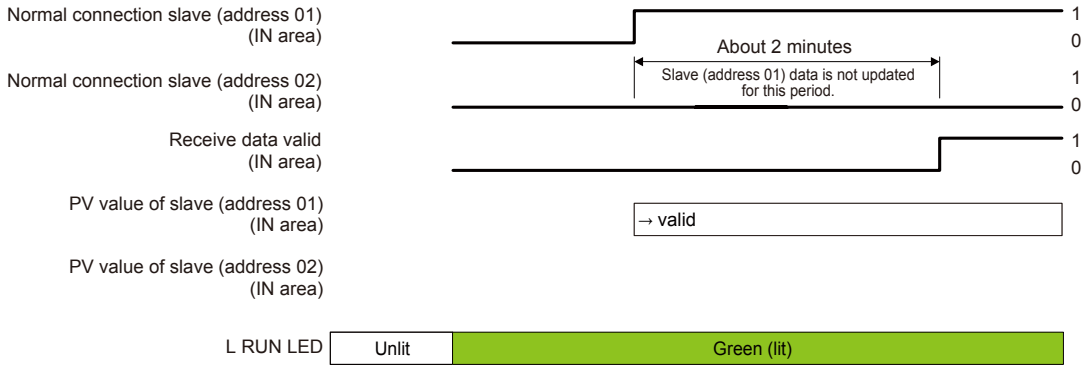
4.5.1 Example at the Time of Power-On

Example of connecting 2 slaves (address 01 and 02):



4.5.2 Example at the Time of Power-On (When Slave (address 02) is not Connected)

Example where slave (address 01) is connected, but slave (address 02) is not connected:



4.6 Reading and Writing UTAdvanced Data

► 4.1 Overview: “Example: CC-Link Communication Connection” in this manual
► “4.9 Profile List” in this manual

4.6.1 Reading

Data in the IN area that is always updated can be read.

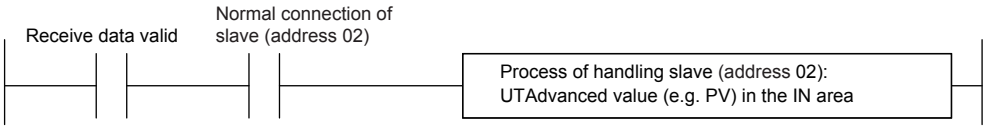
Procedure

1. Check that the receive data valid flag is set to **1**.
2. Check that the normal connection slave flag for a slave to be handled (address 01 to 32) is set to **1**.
3. Data for the corresponding slave (address 01 to 32) in the IN area can be handled.

Note

If both the receive data valid flag and normal connection slave flag are set to 1, the data in the IN area is valid.

Example of ladder program



4.6.2 Writing Individual Parameters

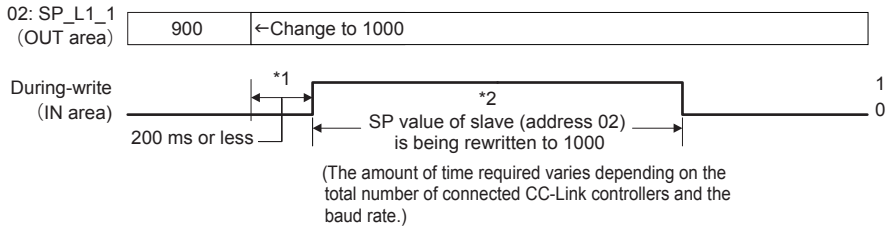
Only the parameter values to be changed in the OUT area can be written.

■ **Simple procedure of writing individual parameters used when the write interval is long**

Procedure

1. Check that the during-write flag is set to **0**.
2. Change the value in the OUT area to which the parameter to be written is assigned.

Example of rewriting the SP value for slave (address 02):



Note

*1 in the figure above

- If the write value is changed multiple times during the period*1, the last write value is valid.

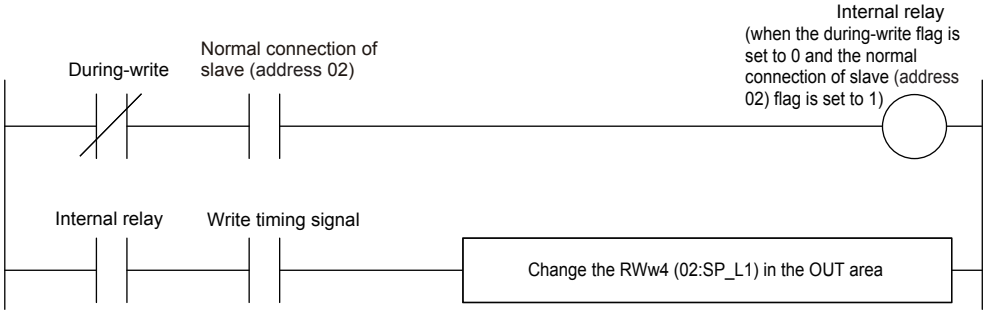
*2 in the figure above

- If the during-write flag is set to 1, a changed value in the OUT area is invalid. The change of the value is ignored (not held).
- Changing a value in the OUT area results in a request for writing the individual parameter.
- UTAdvanced holds the previous values in the OUT area in order to detect changes in the values in the OUT area. The previous values in the OUT area are set to 0 when the power is turned on, or when CC-Link is disconnected. If a value other than 0 is written in the OUT area of a PLC when the disconnected CC-Link is connected, UTAdvanced handles it as a request for writing the individual parameter.
  If you want to write 0 first after the disconnected CC-Link is connected, you need to use batch writing. Furthermore, the first writing after the power is turned on needs to use batch writing.
  ► Batch writing: “4.6.3 Batch writing for Each Communication Address” of this manual
- If the CC-Link baud rate is slow or the PLC scan cycle is long, the PLC program may be unable to detect that the during-write flag in the IN area has been set to 1. In order to reliably detect that the writing is completed, individual parameters need to be written using both the write request flag in the OUT area and the write acknowledgement flag in the OUT area (Procedure of reliably detecting that the writing is completed).

Note

For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User’s Manual.

Example of ladder program

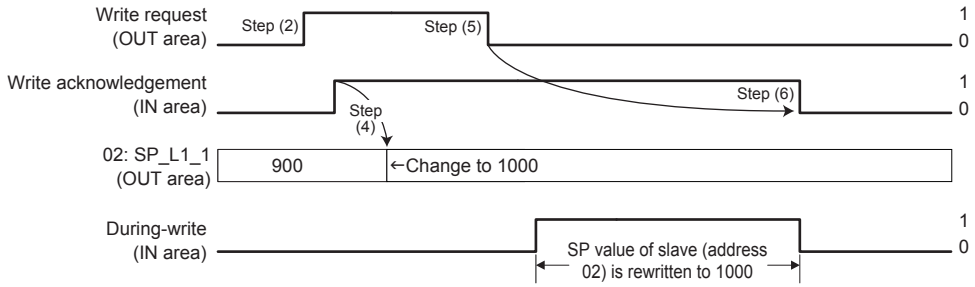


■ Writing individual parameters (Procedure to reliably detect that the writing is completed)

Procedure

1. Check that the during-write flag is set to **0**.
2. Change the write request flag from 0 to **1**.
3. Check that the write acknowledgment flag has been set to **1**.
4. Change the value in the OUT area to which the parameter to be written is assigned.
5. Return the write request flag from 1 to **0** (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that of step 4.
6. When the write acknowledgment flag has been set to 0, the writing is completed.

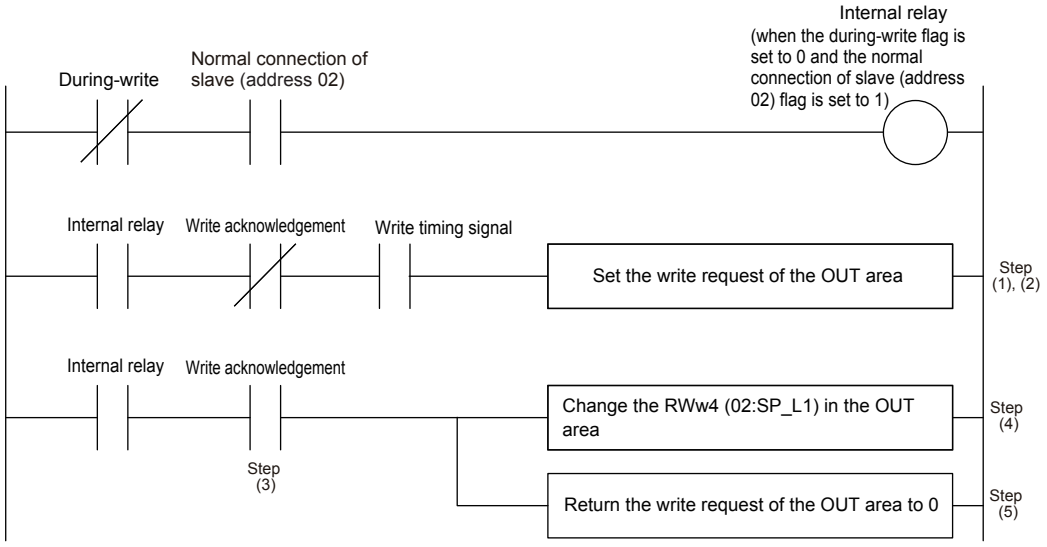
Example of rewriting the SP value of slave (address 02):



Note

- Changing the value in the OUT area results in a request for writing the individual parameter. If you want to write the current values in the OUT area, use batch writing.
- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



4.6.3 Batch Writing for Each Communication Address

Batch writing can be performed on parameter values assigned to the OUT area for each communication address.

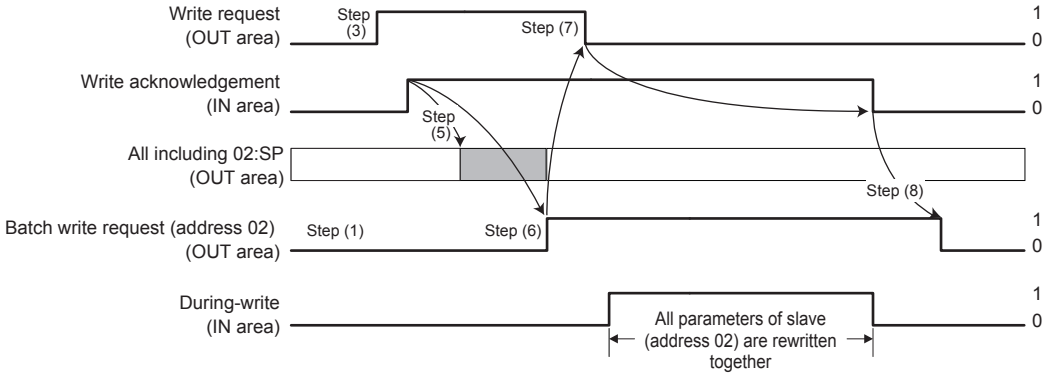
Procedure

1. Set the write request flag for the slaves to be written (address 01 to 32) to 0.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Set the values in the OUT area to which the parameters to be written are assigned. (The same values as the previously written values can also be written.)
6. Change the write request flag for the slaves to be written (address 01 to 32) from 0 to **1**.
7. Return the write request flag from 1 to **0** (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that in steps 5 and 6.
8. When the write acknowledgment flag has been set to 0, the writing is completed. Return the write request flag from 1 to **0**.

Note

- Batch writing writes the values in the OUT area at the point of step (7).
- Changing the write request flag from 0 to 1 (step 6) needs to be performed when the during-write flag is set to 0 and the write acknowledgment flag is set to 1. If these conditions are not met, the write request is invalid.

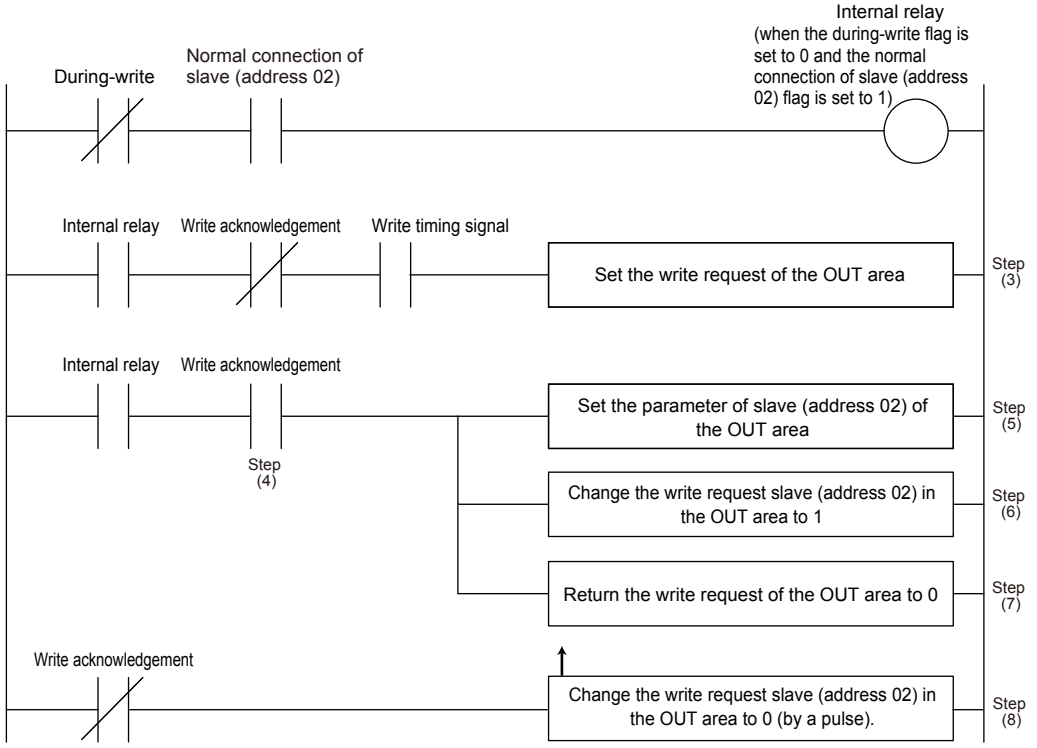
Example of changing the write request flag for slave (address 02) from 0 to 1:



Note

- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.
- When the CC-Link baud rate is slow, or the scan cycle of a PLC is long, the PLC program may be unable to detect that the during-write flag has been set to 0.

Example of ladder program



4.6.4 Reading Program Pattern

Procedure

1. Check that the receive data valid flag and the normal connection slave flag are set to **1**.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
6. Return the write request flag from 1 to **0**.
7. Check that the write acknowledgment flag has been set to **0**.
8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then confirm that is "0".
9. Check that the during-write flag is set to **0**.
10. Change the write request flag from 0 to **1**.
11. Check that the write acknowledgment flag has been set to **1**.
12. Write the required pattern number and the segment number to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
13. Return the write request flag from 1 to **0**.
14. Check that the write acknowledgment flag has been set to **0**.
15. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), the Segment number designation (SEGNO._C) and the Read/write error information (PTN.ERR).

    Confirm that the required pattern number and the segment number are set to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then the Read/write error information (PTN.ERR) has been set to "0".
16. Read the data corresponding to the segment parameters in the IN area; from the Final target setpoint (TSP_L1) to the Off time of time event 16 (T.OF16).

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

4.6.5 Writing Program Pattern

Procedure

1. Check that the receive data valid flag and the normal connection slave flag are set to **1**.
2. Check that the during-write flag is set to **0**.
3. Change the write request flag from 0 to **1**.
4. Check that the write acknowledgment flag has been set to **1**.
5. Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
6. Return the write request flag from 1 to **0**.
7. Check that the write acknowledgment flag has been set to **0**.
8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), and confirm that is "0".
9. Check that the during-write flag is set to **0**.
10. Change the write request flag from 0 to **1**.
11. Check that the write acknowledgment flag has been set to **1**.
12. Write the required pattern number and pattern data to the OUT area corresponding to the Program pattern number selection (PTNO._C) and Starting target setpoint (SSP_L1) to Program pattern name (P.NAME).
13. Return the write request flag from 0 to **1**.
14. Return the write request flag from 1 to **0**.
15. Check that the write acknowledgment flag has been set to **0**.
16. Return the write request flag from 1 to **0**.
17. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR).

    Confirm that the required pattern number is set to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR) has been set to "0".

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

4.7 Switching Pages

Pages can be switched by changing the value for the page change request (in RWw0 of the OUT area fixed-part).

► 4.1 Overview: “Example: CC-Link Communication Connection” in this manual
► “4.9 Profile List” in this manual

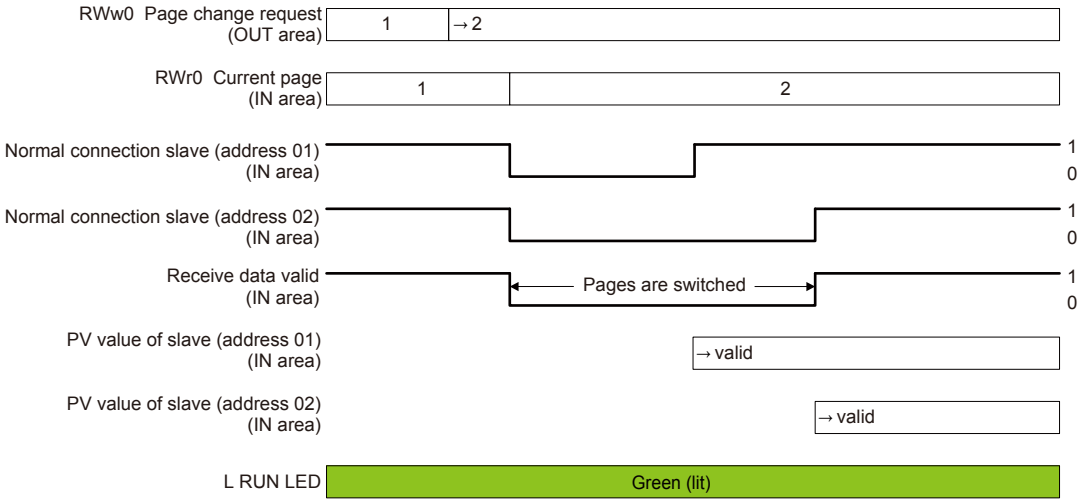
Procedure

1. Check that the during-write flag is set to **0**.
2. Change the value for the page change request (in RWw0 of the OUT area fixed-part) to a value (any of 1 to 4) that is different from the current page (in RWw0 of the IN area). The pages will be switched.

Note

- The page change request is accepted even when the during-write flag is set to 1. However, the page is actually changed when the writing is completed.
- CC-Link communication remains connected during the period when the page is being changed.
- The data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 (“→valid” in the figure below). However, it is recommended to handle them after the receive data valid flag has been set to 1.
- The value for the page change request needs to be held without change for 200 ms or longer. It is recommended that the next page change request is made after the receive data valid flag has been changed from 0 to 1.

Change of the flag when the page is switched from 1 to 2 when 2 slaves (address 01 and 02) are connected:



Checking page

The current page can be checked with the RWr0 of the IN area fixed-part.

4.8 Request for Rescanning

UTAdvanced that runs as a Modbus master attempts to establish communication with Modbus slaves registered in the profile, and if it cannot connect to a slave because of a wiring error or inconsistency in the communication conditions, it gives up the attempt to establish communication with that slave from the next time. It reduces the update cycle of the read data by reducing the time of communication with slaves that are disabled for communication.
A request for rescanning is made to attempt to start communication with slaves that were disabled for communication after errors with the wiring and communication conditions are fixed.
There are two types of request for rescanning: one type of request is made as needed, and the other is made at a constant frequency (automatic rescan time in SCAN). This section describes the type of rescan request that is made as needed.

► 4.1 Overview: “Example: CC-Link Communication Connection” in this manual
► “4.9 Profile List” in this manual

Procedure

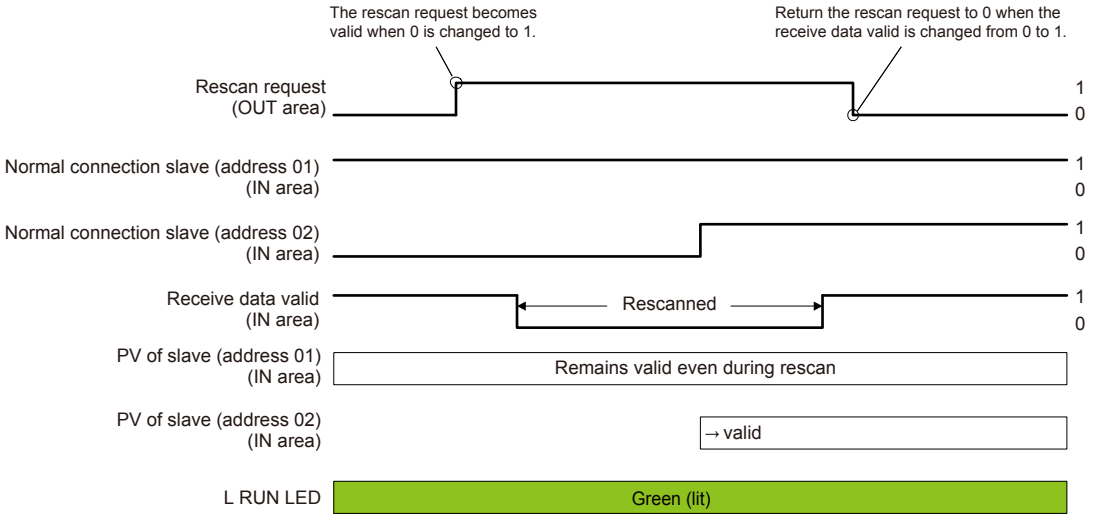
1. Change the rescan request flag (in RY0 of the OUT area fixed-part) from 0 to **1**. Rescanning starts.
2. Return the rescan request flag from 1 to **0**.

Note

- A request for rescanning is accepted even when the during-write flag is set to 0. However, the rescan request process is actually performed after the writing is completed.
- The data and write request in the IN area become valid when the normal connection flag for each salve is set to 1 (“→valid” in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1. This is why if there are slaves to which connection cannot be established, the updating of the data of the salves to which connection can be established will be delayed by a time equaling the number of unconnected slaves multiplied by about 2 seconds. If there are many slaves that cannot be connected, it is recommended for the same reason to use the automatic rescan function.
- 0 of the rescan request flag needs to be held for 200 ms or longer before it is set to 1. Furthermore, after it is set to 1, 1 needs to be held for 200 ms or longer before it is set to 0. It is recommended to return the rescan request flag to 0 after the receive data valid flag is changed from 0 to 1.
- The rescan operation is performed on slaves that are not connected. If connection is normally established with all slaves registered in the profile, the receive data valid flag remains set to 1 even when a rescan request is made.

► “4.10 Changing Automatic Rescan Time (SCAN in CC-L Menu)” in this manual

The operation of each flag when slave (address 01) is connected and slave (address 02) is not connected, and the rescan request flag is changed from 0 to 1 in order to establish a connection with the slave (address 02):



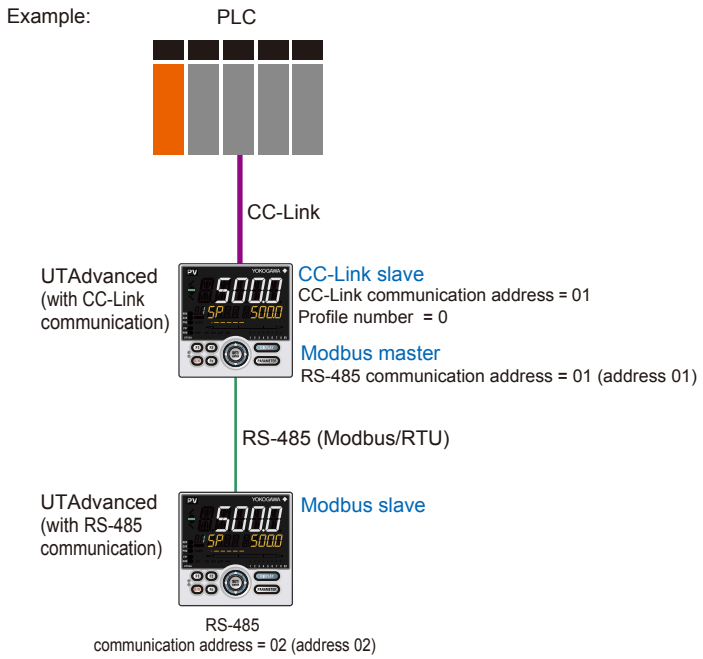
4.9 Profile List

For how to read the profile, see “4.4 Profile.”

4.9.1 Profile List for UT55A/UT35A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) (Ver.1.10, 3-station occupied)

UT55A UT35A



Page 1

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	RX8	(Reserved)		RY8	(Reserved)
	RX9	(Reserved)		RY9	(Reserved)
	RX10	(Reserved)		RY10	(Reserved)
	RX11	(Reserved)		RY11	(Reserved)
	RX12	(Reserved)		RY12	(Reserved)
	RX13	(Reserved)		RY13	(Reserved)
	RX14	(Reserved)		RY14	(Reserved)
	RX15	(Reserved)		RY15	(Reserved)
	RX16	Normal connection slave (address 01)		RY16	Batch write request (address 01)
	RX17	Normal connection slave (address 02)		RY17	Batch write request (address 02)
	RX18	Normal connection slave (address 03)		RY18	Batch write request (address 03)
	RX19	Normal connection slave (address 04)		RY19	Batch write request (address 04)
	RX20	Normal connection slave (address 05)		RY20	Batch write request (address 05)
	RX21	Normal connection slave (address 06)		RY21	Batch write request (address 06)
	RX22	Normal connection slave (address 07)		RY22	Batch write request (address 07)
	RX23	Normal connection slave (address 08)		RY23	Batch write request (address 08)
	RX24	Normal connection slave (address 09)		RY24	Batch write request (address 09)
	RX25	Normal connection slave (address 10)		RY25	Batch write request (address 10)
	RX26	Normal connection slave (address 11)		RY26	Batch write request (address 11)
	RX27	Normal connection slave (address 12)		RY27	Batch write request (address 12)
	RX28	Normal connection slave (address 13)		RY28	Batch write request (address 13)
	RX29	Normal connection slave (address 14)		RY29	Batch write request (address 14)
	RX30	Normal connection slave (address 15)		RY30	Batch write request (address 15)
	RX31	Normal connection slave (address 16)		RY31	Batch write request (address 16)
	RX32	Normal connection slave (address 17)		RY32	Batch write request (address 17)
	RX33	Normal connection slave (address 18)		RY33	Batch write request (address 18)
	RX34	Normal connection slave (address 19)		RY34	Batch write request (address 19)

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX35	Normal connection slave (address 20)		RY35	Batch write request (address 20)
	RX36	Normal connection slave (address 21)		RY36	Batch write request (address 21)
	RX37	Normal connection slave (address 22)		RY37	Batch write request (address 22)
	RX38	Normal connection slave (address 23)		RY38	Batch write request (address 23)
	RX39	Normal connection slave (address 24)		RY39	Batch write request (address 24)
	RX40	Normal connection slave (address 25)		RY40	Batch write request (address 25)
	RX41	Normal connection slave (address 26)		RY41	Batch write request (address 26)
	RX42	Normal connection slave (address 27)		RY42	Batch write request (address 27)
	RX43	Normal connection slave (address 28)		RY43	Batch write request (address 28)
	RX44	Normal connection slave (address 29)		RY44	Batch write request (address 29)
	RX45	Normal connection slave (address 30)		RY45	Batch write request (address 30)
	RX46	Normal connection slave (address 31)		RY46	Batch write request (address 31)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: A.M		RY48	01: A.M
	RX49	01: R.L_L1		RY49	01: R.L_L1
	RX50	01: S.R		RY50	01: S.R
	RX51	(Unused)		RY51	(Unused)
	RX52	(Unused)		RY52	(Unused)
	RX53	(Unused)		RY53	(Unused)
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: ALM1_L1		RY56	(Unused)
	RX57	01: ALM2_L1		RY57	(Unused)
	RX58	01: ALM3_L1		RY58	(Unused)
	RX59	01: ALM4_L1		RY59	(Unused)
	RX60	01: ALM5_L1 } UT35A: unused		RY60	(Unused)
	RX61	01: ALM6_L1		RY61	(Unused)
	RX62	01: ALM7_L1		RY62	(Unused)
	RX63	01: ALM8_L1		RY63	(Unused)
	RX64	02: A.M		RY64	02: A.M
	RX65	02: R.L_L1		RY65	02: R.L_L1
	RX66	02: S.R		RY66	02: S.R
	RX67	(Unused)		RY67	(Unused)
	RX68	(Unused)		RY68	(Unused)
	RX69	(Unused)		RY69	(Unused)
	RX70	(Unused)		RY70	(Unused)
	RX71	(Unused)		RY71	(Unused)
	RX72	02: ALM1_L1		RY72	(Unused)
	RX73	02: ALM2_L1		RY73	(Unused)
	RX74	02: ALM3_L1		RY74	(Unused)
	RX75	02: ALM4_L1		RY75	(Unused)
	RX76	02: ALM5_L1 } UT35A: unused		RY76	(Unused)
	RX77	02: ALM6_L1		RY77	(Unused)
	RX78	02: ALM7_L1		RY78	(Unused)
	RX79	02: ALM8_L1		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮			⋮	
	RX91	Remote Ready		RY91	(Reserved)
	⋮			⋮	
	RX95	(Reserved)		RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		02: PV_L1	RWw2		(Unused)
RWr3		01: CSP_L1	RWw3		01: SP_L1_1
RWr4		02: CSP_L1	RWw4		02: SP_L1_1
RWr5		01: OUT_L1	RWw5		01: MOUT_L1
RWr6		02: OUT_L1	RWw6		02: MOUT_L1
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 2

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 2 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)				(Unused)
	⋮				⋮	
	RX79	(Unused)				(Unused)
	RX80	(Reserved)			RY80	(Reserved)
	⋮				⋮	
	RX91	Remote Ready			RY91	(Reserved)
	⋮				⋮	
	RX95	(Reserved)			RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		01: I_L1_1	RWw3		01: I_L1_1
RWr4		02: I_L1_1	RWw4		02: I_L1_1
RWr5		01: D_L1_1	RWw5		01: D_L1_1
RWr6		02: D_L1_1	RWw6		02: D_L1_1
RWr7		01: SPNO.	RWw7		01: SPNO.
RWr8		02: SPNO.	RWw8		02: SPNO.
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 3

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 3 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	⋮			⋮	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮			⋮	
	RX91	Remote Ready		RY91	(Reserved)
	⋮			⋮	
	RX95	(Reserved)		RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		01: Ic_L1_1	RWw3		01: Ic_L1_1
RWr4		02: Ic_L1_1	RWw4		02: Ic_L1_1
RWr5		01: Dc_L1_1	RWw5		01: Dc_L1_1
RWr6		02: Dc_L1_1	RWw6		02: Dc_L1_1
RWr7		01: SPNO.	RWw7		01: SPNO.
RWr8		02: SPNO.	RWw8		02: SPNO.
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 4

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 4 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)				(Unused)
	⋮				⋮	
	RX79	(Unused)				(Unused)
	RX80	(Reserved)			RY80	(Reserved)
	⋮				⋮	
	RX91	Remote Ready			RY91	(Reserved)
	⋮				⋮	
	RX95	(Reserved)			RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1
RWr3		01: A2_L1_1	RWw3		01: A2_L1_1
RWr4		02: A2_L1_1	RWw4		02: A2_L1_1
RWr5		01: A3_L1_1	RWw5		01: A3_L1_1
RWr6		02: A3_L1_1	RWw6		02: A3_L1_1
RWr7		01: A4_L1_1	RWw7		01: A4_L1_1
RWr8		02: A4_L1_1	RWw8		02: A4_L1_1
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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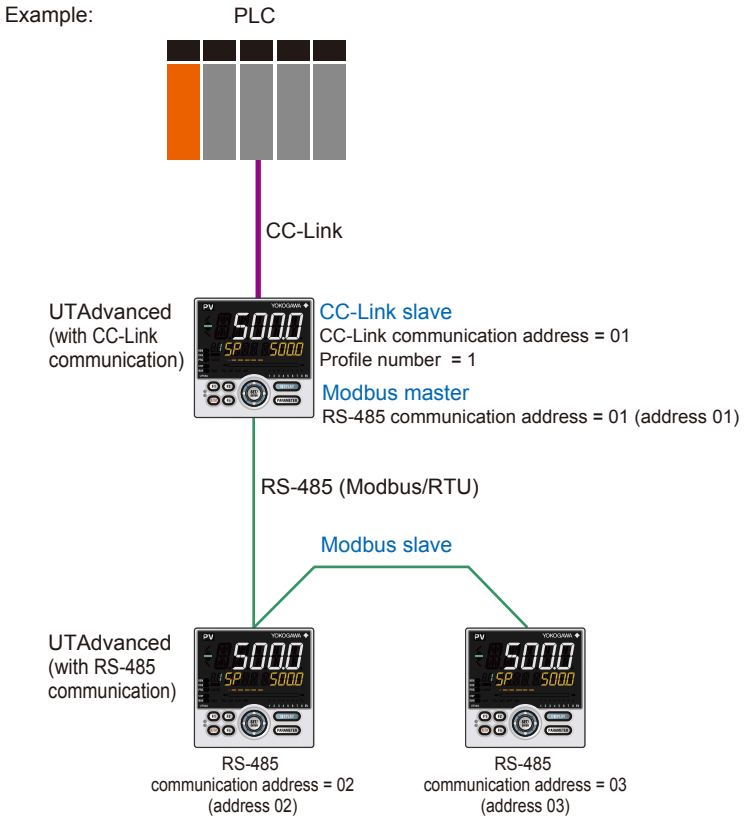
Profile number 1 (Simple PID control with 3 connected controllers) (Ver.1.10, 4-station occupied)

UT55A
UT35A



Page 1

Profile number 1 (Simple PID control with 3 connected controllers) on page 1 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: A.M		RY48	01: A.M
	RX49	01: R.L_L1		RY49	01: R.L_L1
	RX50	01: S.R		RY50	01: S.R
	RX51	(Unused)		RY51	(Unused)
	RX52	(Unused)		RY52	(Unused)
	RX53	(Unused)		RY53	(Unused)
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: ALM1_L1		RY56	(Unused)
	RX57	01: ALM2_L1		RY57	(Unused)
	RX58	01: ALM3_L1		RY58	(Unused)
	RX59	01: ALM4_L1		RY59	(Unused)
	RX60	01: ALM5_L1 } UT35A: unused		RY60	(Unused)
	RX61	01: ALM6_L1 } UT35A: unused		RY61	(Unused)
	RX62	01: ALM7_L1 } UT35A: unused		RY62	(Unused)
	RX63	01: ALM8_L1 } UT35A: unused		RY63	(Unused)

Profile number 1 (Simple PID control with 3 connected controllers) on page 1 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	02: A.M		RY64	02: A.M
	RX65	02: R.L_L1		RY65	02: R.L_L1
	RX66	02: S.R		RY66	02: S.R
	RX67	(Unused)		RY67	(Unused)
	RX68	(Unused)		RY68	(Unused)
	RX69	(Unused)		RY69	(Unused)
	RX70	(Unused)		RY70	(Unused)
	RX71	(Unused)		RY71	(Unused)
	RX72	02: ALM1_L1		RY72	(Unused)
	RX73	02: ALM2_L1		RY73	(Unused)
	RX74	02: ALM3_L1		RY74	(Unused)
	RX75	02: ALM4_L1		RY75	(Unused)
	RX76	02: ALM5_L1 } UT35A: unused		RY76	(Unused)
	RX77	02: ALM6_L1 } UT35A: unused		RY77	(Unused)
	RX78	02: ALM7_L1 } UT35A: unused		RY78	(Unused)
	RX79	02: ALM8_L1 } UT35A: unused		RY79	(Unused)
	RX80	03: A.M		RY80	03: A.M
	RX81	03: R.L_L1		RY81	03: R.L_L1
	RX82	03: S.R		RY82	03: S.R
	RX83	(Unused)		RY83	(Unused)
	RX84	(Unused)		RY84	(Unused)
	RX85	(Unused)		RY85	(Unused)
	RX86	(Unused)		RY86	(Unused)
	RX87	(Unused)		RY87	(Unused)
	RX88	03: ALM1_L1		RY88	(Unused)
	RX89	03: ALM2_L1		RY89	(Unused)
	RX90	03: ALM3_L1		RY90	(Unused)
	RX91	03: ALM4_L1		RY91	(Unused)
	RX92	03: ALM5_L1 } UT35A: unused		RY92	(Unused)
	RX93	03: ALM6_L1 } UT35A: unused		RY93	(Unused)
	RX94	03: ALM7_L1 } UT35A: unused		R94	(Unused)
	RX95	03: ALM8_L1 } UT35A: unused		RY95	(Unused)
	RX96	(Unused)		RY96	(Unused)
	⋮			⋮	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		02: PV_L1	RWw2		(Unused)
RWr3		03: PV_L1	RWw3		(Unused)
RWr4		01: CSP_L1	RWw4		01: SP_L1_1
RWr5		02: CSP_L1	RWw5		02: SP_L1_1
RWr6		03: CSP_L1	RWw6		03: SP_L1_1
RWr7		01: OUT_L1	RWw7		01: MOUT_L1
RWr8		02: OUT_L1	RWw8		02: MOUT_L1
RWr9		03: OUT_L1	RWw9		03: MOUT_L1
RWr10		01: H.OUT_L1	RWw10		01: MOUT_L1
RWr11		02: H.OUT_L1	RWw11		02: MOUT_L1
RWr12		03: H.OUT_L1	RWw12		03: MOUT_L1
RWr13		01: C.OUT_L1	RWw13		01: MOUTc_L1
RWr14		02: C.OUT_L1	RWw14		02: MOUTc_L1
RWr15		03: C.OUT_L1	RWw15		03: MOUTc_L1

Page 2

Profile number 1 (Simple PID control with 3 connected controllers) on page 2 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		01: I_L1_1	RWw4		01: I_L1_1
RWr5		02: I_L1_1	RWw5		02: I_L1_1
RWr6		03: I_L1_1	RWw6		03: I_L1_1
RWr7		01: D_L1_1	RWw7		01: D_L1_1
RWr8		02: D_L1_1	RWw8		02: D_L1_1
RWr9		03: D_L1_1	RWw9		03: D_L1_1
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		02: SPNO.	RWw11		02: SPNO.
RWr12		03: SPNO.	RWw12		03: SPNO.
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)

Page 3

Profile number 1 (Simple PID control with 3 connected controllers) on page 3 (Ver.1.10, 4-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1
RWr4		01: Ic_L1_1	RWw4		01: Ic_L1_1
RWr5		02: Ic_L1_1	RWw5		02: Ic_L1_1
RWr6		03: Ic_L1_1	RWw6		03: Ic_L1_1
RWr7		01: Dc_L1_1	RWw7		01: Dc_L1_1
RWr8		02: Dc_L1_1	RWw8		02: Dc_L1_1
RWr9		03: Dc_L1_1	RWw9		03: Dc_L1_1
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		02: SPNO.	RWw11		02: SPNO.
RWr12		03: SPNO.	RWw12		03: SPNO.
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)

Page 4

Profile number 1 (Simple PID control with 3 connected controllers) on page 4 (Ver.1.10, 4-station occupied)

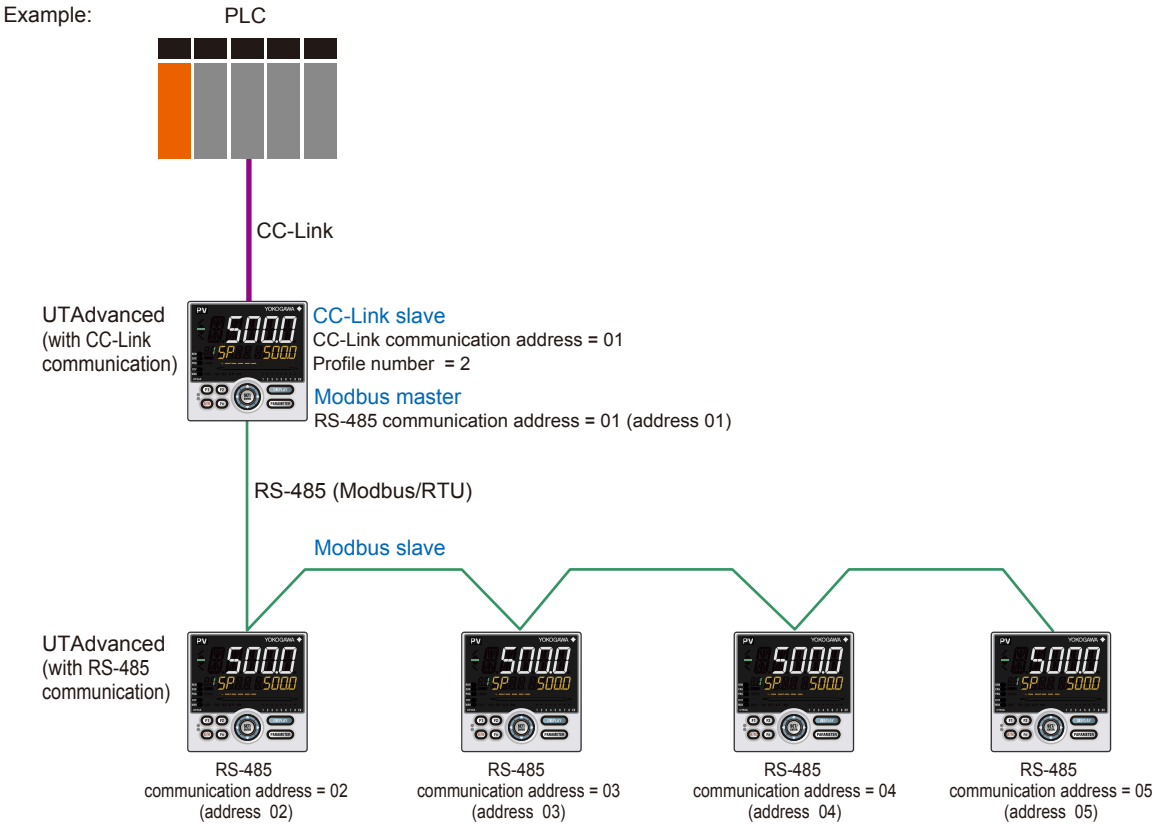
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)

RWr0		Current page		RWw0		Page change request
RWr1		01: A1_L1_1		RWw1		01: A1_L1_1
RWr2		02: A1_L1_1		RWw2		01: A1_L1_1
RWr3		03: A1_L1_1		RWw3		03: A1_L1_1
RWr4		01: A2_L1_1		RWw4		01: A2_L1_1
RWr5		02: A2_L1_1		RWw5		02: A2_L1_1
RWr6		03: A2_L1_1		RWw6		03: A2_L1_1
RWr7		01: A3_L1_1		RWw7		01: A3_L1_1
RWr8		02: A3_L1_1		RWw8		02: A3_L1_1
RWr9		03: A3_L1_1		RWw9		03: A3_L1_1
RWr10		01: A4_L1_1		RWw10		01: A4_L1_1
RWr11		02: A4_L1_1		RWw11		02: A4_L1_1
RWr12		03: A4_L1_1		RWw12		03: A4_L1_1
RWr13		01: A5_L1_1		RWw13		01: A5_L1_1
RWr14		02: A5_L1_1		RWw14		02: A5_L1_1
RWr15		03: A5_L1_1		RWw15		03: A5_L1_1

UT35A: unused

UT55A
UT35A

Profile number 2 (Simple PID control with 5 connected controllers) (Ver.2.00, 1-station occupied x8 setting)



Page 1

Profile number 2 (Simple PID control with 5 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: A.M		RY48	01: A.M
	RX49	01: R.L_L1		RY49	01: R.L_L1
	RX50	01: S.R		RY50	01: S.R
	RX51	01: ALM1_L1		RY51	(Unused)
	RX52	01: ALM2_L1		RY52	(Unused)
	RX53	01: ALM3_L1		RY53	(Unused)
	RX54	01: ALM4_L1		RY54	(Unused)
	RX55	01: ALM5_L1 } UT35A: unused		RY55	(Unused)
	RX56	01: ALM6_L1 } UT35A: unused		RY56	(Unused)
	RX57	01: ALM7_L1 } UT35A: unused		RY57	(Unused)
	RX58	01: ALM8_L1 } UT35A: unused		RY58	(Unused)
	RX59	02: A.M		RY59	02: A.M
	RX60	02: R.L_L1		RY60	02: R.L_L1
	RX61	02: S.R		RY61	02: S.R
	RX62	02: ALM1_L1		RY62	(Unused)
	RX63	02: ALM2_L1		RY63	(Unused)

Profile number 2 (Simple PID control with 5 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	02: ALM3_L1		RY64	(Unused)
	RX65	02: ALM4_L1		RY65	(Unused)
	RX66	02: ALM5_L1		RY66	(Unused)
	RX67	02: ALM6_L1		RY67	(Unused)
	RX68	02: ALM7_L1		RY68	(Unused)
	RX69	02: ALM8_L1		RY69	(Unused)
	RX70	03: A.M		RY70	03: A.M
	RX71	03: R.L_L1		RY71	03: R.L_L1
	RX72	03: S.R		RY72	03: S.R
	RX73	03: ALM1_L1		RY73	(Unused)
	RX74	03: ALM2_L1		RY74	(Unused)
	RX75	03: ALM3_L1		RY75	(Unused)
	RX76	03: ALM4_L1		RY76	(Unused)
	RX77	03: ALM5_L1		RY77	(Unused)
	RX78	03: ALM6_L1		RY78	(Unused)
	RX79	03: ALM7_L1		RY79	(Unused)
	RX80	03: ALM8_L1		RY80	(Unused)
	RX81	04: A.M		RY81	04: A.M
	RX82	04: R.L_L1		RY82	04: R.L_L1
	RX83	04: S.R		RY83	04: S.R
	RX84	04: ALM1_L1		RY84	(Unused)
	RX85	04: ALM2_L1		RY85	(Unused)
	RX86	04: ALM3_L1		RY86	(Unused)
	RX87	04: ALM4_L1		RY87	(Unused)
	RX88	04: ALM5_L1		RY88	(Unused)
	RX89	04: ALM6_L1		RY89	(Unused)
	RX90	04: ALM7_L1		RY90	(Unused)
	RX91	04: ALM8_L1		RY91	(Unused)
	RX92	05: A.M		RY92	05: A.M
	RX93	05: R.L_L1		RY93	05: R.L_L1
	RX94	05: R.L_L1		RY94	05: R.L_L1
	RX95	05: ALM1_L1		RY95	(Unused)
	RX96	05: ALM2_L1		RY96	(Unused)
	RX97	05: ALM3_L1		RY97	(Unused)
	RX98	05: ALM4_L1		RY98	(Unused)
	RX99	05: ALM5_L1		RY99	(Unused)
	RX100	05: ALM6_L1		RY100	(Unused)
	RX101	05: ALM7_L1		RY101	(Unused)
	RX102	05: ALM8_L1		RY102	(Unused)
	RX103	(Unused)		RY103	(Unused)
	RX104	(Unused)		RY104	(Unused)
	RX105	(Unused)		RY105	(Unused)
	RX106	(Unused)		RY106	(Unused)
	RX107	(Unused)		RY107	(Unused)
	RX108	(Unused)		RY108	(Unused)
	RX109	(Unused)		RY109	(Unused)
	RX110	(Unused)		RY110	(Unused)
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

UT35A: unused

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		02: PV_L1	RWw2		(Unused)
RWr3		03: PV_L1	RWw3		(Unused)
RWr4		04: PV_L1	RWw4		(Unused)
RWr5		05: PV_L1	RWw5		(Unused)
RWr6		01: CSP_L1	RWw6		01: SP_L1_1
RWr7		02: CSP_L1	RWw7		02: SP_L1_1
RWr8		03: CSP_L1	RWw8		03: SP_L1_1

Profile number 2 (Simple PID control with 5 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr9		04: CSP_L1		RWw9		04: SP_L1_1
RWr10		05: CSP_L1		RWw10		05: SP_L1_1
RWr11		01: OUT_L1		RWw11		01: MOUT_L1
RWr12		02: OUT_L1		RWw12		02: MOUT_L1
RWr13		03: OUT_L1		RWw13		03: MOUT_L1
RWr14		04: OUT_L1		RWw14		04: MOUT_L1
RWr15		05: OUT_L1		RWw15		05: MOUT_L1
RWr16		01: H.OUT_L1		RWw16		01: MOUT_L1
RWr17		02: H.OUT_L1		RWw17		02: MOUT_L1
RWr18		03: H.OUT_L1		RWw18		03: MOUT_L1
RWr19		04: H.OUT_L1		RWw19		04: MOUT_L1
RWr20		05: H.OUT_L1		RWw20		05: MOUT_L1
RWr21		01: C.OUT_L1		RWw21		01: MOUTc_L1
RWr22		02: C.OUT_L1		RWw22		02: MOUTc_L1
RWr23		03: C.OUT_L1		RWw23		03: MOUTc_L1
RWr24		04: C.OUT_L1		RWw24		04: MOUTc_L1
RWr25		05: C.OUT_L1		RWw25		05: MOUTc_L1
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

Page 2

Profile number 2 (Simple PID control with 5 connected controllers) on page 2 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		04: P_L1_1	RWw4		04: P_L1_1
RWr5		05: P_L1_1	RWw5		05: P_L1_1
RWr6		01: I_L1_1	RWw6		01: I_L1_1
RWr7		02: I_L1_1	RWw7		02: I_L1_1
RWr8		03: I_L1_1	RWw8		03: I_L1_1
RWr9		04: I_L1_1	RWw9		04: I_L1_1
RWr10		05: I_L1_1	RWw10		05: I_L1_1
RWr11		01: D_L1_1	RWw11		01: D_L1_1
RWr12		02: D_L1_1	RWw12		02: D_L1_1
RWr13		03: D_L1_1	RWw13		03: D_L1_1
RWr14		04: D_L1_1	RWw14		04: D_L1_1
RWr15		05: D_L1_1	RWw15		05: D_L1_1
RWr16		01: SPNO.	RWw16		01: SPNO.
RWr17		02: SPNO.	RWw17		02: SPNO.
RWr18		03: SPNO.	RWw18		03: SPNO.
RWr19		04: SPNO.	RWw19		04: SPNO.
RWr20		05: SPNO.	RWw20		05: SPNO.
RWr21		(Unused)	RWw21		(Unused)
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31		(Unused)	RWw31		(Unused)

Page 3

Profile number 2 (Simple PID control with 5 connected controllers) on page 3 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX111	(Unused)
	RX112	(Reserved)
	⋮	
	RX123	Remote Ready
	⋮	
	RX127	(Reserved)

OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY111	(Unused)
	RY112	(Reserved)
	⋮	
	RY123	(Reserved)
	⋮	
	RY127	(Reserved)

RWr0		Current page
RWr1		01: Pc_L1_1
RWr2		02: Pc_L1_1
RWr3		03: Pc_L1_1
RWr4		04: Pc_L1_1
RWr5		05: Pc_L1_1
RWr6		01: Ic_L1_1
RWr7		02: Ic_L1_1
RWr8		03: Ic_L1_1
RWr9		04: Ic_L1_1
RWr10		05: Ic_L1_1
RWr11		01: Dc_L1_1
RWr12		02: Dc_L1_1
RWr13		03: Dc_L1_1
RWr14		04: Dc_L1_1
RWr15		05: Dc_L1_1
RWr16		01: SPNO.
RWr17		02: SPNO.
RWr18		03: SPNO.
RWr19		04: SPNO.
RWr20		05: SPNO.
RWr21		(Unused)
RWr22		(Unused)
RWr23		(Unused)
RWr24		(Unused)
RWr25		(Unused)
RWr26		(Unused)
RWr27		(Unused)
RWr28		(Unused)
RWr29		(Unused)
RW30		(Unused)
RWr31		(Unused)

RWw0		Page change request
RWw1		01: Pc_L1_1
RWw2		02: Pc_L1_1
RWw3		03: Pc_L1_1
RWw4		04: Pc_L1_1
RWw5		05: Pc_L1_1
RWw6		01: Ic_L1_1
RWw7		02: Ic_L1_1
RWw8		03: Ic_L1_1
RWw9		04: Ic_L1_1
RWw10		05: Ic_L1_1
RWw11		01: Dc_L1_1
RWw12		02: Dc_L1_1
RWw13		03: Dc_L1_1
RWw14		04: Dc_L1_1
RWw15		05: Dc_L1_1
RWw16		01: SPNO.
RWw17		02: SPNO.
RWw18		03: SPNO.
RWw19		04: SPNO.
RWw20		05: SPNO.
RWw21		(Unused)
RWw22		(Unused)
RWw23		(Unused)
RWw24		(Unused)
RWw25		(Unused)
RWw26		(Unused)
RWw27		(Unused)
RWw28		(Unused)
RWw29		(Unused)
RWw30		(Unused)
RWw31		(Unused)

Page 4

Profile number 2 (Simple PID control with 5 connected controllers) on page 4 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)

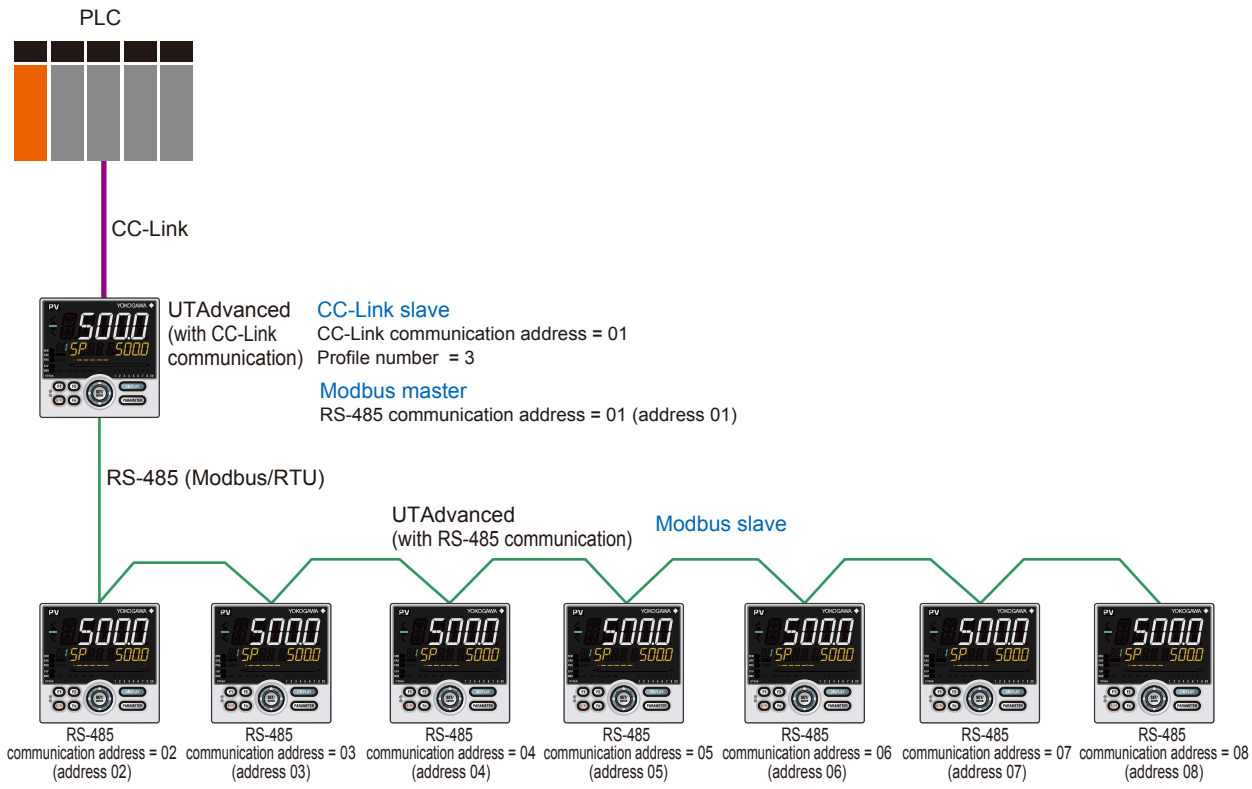
RWr0		Current page		RWw0		Page change request
RWr1		01: A1_L1_1		RWw1		01: A1_L1_1
RWr2		02: A1_L1_1		RWw2		02: A1_L1_1
RWr3		03: A1_L1_1		RWw3		03: A1_L1_1
RWr4		04: A1_L1_1		RWw4		04: A1_L1_1
RWr5		05: A1_L1_1		RWw5		05: A1_L1_1
RWr6		01: A2_L1_1		RWw6		01: A2_L1_1
RWr7		02: A2_L1_1		RWw7		02: A2_L1_1
RWr8		03: A2_L1_1		RWw8		03: A2_L1_1
RWr9		04: A2_L1_1		RWw9		04: A2_L1_1
RWr10		05: A2_L1_1		RWw10		05: A2_L1_1
RWr11		01: A3_L1_1		RWw11		01: A3_L1_1
RWr12		02: A3_L1_1		RWw12		02: A3_L1_1
RWr13		03: A3_L1_1		RWw13		03: A3_L1_1
RWr14		04: A3_L1_1		RWw14		04: A3_L1_1
RWr15		05: A3_L1_1		RWw15		05: A3_L1_1
RWr16		01: A4_L1_1		RWw16		01: A4_L1_1
RWr17		02: A4_L1_1		RWw17		02: A4_L1_1
RWr18		03: A4_L1_1		RWw18		03: A4_L1_1
RWr19		04: A4_L1_1		RWw19		04: A4_L1_1
RWr20		05: A4_L1_1		RWw20		05: A4_L1_1
RWr21		01: A5_L1_1		RWw21		01: A5_L1_1
RWr22		02: A5_L1_1		RWw22		02: A5_L1_1
RWr23		03: A5_L1_1	UT35A: unused	RWw23		03: A5_L1_1 UT35A: unused
RWr24		04: A5_L1_1		RWw24		04: A5_L1_1
RWr25		05: A5_L1_1		RWw25		05: A5_L1_1
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

Intentionally blank

Profile number 3 (Simple PID control with 8 connected controllers) (Ver.2.00, 2-station occupied x8 setting)

UT55A
UT35A

Example:



Page 1

Profile number 3 (Simple PID control with 8 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: A.M		RY48	01: A.M
	RX49	01: R.L_L1		RY49	01: R.L_L1
	RX50	01: S.R		RY50	01: S.R
	RX51	(Unused)		RY51	(Unused)
	RX52	(Unused)		RY52	(Unused)
	RX53	(Unused)		RY53	(Unused)
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: ALM1_L1		RY56	(Unused)
	RX57	01: ALM2_L1		RY57	(Unused)
	RX58	01: ALM3_L1		RY58	(Unused)
	RX59	01: ALM4_L1		RY59	(Unused)

Profile number 3 (Simple PID control with 8 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX60	01: ALM5_L1		RY60	(Unused)
	RX61	01: ALM6_L1		RY61	(Unused)
	RX62	01: ALM7_L1		RY62	(Unused)
	RX63	01: ALM8_L1		RY63	(Unused)
	RX64	02: A.M		RY64	02: A.M
	RX65	02: R.L_L1		RY65	02: R.L_L1
	RX66	02: S.R		RY66	02: S.R
	RX67	(Unused)		RY67	(Unused)
	RX68	(Unused)		RY68	(Unused)
	RX69	(Unused)		RY69	(Unused)
	RX70	(Unused)		RY70	(Unused)
	RX71	(Unused)		RY71	(Unused)
	RX72	02: ALM1_L1		RY72	(Unused)
	RX73	02: ALM2_L1		RY73	(Unused)
	RX74	02: ALM3_L1		RY74	(Unused)
	RX75	02: ALM4_L1		RY75	(Unused)
	RX76	02: ALM5_L1		RY76	(Unused)
	RX77	02: ALM6_L1		RY77	(Unused)
	RX78	02: ALM7_L1		RY78	(Unused)
	RX79	02: ALM8_L1		RY79	(Unused)
	RX80	03: A.M		RY80	03: A.M
	RX81	03: R.L_L1		RY81	03: R.L_L1
	RX82	03: S.R		RY82	03: S.R
	RX83	(Unused)		RY83	(Unused)
	RX84	(Unused)		RY84	(Unused)
	RX85	(Unused)		RY85	(Unused)
	RX86	(Unused)		RY86	(Unused)
	RX87	(Unused)		RY87	(Unused)
	RX88	03: ALM1_L1		RY88	(Unused)
	RX89	03: ALM2_L1		RY89	(Unused)
	RX90	03: ALM3_L1		RY90	(Unused)
	RX91	03: ALM4_L1		RY91	(Unused)
	RX92	03: ALM5_L1		RY92	(Unused)
	RX93	03: ALM6_L1		RY93	(Unused)
	RX94	03: ALM7_L1		RY94	(Unused)
	RX95	03: ALM8_L1		RY95	(Unused)
	RX96	04: A.M		RY96	04: A.M
	RX97	04: R.L_L1		RY97	04: R.L_L1
	RX98	04: S.R		RY98	04: S.R
	RX99	(Unused)		RY99	(Unused)
	RX100	(Unused)		RY100	(Unused)
	RX101	(Unused)		RY101	(Unused)
	RX102	(Unused)		RY102	(Unused)
	RX103	(Unused)		RY103	(Unused)
	RX104	04: ALM1_L1		RY104	(Unused)
	RX105	04: ALM2_L1		RY105	(Unused)
	RX106	04: ALM3_L1		RY106	(Unused)
	RX107	04: ALM4_L1		RY107	(Unused)
	RX108	04: ALM5_L1		RY108	(Unused)
	RX109	04: ALM6_L1		RY109	(Unused)
	RX110	04: ALM7_L1		RY110	(Unused)
	RX111	04: ALM8_L1		RY111	(Unused)
	RX112	05: A.M		RY112	05: A.M
	RX113	05: R.L_L1		RY113	05: R.L_L1
	RX114	05: S.R		RY114	05: S.R
	RX115	(Unused)		RY115	(Unused)
	RX116	(Unused)		RY116	(Unused)
	RX117	(Unused)		RY117	(Unused)
	RX118	(Unused)		RY118	(Unused)
	RX119	(Unused)		RY119	(Unused)
	RX120	05: ALM1_L1		RY120	(Unused)
	RX121	05: ALM2_L1		RY121	(Unused)
	RX122	05: ALM3_L1		RY122	(Unused)
	RX123	05: ALM4_L1		RY123	(Unused)

UT35A: unused

Profile number 3 (Simple PID control with 8 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX124	05: ALM5_L1		RY124	(Unused)
	RX125	05: ALM6_L1		RY125	(Unused)
	RX126	05: ALM7_L1		RY126	(Unused)
	RX127	05: ALM8_L1		RY127	(Unused)
	RX128	06: A.M		RY128	06: A.M
	RX129	06: R.L_L1		RY129	06: R.L_L1
	RX130	06: S.R		RY130	06: S.R
	RX131	(Unused)		RY131	(Unused)
	RX132	(Unused)		RY132	(Unused)
	RX133	(Unused)		RY133	(Unused)
	RX134	(Unused)		RY134	(Unused)
	RX135	(Unused)		RY135	(Unused)
	RX136	06: ALM1_L1		RY136	(Unused)
	RX137	06: ALM2_L1		RY137	(Unused)
	RX138	06: ALM3_L1		RY138	(Unused)
	RX139	06: ALM4_L1		RY139	(Unused)
	RX140	06: ALM5_L1		RY140	(Unused)
	RX141	06: ALM6_L1		RY141	(Unused)
	RX142	06: ALM7_L1		RY142	(Unused)
	RX143	06: ALM8_L1		RY143	(Unused)
	RX144	07: A.M		RY144	07: A.M
	RX145	07: R.L_L1		RY145	07: R.L_L1
	RX146	07: S.R		RY146	07: S.R
	RX147	(Unused)		RY147	(Unused)
	RX148	(Unused)		RY148	(Unused)
	RX149	(Unused)		RY149	(Unused)
	RX150	(Unused)		RY150	(Unused)
	RX151	(Unused)		RY151	(Unused)
	RX152	07: ALM1_L1		RY152	(Unused)
	RX153	07: ALM2_L1		RY153	(Unused)
	RX154	07: ALM3_L1		RY154	(Unused)
	RX155	07: ALM4_L1		RY155	(Unused)
	RX156	07: ALM5_L1		RY156	(Unused)
	RX157	07: ALM6_L1		RY157	(Unused)
	RX158	07: ALM7_L1		RY158	(Unused)
	RX159	07: ALM8_L1		RY159	(Unused)
	RX160	08: A.M		RY160	08: A.M
	RX161	08: R.L_L1		RY161	08: R.L_L1
	RX162	08: S.R		RY162	08: S.R
	RX163	(Unused)		RY163	(Unused)
	RX164	(Unused)		RY164	(Unused)
	RX165	(Unused)		RY165	(Unused)
	RX166	(Unused)		RY166	(Unused)
	RX167	(Unused)		RY167	(Unused)
	RX168	08: ALM1_L1		RY168	(Unused)
	RX169	08: ALM2_L1		RY169	(Unused)
	RX170	08: ALM3_L1		RY170	(Unused)
	RX171	08: ALM4_L1		RY171	(Unused)
	RX172	08: ALM5_L1		RY172	(Unused)
	RX173	08: ALM6_L1		RY173	(Unused)
	RX174	08: ALM7_L1		RY174	(Unused)
	RX175	08: ALM8_L1		RY175	(Unused)
	RX176	(Unused)		RY176	(Unused)
	⋮			⋮	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	⋮			⋮	
	RX379	Remote Ready		RY379	(Reserved)
	⋮			⋮	
	RX383	(Reserved)		RY383	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)

UT35A: unused

Profile number 3 (Simple PID control with 8 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr2		02: PV_L1		RWw2		(Unused)
RWr3		03: PV_L1		RWw3		(Unused)
RWr4		04: PV_L1		RWw4		(Unused)
RWr5		05: PV_L1		RWw5		(Unused)
RWr6		06: PV_L1		RWw6		(Unused)
RWr7		07: PV_L1		RWw7		(Unused)
RWr8		08: PV_L1		RWw8		(Unused)
RWr9		01: CSP_L1		RWw9		01: SP_L1_1
RWr10		02: CSP_L1		RWw10		02: SP_L1_1
RWr11		03: CSP_L1		RWw11		03: SP_L1_1
RWr12		04: CSP_L1		RWw12		04: SP_L1_1
RWr13		05: CSP_L1		RWw13		05: SP_L1_1
RWr14		06: CSP_L1		RWw14		06: SP_L1_1
RWr15		07: CSP_L1		RWw15		07: SP_L1_1
RWr16		08: CSP_L1		RWw16		08: SP_L1_1
RWr17		01: OUT_L1		RWw17		01: MOUT_L1
RWr18		02: OUT_L1		RWw18		02: MOUT_L1
RWr19		03: OUT_L1		RWw19		03: MOUT_L1
RWr20		04: OUT_L1		RWw20		04: MOUT_L1
RWr21		05: OUT_L1		RWw21		05: MOUT_L1
RWr22		06: OUT_L1		RWw22		06: MOUT_L1
RWr23		07: OUT_L1		RWw23		07: MOUT_L1
RWr24		08: OUT_L1		RWw24		08: MOUT_L1
RWr25		01: H.OUT_L1		RWw25		01: MOUT_L1
RWr26		02: H.OUT_L1		RWw26		02: MOUT_L1
RWr27		03: H.OUT_L1		RWw27		03: MOUT_L1
RWr28		04: H.OUT_L1		RWw28		04: MOUT_L1
RWr29		05: H.OUT_L1		RWw29		05: MOUT_L1
RWr30		06: H.OUT_L1		RWw30		06: MOUT_L1
RWr31		07: H.OUT_L1		RWw31		07: MOUT_L1
RWr32		08: H.OUT_L1		RWw32		08: MOUT_L1
RWr33		01: C.OUT_L1		RWw33		01: MOUTc_L1
RWr34		02: C.OUT_L1		RWw34		02: MOUTc_L1
RWr35		03: C.OUT_L1		RWw35		03: MOUTc_L1
RWr36		04: C.OUT_L1		RWw36		04: MOUTc_L1
RWr37		05: C.OUT_L1		RWw37		05: MOUTc_L1
RWr38		06: C.OUT_L1		RWw38		06: MOUTc_L1
RWr39		07: C.OUT_L1		RWw39		07: MOUTc_L1
RWr40		08: C.OUT_L1		RWw40		08: MOUTc_L1
RWr41		(Unused)		RWw41		(Unused)
⋮				⋮		
RWr63		(Unused)		RWw63		(Unused)

Page 2

Profile number 3 (Simple PID control with 8 connected controllers) on page 2 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	⋮			⋮	
	RX379	Remote Ready		RY379	(Reserved)
	⋮			⋮	
	RX383	(Reserved)		RY383	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		04: P_L1_1	RWw4		04: P_L1_1
RWr5		05: P_L1_1	RWw5		05: P_L1_1
RWr6		06: P_L1_1	RWw6		06: P_L1_1
RWr7		07: P_L1_1	RWw7		07: P_L1_1
RWr8		08: P_L1_1	RWw8		08: P_L1_1
RWr9		01: I_L1_1	RWw9		01: I_L1_1
RWr10		02: I_L1_1	RWw10		02: I_L1_1
RWr11		03: I_L1_1	RWw11		03: I_L1_1
RWr12		04: I_L1_1	RWw12		04: I_L1_1
RWr13		05: I_L1_1	RWw13		05: I_L1_1
RWr14		06: I_L1_1	RWw14		06: I_L1_1
RWr15		07: I_L1_1	RWw15		07: I_L1_1
RWr16		08: I_L1_1	RWw16		08: I_L1_1
RWr17		01: D_L1_1	RWw17		01: D_L1_1
RWr18		02: D_L1_1	RWw18		02: D_L1_1
RWr19		03: D_L1_1	RWw19		03: D_L1_1
RWr20		04: D_L1_1	RWw20		04: D_L1_1
RWr21		05: D_L1_1	RWw21		05: D_L1_1
RWr22		06: D_L1_1	RWw22		06: D_L1_1
RWr23		07: D_L1_1	RWw23		07: D_L1_1
RWr24		08: D_L1_1	RWw24		08: D_L1_1
RWr25		01: SPNO.	RWw25		01: SPNO.
RWr26		02: SPNO.	RWw26		02: SPNO.
RWr27		03: SPNO.	RWw27		03: SPNO.
RWr28		04: SPNO.	RWw28		04: SPNO.
RWr29		05: SPNO.	RWw29		05: SPNO.
RWr30		06: SPNO.	RWw30		06: SPNO.
RWr31		07: SPNO.	RWw31		07: SPNO.
RWr32		08: SPNO.	RWw32		08: SPNO.
RWr33		(Unused)	RWw33		(Unused)
⋮			⋮		
RWr63		(Unused)	RWw63		(Unused)

Page 3

Profile number 3 (Simple PID control with 8 connected controllers) on page 3 (Ver.2.00, 2-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	⋮			⋮	
	RX379	Remote Ready		RY379	(Reserved)
	⋮			⋮	
	RX383	(Reserved)		RY383	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1
RWr4		04: Pc_L1_1	RWw4		04: Pc_L1_1
RWr5		05: Pc_L1_1	RWw5		05: Pc_L1_1
RWr6		06: Pc_L1_1	RWw6		06: Pc_L1_1
RWr7		07: Pc_L1_1	RWw7		07: Pc_L1_1
RWr8		08: Pc_L1_1	RWw8		08: Pc_L1_1
RWr9		01: Ic_L1_1	RWw9		01: Ic_L1_1
RWr10		02: Ic_L1_1	RWw10		02: Ic_L1_1
RWr11		03: Ic_L1_1	RWw11		03: Ic_L1_1
RWr12		04: Ic_L1_1	RWw12		04: Ic_L1_1
RWr13		05: Ic_L1_1	RWw13		05: Ic_L1_1
RWr14		06: Ic_L1_1	RWw14		06: Ic_L1_1
RWr15		07: Ic_L1_1	RWw15		07: Ic_L1_1
RWr16		08: Ic_L1_1	RWw16		08: Ic_L1_1
RWr17		01: Dc_L1_1	RWw17		01: Dc_L1_1
RWr18		02: Dc_L1_1	RWw18		02: Dc_L1_1
RWr19		03: Dc_L1_1	RWw19		03: Dc_L1_1
RWr20		04: Dc_L1_1	RWw20		04: Dc_L1_1
RWr21		05: Dc_L1_1	RWw21		05: Dc_L1_1
RWr22		06: Dc_L1_1	RWw22		06: Dc_L1_1
RWr23		07: Dc_L1_1	RWw23		07: Dc_L1_1
RWr24		08: Dc_L1_1	RWw24		08: Dc_L1_1
RWr25		01: SPNO.	RWw25		01: SPNO.
RWr26		02: SPNO.	RWw26		02: SPNO.
RWr27		03: SPNO.	RWw27		03: SPNO.
RWr28		04: SPNO.	RWw28		04: SPNO.
RWr29		05: SPNO.	RWw29		05: SPNO.
RWr30		06: SPNO.	RWw30		06: SPNO.
RWr31		07: SPNO.	RWw31		07: SPNO.
RWr32		08: SPNO.	RWw32		08: SPNO.
RWr33		(Unused)	RWw33		(Unused)
⋮			⋮		
RWr63		(Unused)	RWw63		(Unused)

Page 4

Profile number 3 (Simple PID control with 8 connected controllers) on page 4 (Ver.2.00, 2-station occupied x8 setting)

IN area
CC-Link slave (UTAdvanced) → CC-Link master

Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX367	(Unused)
	RX368	(Reserved)
	⋮	
	RX379	Remote Ready
	⋮	
	RX383	(Reserved)

OUT area
CC-Link master → CC-Link slave (UTAdvanced)

Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY367	(Unused)
	RY368	(Reserved)
	⋮	
	RY379	(Reserved)
	⋮	
	RY383	(Reserved)

RWr0		Current page
RWr1		01: A1_L1_1
RWr2		02: A1_L1_1
RWr3		03: A1_L1_1
RWr4		04: A1_L1_1
RWr5		05: A1_L1_1
RWr6		06: A1_L1_1
RWr7		07: A1_L1_1
RWr8		08: A1_L1_1
RWr9		01: A2_L1_1
RWr10		02: A2_L1_1
RWr11		03: A2_L1_1
RWr12		04: A2_L1_1
RWr13		05: A2_L1_1
RWr14		06: A2_L1_1
RWr15		07: A2_L1_1
RWr16		08: A2_L1_1
RWr17		01: A3_L1_1
RWr18		02: A3_L1_1
RWr19		03: A3_L1_1
RWr20		04: A3_L1_1
RWr21		05: A3_L1_1
RWr22		06: A3_L1_1
RWr23		07: A3_L1_1
RWr24		08: A3_L1_1
RWr25		01: A4_L1_1
RWr26		02: A4_L1_1
RWr27		03: A4_L1_1
RWr28		04: A4_L1_1
RWr29		05: A4_L1_1
RWr30		06: A4_L1_1
RWr31		07: A4_L1_1
RWr32		08: A4_L1_1
RWr33		01: A5_L1_1
RWr34		02: A5_L1_1
RWr35		03: A5_L1_1
RWr36		04: A5_L1_1
RWr37		05: A5_L1_1
RWr38		06: A5_L1_1
RWr39		07: A5_L1_1
RWr40		08: A5_L1_1

UT35A: unused

RWw0		Page change request
RWw1		01: A1_L1_1
RWw2		02: A1_L1_1
RWw3		03: A1_L1_1
RWw4		04: A1_L1_1
RWw5		05: A1_L1_1
RWw6		06: A1_L1_1
RWw7		07: A1_L1_1
RWw8		08: A1_L1_1
RWw9		01: A2_L1_1
RWw10		02: A2_L1_1
RWw11		03: A2_L1_1
RWw12		04: A2_L1_1
RWw13		05: A2_L1_1
RWw14		06: A2_L1_1
RWw15		07: A2_L1_1
RWw16		08: A2_L1_1
RWw17		01: A3_L1_1
RWw18		02: A3_L1_1
RWw19		03: A3_L1_1
RWw20		04: A3_L1_1
RWw21		05: A3_L1_1
RWw22		06: A3_L1_1
RWw23		07: A3_L1_1
RWw24		08: A3_L1_1
RWw25		01: A4_L1_1
RWw26		02: A4_L1_1
RWw27		03: A4_L1_1
RWw28		04: A4_L1_1
RWw29		05: A4_L1_1
RWw30		06: A4_L1_1
RWw31		07: A4_L1_1
RWw32		08: A4_L1_1
RWw33		01: A5_L1_1
RWw34		02: A5_L1_1
RWw35		03: A5_L1_1
RWw36		04: A5_L1_1
RWw37		05: A5_L1_1
RWw38		06: A5_L1_1
RWw39		07: A5_L1_1
RWw40		08: A5_L1_1

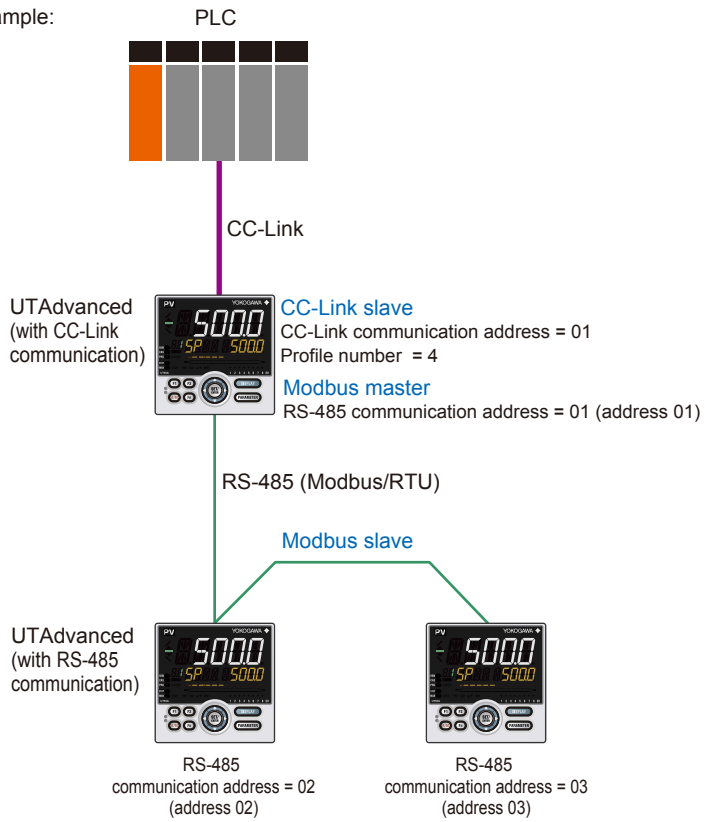
UT35A: unused

Profile number 3 (Simple PID control with 8 connected controllers) on page 4 (Ver.2.00, 2-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr41		(Unused)		RWw41		(Unused)
⋮				⋮		
RWr63		(Unused)		RWw63		(Unused)

Profile number 4 (Cascade control with 3 connected controllers) (Ver.2.00, 1-station occupied x8 setting)

UT55A

Example:



Page 1

Profile number 4 (Cascade control with 3 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Unused)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: R.L_L1		RY48	01: R.L_L1
	RX49	01: S.R		RY49	01: S.R
	RX50	01: ALM1_L1		RY50	(Unused)
	RX51	01: ALM2_L1		RY51	(Unused)
	RX52	01: ALM3_L1		RY52	(Unused)
	RX53	01: ALM4_L1		RY53	(Unused)
	RX54	01: ALM5_L1		RY54	(Unused)
	RX55	01: ALM6_L1		RY55	(Unused)
	RX56	01: ALM7_L1		RY56	(Unused)
	RX57	01: ALM8_L1		RY57	(Unused)
	RX58	01: ALM1_L2		RY58	(Unused)
	RX59	01: ALM2_L2		RY59	(Unused)
	RX60	01: ALM3_L2		RY60	(Unused)
	RX61	01: ALM4_L2		RY61	(Unused)
	RX62	01: ALM5_L2		RY62	(Unused)
	RX63	01: ALM6_L2		RY63	(Unused)

Profile number 4 (Cascade control with 3 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	01: ALM7_L2		RY64	(Unused)
	RX65	01: ALM8_L2		RY65	(Unused)
	RX66	02: R.L_L1		RY66	02: R.L_L1
	RX67	02: S.R		RY67	02: S.R
	RX68	02: ALM1_L1		RY68	(Unused)
	RX69	02: ALM2_L1		RY69	(Unused)
	RX70	02: ALM3_L1		RY70	(Unused)
	RX71	02: ALM4_L1		RY71	(Unused)
	RX72	02: ALM5_L1		RY72	(Unused)
	RX73	02: ALM6_L1		RY73	(Unused)
	RX74	02: ALM7_L1		RY74	(Unused)
	RX75	02: ALM8_L1		RY75	(Unused)
	RX76	02: ALM1_L2		RY76	(Unused)
	RX77	02: ALM2_L2		RY77	(Unused)
	RX78	02: ALM3_L2		RY78	(Unused)
	RX79	02: ALM4_L2		RY79	(Unused)
	RX80	02: ALM5_L2		RY80	(Unused)
	RX81	02: ALM6_L2		RY81	(Unused)
	RX82	02: ALM7_L2		RY82	(Unused)
	RX83	02: ALM8_L2		RY83	(Unused)
	RX84	03: R.L_L1		RY84	03: R.L_L1
	RX85	03: S.R		RY85	03: S.R
	RX86	03: ALM1_L1		RY86	(Unused)
	RX87	03: ALM2_L1		RY87	(Unused)
	RX88	03: ALM3_L1		RY88	(Unused)
	RX89	03: ALM4_L1		RY89	(Unused)
	RX90	03: ALM5_L1		RY90	(Unused)
	RX91	03: ALM6_L1		RY91	(Unused)
	RX92	03: ALM7_L1		RY92	(Unused)
	RX93	03: ALM8_L1		RY93	(Unused)
	RX94	03: ALM1_L2		R94	(Unused)
	RX95	03: ALM2_L2		RY95	(Unused)
	RX96	03: ALM3_L2		RY96	(Unused)
	RX97	03: ALM4_L2		RY97	(Unused)
	RX98	03: ALM5_L2		RY98	(Unused)
	RX99	03: ALM6_L2		RY99	(Unused)
	RX100	03: ALM7_L2		RY100	(Unused)
	RX101	03: ALM8_L2		RY101	(Unused)
	RX102	(Unused)		RY102	(Unused)
	RX103	(Unused)		RY103	(Unused)
	RX104	(Unused)		RY104	(Unused)
	RX105	(Unused)		RY105	(Unused)
	RX106	(Unused)		RY106	(Unused)
	RX107	(Unused)		RY107	(Unused)
	RX108	(Unused)		RY108	(Unused)
	RX109	(Unused)		RY109	(Unused)
	RX110	(Unused)		RY110	(Unused)
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		02: PV_L1	RWw2		(Unused)
RWr3		03: PV_L1	RWw3		(Unused)
RWr4		01: PV_L2	RWw4		(Unused)
RWr5		02: PV_L2	RWw5		(Unused)
RWr6		03: PV_L2	RWw6		(Unused)
RWr7		01: CSP_L1	RWw7		01: SP_L1_1
RWr8		02: CSP_L1	RWw8		02: SP_L1_1

Profile number 4 (Cascade control with 3 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr9		03: CSP_L1		RWw9		03: SP_L1_1
RWr10		01: CSP_L2		RWw10		01: SP_L2_1
RWr11		02: CSP_L2		RWw11		02: SP_L2_1
RWr12		03: CSP_L2		RWw12		03: SP_L2_1
RWr13		01: C.A.M		RWw13		01: C.A.M
RWr14		02: C.A.M		RWw14		02: C.A.M
RWr15		03: C.A.M		RWw15		03: C.A.M
RWr16		01: OUT_L2		RWw16		01: MOUT_L2
RWr17		02: OUT_L2		RWw17		02: MOUT_L2
RWr18		03: OUT_L2		RWw18		03: MOUT_L2
RWr19		01: H.OUT_L2		RWw19		01: MOUT_L2
RWr20		02: H.OUT_L2		RWw20		02: MOUT_L2
RWr21		03: H.OUT_L2		RWw21		03: MOUT_L2
RWr22		01: C.OUT_L2		RWw22		01: MOUTc_L2
RWr23		02: C.OUT_L2		RWw23		02: MOUTc_L2
RWr24		03: C.OUT_L2		RWw24		03: MOUTc_L2
RWr25		(Unused)		RWw25		(Unused)
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

Page 2

Profile number 4 (Cascade control with 3 connected controllers) on page 2 (Ver.2.00, 1-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		01: I_L1_1	RWw4		01: I_L1_1
RWr5		02: I_L1_1	RWw5		02: I_L1_1
RWr6		03: I_L1_1	RWw6		03: I_L1_1
RWr7		01: D_L1_1	RWw7		01: D_L1_1
RWr8		02: D_L1_1	RWw8		02: D_L1_1
RWr9		03: D_L1_1	RWw9		03: D_L1_1
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		02: SPNO.	RWw11		02: SPNO.
RWr12		03: SPNO.	RWw12		03: SPNO.
RWr13		01: P_L2_1	RWw13		01: P_L2_1
RWr14		02: P_L2_1	RWw14		02: P_L2_1
RWr15		03: P_L2_1	RWw15		03: P_L2_1
RWr16		01: I_L2_1	RWw16		01: I_L2_1
RWr17		02: I_L2_1	RWw17		02: I_L2_1
RWr18		03: I_L2_1	RWw18		03: I_L2_1
RWr19		01: D_L2_1	RWw19		01: D_L2_1
RWr20		02: D_L2_1	RWw20		02: D_L2_1
RWr21		03: D_L2_1	RWw21		03: D_L2_1
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31		(Unused)	RWw31		(Unused)

Page 3

Profile number 4 (Cascade control with 3 connected controllers) on page 3 (Ver.2.00, 1-station occupied/ x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		(Unused)		RWw1		(Unused)
RWr2		(Unused)		RWw2		(Unused)
RWr3		(Unused)		RWw3		(Unused)
RWr4		(Unused)		RWw4		(Unused)
RWr5		(Unused)		RWw5		(Unused)
RWr6		(Unused)		RWw6		(Unused)
RWr7		(Unused)		RWw7		(Unused)
RWr8		(Unused)		RWw8		(Unused)
RWr9		(Unused)		RWw9		(Unused)
RWr10		01: SPNO.		RWw10		01: SPNO.
RWr11		02: SPNO.		RWw11		02: SPNO.
RWr12		03: SPNO.		RWw12		03: SPNO.
RWr13		01: Pc_L2_1		RWw13		01: Pc_L2_1
RWr14		02: Pc_L2_1		RWw14		02: Pc_L2_1
RWr15		03: Pc_L2_1		RWw15		03: Pc_L2_1
RWr16		01: Ic_L2_1		RWw16		01: Ic_L2_1
RWr17		02: Ic_L2_1		RWw17		02: Ic_L2_1
RWr18		03: Ic_L2_1		RWw18		03: Ic_L2_1
RWr19		01: Dc_L2_1		RWw19		01: Dc_L2_1
RWr20		02: Dc_L2_1		RWw20		02: Dc_L2_1
RWr21		03: Dc_L2_1		RWw21		03: Dc_L2_1
RWr22		(Unused)		RWw22		(Unused)
RWr23		(Unused)		RWw23		(Unused)
RWr24		(Unused)		RWw24		(Unused)
RWr25		(Unused)		RWw25		(Unused)
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

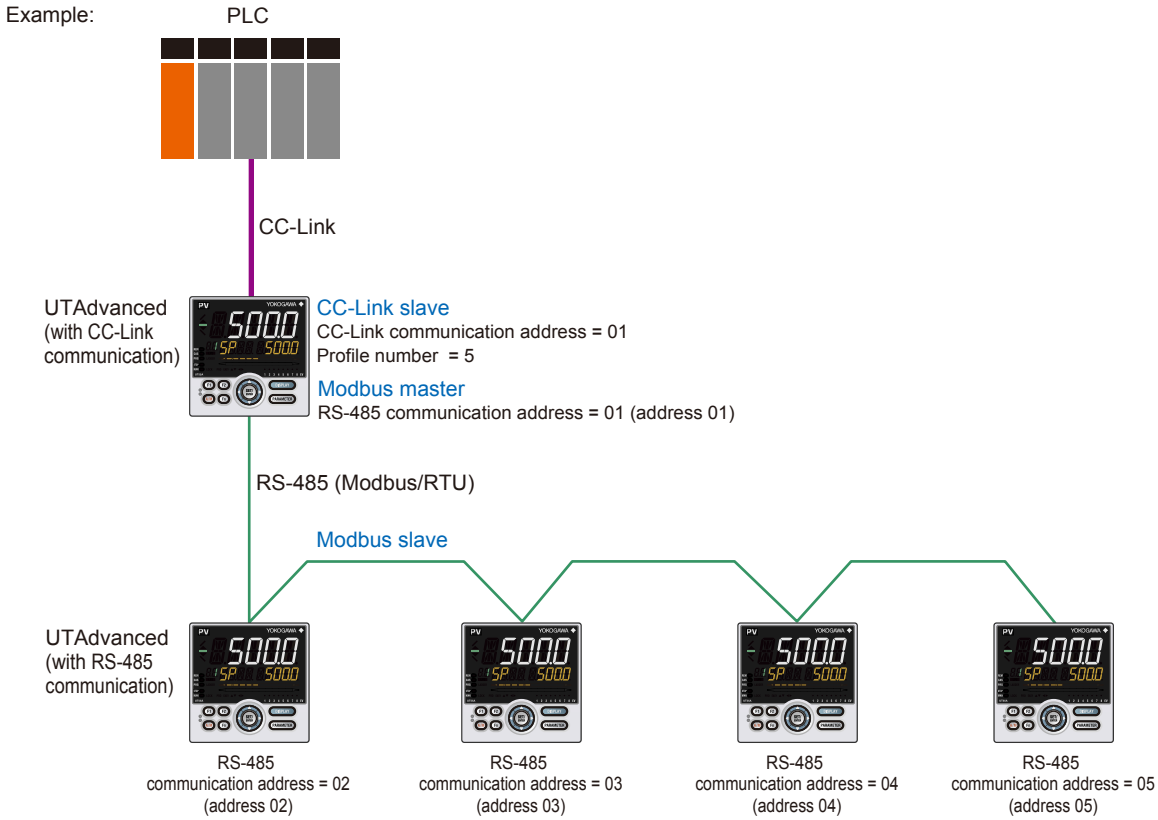
Page 4

Profile number 4 (Cascade control with 3 connected controllers) on page 4 (Ver.2.00, 1-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		01: A1_L1_1		RWw1		01: A1_L1_1
RWr2		02: A1_L1_1		RWw2		02: A1_L1_1
RWr3		03: A1_L1_1		RWw3		03: A1_L1_1
RWr4		01: A2_L1_1		RWw4		01: A2_L1_1
RWr5		02: A2_L1_1		RWw5		02: A2_L1_1
RWr6		03: A2_L1_1		RWw6		03: A2_L1_1
RWr7		01: A3_L1_1		RWw7		01: A3_L1_1
RWr8		02: A3_L1_1		RWw8		02: A3_L1_1
RWr9		03: A3_L1_1		RWw9		03: A3_L1_1
RWr10		01: A4_L1_1		RWw10		01: A4_L1_1
RWr11		02: A4_L1_1		RWw11		02: A4_L1_1
RWr12		03: A4_L1_1		RWw12		03: A4_L1_1
RWr13		01: A5_L1_1		RWw13		01: A5_L1_1
RWr14		02: A5_L1_1		RWw14		02: A5_L1_1
RWr15		03: A5_L1_1		RWw15		03: A5_L1_1
RWr16		01: A1_L2_1		RWw16		01: A1_L2_1
RWr17		02: A1_L2_1		RWw17		02: A1_L2_1
RWr18		03: A1_L2_1		RWw18		03: A1_L2_1
RWr19		01: A2_L2_1		RWw19		01: A2_L2_1
RWr20		02: A2_L2_1		RWw20		02: A2_L2_1
RWr21		03: A2_L2_1		RWw21		03: A2_L2_1
RWr22		01: A3_L2_1		RWw22		01: A3_L2_1
RWr23		02: A3_L2_1		RWw23		02: A3_L2_1
RWr24		03: A3_L2_1		RWw24		03: A3_L2_1
RWr25		01: A4_L2_1		RWw25		01: A4_L2_1
RWr26		03: A5_L2_1		RWw26		02: A4_L2_1
RWr27		03: A4_L2_1		RWw27		03: A4_L2_1
RWr28		01: A5_L2_1		RWw28		01: A5_L2_1
RWr29		02: A5_L2_1		RWw29		02: A5_L2_1
RW30		03: A5_L2_1		RWw30		03: A5_L2_1
RWr31		(Reserved)		RWw31		(Reserved)

Profile number 5 (Cascade control with 5 connected controllers) (Ver.2.00, 2-station occupied x8 setting)

UT55A



Page 1

Profile number 5 (Cascade control with 5 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Unused)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	RX49	01: R.L_L1		RY49	01: R.L_L1
	RX50	01: S.R		RY50	01: S.R
	RX51	(Unused)		RY51	(Unused)
	RX52	(Unused)		RY52	(Unused)
	RX53	(Unused)		RY53	(Unused)
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: ALM1_L1		RY56	(Unused)
	RX57	01: ALM2_L1		RY57	(Unused)
	RX58	01: ALM3_L1		RY58	(Unused)
	RX59	01: ALM4_L1		RY59	(Unused)
	RX60	01: ALM5_L1		RY60	(Unused)
	RX61	01: ALM6_L1		RY61	(Unused)
	RX62	01: ALM7_L1		RY62	(Unused)
	RX63	01: ALM8_L1		RY63	(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	(Unused)		RY64	(Unused)
	RX65	(Unused)		RY65	(Unused)
	RX66	(Unused)		RY66	(Unused)
	RX67	(Unused)		RY67	(Unused)
	RX68	(Unused)		RY68	(Unused)
	RX69	(Unused)		RY69	(Unused)
	RX70	(Unused)		RY70	(Unused)
	RX71	(Unused)		RY71	(Unused)
	RX72	01: ALM1_L2		RY72	(Unused)
	RX73	01: ALM2_L2		RY73	(Unused)
	RX74	01: ALM3_L2		RY74	(Unused)
	RX75	01: ALM4_L2		RY75	(Unused)
	RX76	01: ALM5_L2		RY76	(Unused)
	RX77	01: ALM6_L2		RY77	(Unused)
	RX78	01: ALM7_L2		RY78	(Unused)
	RX79	01: ALM8_L2		RY79	(Unused)
	RX80	(Unused)		RY80	(Unused)
	RX81	02: R.L_L1		RY81	02: R.L_L1
	RX82	02: S.R		RY82	02: S.R
	RX83	(Unused)		RY83	(Unused)
	RX84	(Unused)		RY84	(Unused)
	RX85	(Unused)		RY85	(Unused)
	RX86	(Unused)		RY86	(Unused)
	RX87	(Unused)		RY87	(Unused)
	RX88	02: ALM1_L1		RY88	(Unused)
	RX89	02: ALM2_L1		RY89	(Unused)
	RX90	02: ALM3_L1		RY90	(Unused)
	RX91	02: ALM4_L1		RY91	(Unused)
	RX92	02: ALM5_L1		RY92	(Unused)
	RX93	02: ALM6_L1		RY93	(Unused)
	RX94	02: ALM7_L1		RY94	(Unused)
	RX95	02: ALM8_L1		RY95	(Unused)
	RX96	(Unused)		RY96	(Unused)
	RX97	(Unused)		RY97	(Unused)
	RX98	(Unused)		RY98	(Unused)
	RX99	(Unused)		RY99	(Unused)
	RX100	(Unused)		RY100	(Unused)
	RX101	(Unused)		RY101	(Unused)
	RX102	(Unused)		RY102	(Unused)
	RX103	(Unused)		RY103	(Unused)
	RX104	02: ALM1_L2		RY104	(Unused)
	RX105	02: ALM2_L2		RY105	(Unused)
	RX106	02: ALM3_L2		RY106	(Unused)
	RX107	02: ALM4_L2		RY107	(Unused)
	RX108	02: ALM5_L2		RY108	(Unused)
	RX109	02: ALM6_L2		RY109	(Unused)
	RX110	02: ALM7_L2		RY110	(Unused)
	RX111	02: ALM8_L2		RY111	(Unused)
	RX112	(Unused)		RY112	(Unused)
	RX113	03: R.L_L1		RY113	03: R.L_L1
	RX114	03: S.R		RY114	03: S.R
	RX115	(Unused)		RY115	(Unused)
	RX116	(Unused)		RY116	(Unused)
	RX117	(Unused)		RY117	(Unused)
	RX118	(Unused)		RY118	(Unused)
	RX119	(Unused)		RY119	(Unused)
	RX120	03: ALM1_L1		RY120	(Unused)
	RX121	03: ALM2_L1		RY121	(Unused)
	RX122	03: ALM3_L1		RY122	(Unused)
	RX123	03: ALM4_L1		RY123	(Unused)
	RX124	03: ALM5_L1		RY124	(Unused)
	RX125	03: ALM6_L1		RY125	(Unused)
	RX126	03: ALM7_L1		RY126	(Unused)
	RX127	03: ALM8_L1		RY127	(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX128	(Unused)		RY128	(Unused)
	RX129	(Unused)		RY129	(Unused)
	RX130	(Unused)		RY130	(Unused)
	RX131	(Unused)		RY131	(Unused)
	RX132	(Unused)		RY132	(Unused)
	RX133	(Unused)		RY133	(Unused)
	RX134	(Unused)		RY134	(Unused)
	RX135	(Unused)		RY135	(Unused)
	RX136	03: ALM1_L2		RY136	(Unused)
	RX137	03: ALM2_L2		RY137	(Unused)
	RX138	03: ALM3_L2		RY138	(Unused)
	RX139	03: ALM4_L2		RY139	(Unused)
	RX140	03: ALM5_L2		RY140	(Unused)
	RX141	03: ALM6_L2		RY141	(Unused)
	RX142	03: ALM7_L2		RY142	(Unused)
	RX143	03: ALM8_L2		RY143	(Unused)
	RX144	(Unused)		RY144	(Unused)
	RX145	04: R.L_L1		RY145	04: R.L_L1
	RX146	04: S.R		RY146	04: S.R
	RX147	(Unused)		RY147	(Unused)
	RX148	(Unused)		RY148	(Unused)
	RX149	(Unused)		RY149	(Unused)
	RX150	(Unused)		RY150	(Unused)
	RX151	(Unused)		RY151	(Unused)
	RX152	04: ALM1_L1		RY152	(Unused)
	RX153	04: ALM2_L1		RY153	(Unused)
	RX154	04: ALM3_L1		RY154	(Unused)
	RX155	04: ALM4_L1		RY155	(Unused)
	RX156	04: ALM5_L1		RY156	(Unused)
	RX157	04: ALM6_L1		RY157	(Unused)
	RX158	04: ALM7_L1		RY158	(Unused)
	RX159	04: ALM8_L1		RY159	(Unused)
	RX160	(Unused)		RY160	(Unused)
	RX161	(Unused)		RY161	(Unused)
	RX162	(Unused)		RY162	(Unused)
	RX163	(Unused)		RY163	(Unused)
	RX164	(Unused)		RY164	(Unused)
	RX165	(Unused)		RY165	(Unused)
	RX166	(Unused)		RY166	(Unused)
	RX167	(Unused)		RY167	(Unused)
	RX168	04: ALM1_L2		RY168	(Unused)
	RX169	04: ALM2_L2		RY169	(Unused)
	RX170	04: ALM3_L2		RY170	(Unused)
	RX171	04: ALM4_L2		RY171	(Unused)
	RX172	04: ALM5_L2		RY172	(Unused)
	RX173	04: ALM6_L2		RY173	(Unused)
	RX174	04: ALM7_L2		RY174	(Unused)
	RX175	04: ALM8_L2		RY175	(Unused)
	RX176	(Unused)		RY176	(Unused)
	RX177	05: R.L_L1		RY177	05: R.L_L1
	RX178	05: S.R		RY178	05: S.R
	RX179	(Unused)		RY179	(Unused)
	RX180	(Unused)		RY180	(Unused)
	RX181	(Unused)		RY181	(Unused)
	RX182	(Unused)		RY182	(Unused)
	RX183	(Unused)		RY183	(Unused)
	RX184	05: ALM1_L1		RY184	(Unused)
	RX185	05: ALM2_L1		RY185	(Unused)
	RX186	05: ALM3_L1		RY186	(Unused)
	RX187	05: ALM4_L1		RY187	(Unused)
	RX188	05: ALM5_L1		RY188	(Unused)
	RX189	05: ALM6_L1		RY189	(Unused)
	RX190	05: ALM7_L1		RY190	(Unused)
	RX191	05: ALM8_L1		RY191	(Unused)

Profile number 5 (Cascade control with 5 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX192	(Unused)		RY192	(Unused)
	RX193	(Unused)		RY193	(Unused)
	RX194	(Unused)		RY194	(Unused)
	RX195	(Unused)		RY195	(Unused)
	RX196	(Unused)		RY196	(Unused)
	RX197	(Unused)		RY197	(Unused)
	RX198	(Unused)		RY198	(Unused)
	RX199	(Unused)		RY199	(Unused)
	RX200	05: ALM1_L2		RY200	(Unused)
	RX201	05: ALM2_L2		RY201	(Unused)
	RX202	05: ALM3_L2		RY202	(Unused)
	RX203	05: ALM4_L2		RY203	(Unused)
	RX204	05: ALM5_L2		RY204	(Unused)
	RX205	05: ALM6_L2		RY205	(Unused)
	RX206	05: ALM7_L2		RY206	(Unused)
	RX207	05: ALM8_L2		RY207	(Unused)
	RX208	(Unused)		RY208	(Unused)
	⋮			⋮	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	⋮			⋮	
	RX379	Remote Ready		RY379	(Reserved)
	⋮			⋮	
	RX383	(Reserved)		RY383	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		02: PV_L1	RWw2		(Unused)
RWr3		03: PV_L1	RWw3		(Unused)
RWr4		04: PV_L1	RWw4		(Unused)
RWr5		05: PV_L1	RWw5		(Unused)
RWr6		01: PV_L2	RWw6		(Unused)
RWr7		02: PV_L2	RWw7		(Unused)
RWr8		03: PV_L2	RWw8		(Unused)
RWr9		04: PV_L2	RWw9		(Unused)
RWr10		05: PV_L2	RWw10		(Unused)
RWr11		01: CSP_L1	RWw11		01: SP_L1_1
RWr12		02: CSP_L1	RWw12		02: SP_L1_1
RWr13		03: CSP_L1	RWw13		03: SP_L1_1
RWr14		04: CSP_L1	RWw14		04: SP_L1_1
RWr15		05: CSP_L1	RWw15		05: SP_L1_1
RWr16		01: CSP_L2	RWw16		01: SP_L2_1
RWr17		02: CSP_L2	RWw17		02: SP_L2_1
RWr18		03: CSP_L2	RWw18		03: SP_L2_1
RWr19		04: CSP_L2	RWw19		04: SP_L2_1
RWr20		05: CSP_L2	RWw20		05: SP_L2_1
RWr21		01: C.A.M	RWw21		01: C.A.M
RWr22		02: C.A.M	RWw22		02: C.A.M
RWr23		03: C.A.M	RWw23		03: C.A.M
RWr24		04: C.A.M	RWw24		04: C.A.M
RWr25		05: C.A.M	RWw25		05: C.A.M
RWr26		01: OUT_L2	RWw26		01: MOUT_L2
RWr27		02: OUT_L2	RWw27		02: MOUT_L2
RWr28		03: OUT_L2	RWw28		03: MOUT_L2
RWr29		04: OUT_L2	RWw29		04: MOUT_L2
RWr30		05: OUT_L2	RWw30		05: MOUT_L2
RWr31		01: H.OUT_L2	RWw31		01: MOUT_L2
RWr32		02: H.OUT_L2	RWw32		02: MOUT_L2
RWr33		03: H.OUT_L2	RWw33		03: MOUT_L2
RWr34		04: H.OUT_L2	RWw34		04: MOUT_L2
RWr35		05: H.OUT_L2	RWw35		05: MOUT_L2
RWr36		01: C.OUT_L2	RWw36		01: MOUTc_L2
RWr37		02: C.OUT_L2	RWw37		02: MOUTc_L2

Profile number 5 (Cascade control with 5 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr38		03: C.OUT_L2		RWw38		03: MOUTc_L2
RWr39		04: C.OUT_L2		RWw39		04: MOUTc_L2
RWr40		05: C.OUT_L2		RWw40		05: MOUTc_L2
RWr41		(Unused)		RWw41		(Unused)
⋮				⋮		
RWr63		(Unused)		RWw63		(Unused)

Page 2

Profile number 5 (Cascade control with 5 connected controllers) on page 2 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX367	(Unused)
	RX368	(Reserved)
	⋮	
	RX379	Remote Ready
	⋮	
	RX383	(Reserved)

OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY367	(Unused)
	RY368	(Reserved)
	⋮	
	RY379	(Reserved)
	⋮	
	RY383	(Reserved)

RWr0		Current page
RWr1		01: P_L1_1
RWr2		02: P_L1_1
RWr3		03: P_L1_1
RWr4		04: P_L1_1
RWr5		05: P_L1_1
RWr6		01: I_L1_1
RWr7		02: I_L1_1
RWr8		03: I_L1_1
RWr9		04: I_L1_1
RWr10		05: I_L1_1
RWr11		01: D_L1_1
RWr12		02: D_L1_1
RWr13		03: D_L1_1
RWr14		04: D_L1_1
RWr15		05: D_L1_1
RWr16		01: SPNO.
RWr17		02: SPNO.
RWr18		03: SPNO.
RWr19		04: SPNO.
RWr20		05: SPNO.
RWr21		01: P_L2_1
RWr22		02: P_L2_1
RWr23		03: P_L2_1
RWr24		04: P_L2_1
RWr25		05: P_L2_1
RWr26		01: I_L2_1
RWr27		02: I_L2_1
RWr28		03: I_L2_1
RWr29		04: I_L2_1
RWr30		05: I_L2_1
RWr31		01: D_L2_1
RWr32		02: D_L2_1
RWr33		03: D_L2_1
RWr34		04: D_L2_1
RWr35		05: D_L2_1
RWr36		(Unused)
⋮		
RWr63		(Unused)

RWw0		Page change request
RWw1		01: P_L1_1
RWw2		02: P_L1_1
RWw3		03: P_L1_1
RWw4		04: P_L1_1
RWw5		05: P_L1_1
RWw6		01: I_L1_1
RWw7		02: I_L1_1
RWw8		03: I_L1_1
RWw9		04: I_L1_1
RWw10		05: I_L1_1
RWw11		01: D_L1_1
RWw12		02: D_L1_1
RWw13		03: D_L1_1
RWw14		04: D_L1_1
RWw15		05: D_L1_1
RWw16		01: SPNO.
RWw17		02: SPNO.
RWw18		03: SPNO.
RWw19		04: SPNO.
RWw20		05: SPNO.
RWw21		01: P_L2_1
RWw22		02: P_L2_1
RWw23		03: P_L2_1
RWw24		04: P_L2_1
RWw25		05: P_L2_1
RWw26		01: I_L2_1
RWw27		02: I_L2_1
RWw28		03: I_L2_1
RWw29		04: I_L2_1
RWw30		05: I_L2_1
RWw31		01: D_L2_1
RWw32		02: D_L2_1
RWw33		03: D_L2_1
RWw34		04: D_L2_1
RWw35		05: D_L2_1
RWw36		(Unused)
⋮		
RWw63		(Unused)

Page 3

Profile number 5 (Cascade control with 5 connected controllers) on page 3 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	⋮			⋮	
	RX379	Remote Ready		RY379	(Reserved)
	⋮			⋮	
	RX383	(Reserved)		RY383	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		(Unused)	RWw1		(Unused)
RWr2		(Unused)	RWw2		(Unused)
RWr3		(Unused)	RWw3		(Unused)
RWr4		(Unused)	RWw4		(Unused)
RWr5		(Unused)	RWw5		(Unused)
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)
RWr12		(Unused)	RWw12		(Unused)
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
RWr16		01: SPNO.	RWw16		01: SPNO.
RWr17		02: SPNO.	RWw17		02: SPNO.
RWr18		03: SPNO.	RWw18		03: SPNO.
RWr19		04: SPNO.	RWw19		04: SPNO.
RWr20		05: SPNO.	RWw20		05: SPNO.
RWr21		01: Pc_L2_1	RWw21		01: Pc_L2_1
RWr22		02: Pc_L2_1	RWw22		02: Pc_L2_1
RWr23		03: Pc_L2_1	RWw23		03: Pc_L2_1
RWr24		04: Pc_L2_1	RWw24		04: Pc_L2_1
RWr25		05: Pc_L2_1	RWw25		05: Pc_L2_1
RWr26		01: Ic_L2_1	RWw26		01: Ic_L2_1
RWr27		02: Ic_L2_1	RWw27		02: Ic_L2_1
RWr28		03: Ic_L2_1	RWw28		03: Ic_L2_1
RWr29		04: Ic_L2_1	RWw29		04: Ic_L2_1
RWr30		05: Ic_L2_1	RWw30		05: Ic_L2_1
RWr31		01: Dc_L2_1	RWw31		01: Dc_L2_1
RWr32		02: Dc_L2_1	RWw32		02: Dc_L2_1
RWr33		03: Dc_L2_1	RWw33		03: Dc_L2_1
RWr34		04: Dc_L2_1	RWw34		04: Dc_L2_1
RWr35		05: Dc_L2_1	RWw35		05: Dc_L2_1
RWr36		(Unused)	RWw36		(Unused)
⋮			⋮		
RWr63		(Unused)	RWw63		(Unused)

Page 4

Profile number 5 (Cascade control with 5 connected controllers) on page 4 (Ver.2.00, 2-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX367	(Unused)
	RX368	(Reserved)
	⋮	
	RX379	Remote Ready
	⋮	
	RX383	(Reserved)

OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY367	(Unused)
	RY368	(Reserved)
	⋮	
	RY379	(Reserved)
	⋮	
	RY383	(Reserved)

RWr0		Current page
RWr1		01: A1_L1_1
RWr2		02: A1_L1_1
RWr3		03: A1_L1_1
RWr4		04: A1_L1_1
RWr5		05: A1_L1_1
RWr6		01: A2_L1_1
RWr7		02: A2_L1_1
RWr8		03: A2_L1_1
RWr9		04: A2_L1_1
RWr10		05: A2_L1_1
RWr11		01: A3_L1_1
RWr12		02: A3_L1_1
RWr13		03: A3_L1_1
RWr14		04: A3_L1_1
RWr15		05: A3_L1_1
RWr16		01: A4_L1_1
RWr17		02: A4_L1_1
RWr18		03: A4_L1_1
RWr19		04: A4_L1_1
RWr20		05: A4_L1_1
RWr21		01: A5_L1_1
RWr22		02: A5_L1_1
RWr23		03: A5_L1_1
RWr24		04: A5_L1_1
RWr25		05: A5_L1_1
RWr26		01: A1_L2_1
RWr27		02: A1_L2_1
RWr28		03: A1_L2_1
RWr29		04: A1_L2_1
RWr30		05: A1_L2_1
RWr31		01: A2_L2_1
RWr32		02: A2_L2_1
RWr33		03: A2_L2_1
RWr34		04: A2_L2_1
RWr35		05: A2_L2_1
RWr36		01: A3_L2_1
RWr37		02: A3_L2_1
RWr38		03: A3_L2_1
RWr39		04: A3_L2_1
RWr40		05: A3_L2_1

RWw0		Page change request
RWw1		01: A1_L1_1
RWw2		02: A1_L1_1
RWw3		03: A1_L1_1
RWw4		04: A1_L1_1
RWw5		05: A1_L1_1
RWw6		01: A2_L1_1
RWw7		02: A2_L1_1
RWw8		03: A2_L1_1
RWw9		04: A2_L1_1
RWw10		05: A2_L1_1
RWw11		01: A3_L1_1
RWw12		02: A3_L1_1
RWw13		03: A3_L1_1
RWw14		04: A3_L1_1
RWw15		05: A3_L1_1
RWw16		01: A4_L1_1
RWw17		02: A4_L1_1
RWw18		03: A4_L1_1
RWw19		04: A4_L1_1
RWw20		05: A4_L1_1
RWw21		01: A5_L1_1
RWw22		02: A5_L1_1
RWw23		03: A5_L1_1
RWw24		04: A5_L1_1
RWw25		05: A5_L1_1
RWw26		01: A1_L2_1
RWw27		02: A1_L2_1
RWw28		03: A1_L2_1
RWw29		04: A1_L2_1
RWw30		05: A1_L2_1
RWw31		01: A2_L2_1
RWw32		02: A2_L2_1
RWw33		03: A2_L2_1
RWw34		04: A2_L2_1
RWw35		05: A2_L2_1
RWw36		01: A3_L2_1
RWw37		02: A3_L2_1
RWw38		03: A3_L2_1
RWw39		04: A3_L2_1
RWw40		05: A3_L2_1

Profile number 5 (Cascade control with 5 connected controllers) on page 4 (Ver.2.00, 2-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr41		01: A4_L2_1		RWw41		01: A4_L2_1
RWr42		02: A4_L2_1		RWw42		02: A4_L2_1
RWr43		03: A4_L2_1		RWw43		03: A4_L2_1
RWr44		04: A4_L2_1		RWw44		04: A4_L2_1
RWr45		05: A4_L2_1		RWw45		05: A4_L2_1
RWr46		01: A5_L2_1		RWw46		01: A5_L2_1
RWr47		02: A5_L2_1		RWw47		02: A5_L2_1
RWr48		03: A5_L2_1		RWw48		03: A5_L2_1
RWr49		04: A5_L2_1		RWw49		04: A5_L2_1
RWr50		05: A5_L2_1		RWw50		05: A5_L2_1
RWr51		(Unused)		RWw51		(Unused)
⋮				⋮		
RWr63		(Unused)		RWw63		(Unused)

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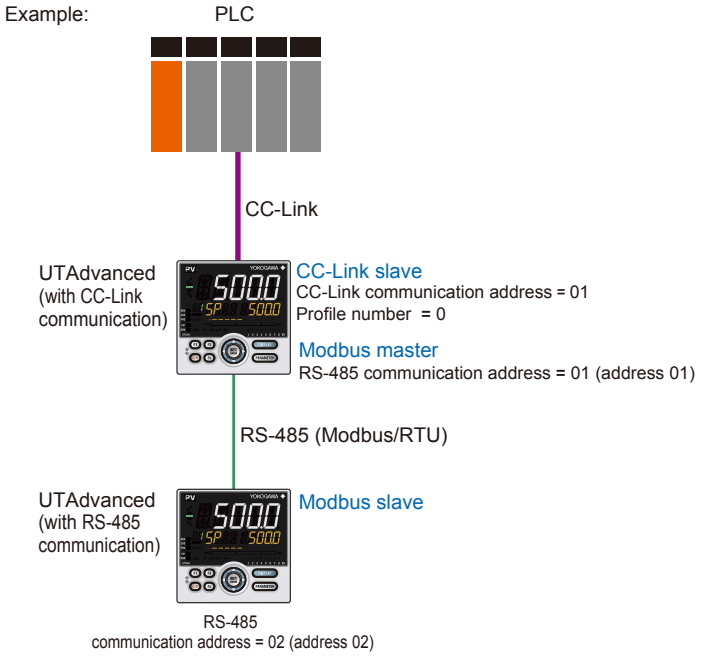
4.9.2 Profile List for UP55A/UP35A

UP55A
UP35A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) (Ver.1.10, 3-station occupied)



Page 1

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	RX8	(Reserved)		RY8	(Reserved)
	RX9	(Reserved)		RY9	(Reserved)
	RX10	(Reserved)		RY10	(Reserved)
	RX11	(Reserved)		RY11	(Reserved)
	RX12	(Reserved)		RY12	(Reserved)
	RX13	(Reserved)		RY13	(Reserved)
	RX14	(Reserved)		RY14	(Reserved)
	RX15	(Reserved)		RY15	(Reserved)
	RX16	Normal connection slave (address 01)		RY16	Batch write request (address 01)
	RX17	Normal connection slave (address 02)		RY17	Batch write request (address 02)
	RX18	Normal connection slave (address 03)		RY18	Batch write request (address 03)
	RX19	Normal connection slave (address 04)		RY19	Batch write request (address 04)
	RX20	Normal connection slave (address 05)		RY20	Batch write request (address 05)
	RX21	Normal connection slave (address 06)		RY21	Batch write request (address 06)
	RX22	Normal connection slave (address 07)		RY22	Batch write request (address 07)
	RX23	Normal connection slave (address 08)		RY23	Batch write request (address 08)
	RX24	Normal connection slave (address 09)		RY24	Batch write request (address 09)
	RX25	Normal connection slave (address 10)		RY25	Batch write request (address 10)
	RX26	Normal connection slave (address 11)		RY26	Batch write request (address 11)
	RX27	Normal connection slave (address 12)		RY27	Batch write request (address 12)
	RX28	Normal connection slave (address 13)		RY28	Batch write request (address 13)
	RX29	Normal connection slave (address 14)		RY29	Batch write request (address 14)
	RX30	Normal connection slave (address 15)		RY30	Batch write request (address 15)
	RX31	Normal connection slave (address 16)		RY31	Batch write request (address 16)
	RX32	Normal connection slave (address 17)		RY32	Batch write request (address 17)
	RX33	Normal connection slave (address 18)		RY33	Batch write request (address 18)
	RX34	Normal connection slave (address 19)		RY34	Batch write request (address 19)

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX35	Normal connection slave (address 20)		RY35	Batch write request (address 20)
	RX36	Normal connection slave (address 21)		RY36	Batch write request (address 21)
	RX37	Normal connection slave (address 22)		RY37	Batch write request (address 22)
	RX38	Normal connection slave (address 23)		RY38	Batch write request (address 23)
	RX39	Normal connection slave (address 24)		RY39	Batch write request (address 24)
	RX40	Normal connection slave (address 25)		RY40	Batch write request (address 25)
	RX41	Normal connection slave (address 26)		RY41	Batch write request (address 26)
	RX42	Normal connection slave (address 27)		RY42	Batch write request (address 27)
	RX43	Normal connection slave (address 28)		RY43	Batch write request (address 28)
	RX44	Normal connection slave (address 29)		RY44	Batch write request (address 29)
	RX45	Normal connection slave (address 30)		RY45	Batch write request (address 30)
	RX46	Normal connection slave (address 31)		RY46	Batch write request (address 31)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON		RY48	01: RST_ON
	RX49	01: PRG_ON		RY49	01: PRG_ON
	RX50	01: LOC_ON		RY50	01: LOC_ON
	RX51	01: HOLD		RY51	01: HOLD
	RX52	(Unused)		RY52	01: ADV
	RX53	01: A.M_L1		RY53	01: A.M_L1
	RX54	01: PV_EV1		RY54	(Unused)
	RX55	01: PV_EV2		RY55	(Unused)
	RX56	01: TIME_EV1		RY56	(Unused)
	RX57	01: TIME_EV2		RY57	(Unused)
	RX58	01: TIME_EV3 } UP35A: unused		RY58	(Unused)
	RX59	01: TIME_EV4		RY59	(Unused)
	RX60	01: TIME_EV5		RY60	(Unused)
	RX61	01: TIME_EV6		RY61	(Unused)
	RX62	01: TIME_EV7		RY62	(Unused)
	RX63	01: TIME_EV8		RY63	(Unused)
	RX64	02: RST_ON		RY64	02: RST_ON
	RX65	02: PRG_ON		RY65	02: PRG_ON
	RX66	02: LOC_ON		RY66	02: LOC_ON
	RX67	02: HOLD		RY67	02: HOLD
	RX68	(Unused)		RY68	02: ADV
	RX69	02: A.M_L1		RY69	02: A.M_L1
	RX70	02: PV_EV1		RY70	(Unused)
	RX71	02: PV_EV2		RY71	(Unused)
	RX72	02: TIME_EV1		RY72	(Unused)
	RX73	02: TIME_EV2		RY73	(Unused)
	RX74	02: TIME_EV3 } UP35A: unused		RY74	(Unused)
	RX75	02: TIME_EV4		RY75	(Unused)
	RX76	02: TIME_EV5		RY76	(Unused)
	RX77	02: TIME_EV6		RY77	(Unused)
	RX78	02: TIME_EV7		RY78	(Unused)
	RX79	02: TIME_EV8		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮			⋮	
	RX91	Remote Ready		RY91	(Reserved)
	⋮			⋮	
	RX95	(Reserved)		RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		01: H.TSP_L1
RWr2		01: CSP_L1	RWw2		01: H.SP_L1
RWr3		01: SEG_RTIME	RWw3		01: H.TM_L1
RWr4		02: PV_L1	RWw4		02: H.TSP_L1
RWr5		02: CSP_L1	RWw5		02: H.SP_L1
RWr6		02: SEG_RTIME	RWw6		02: H.TM_L1
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 2

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 2 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	⋮			⋮	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮			⋮	
	RX91	Remote Ready		RY91	(Reserved)
	⋮			⋮	
	RX95	(Reserved)		RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01 L.PID	RWw4		01: L.PID
RWr5		01: C.PTNO.	RWw5		01: PTNO.
RWr6		01: SEG.N	RWw6		01: SST
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 3

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 3 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	⋮			⋮	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	⋮			⋮	
	RX91	Remote Ready		RY91	(Reserved)
	⋮			⋮	
	RX95	(Reserved)		RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		02: P_L1_1	RWw1		02: P_L1_1
RWr2		02: I_L1_1	RWw2		02: I_L1_1
RWr3		02: D_L1_1	RWw3		02: D_L1_1
RWr4		02: L.PID	RWw4		02: L.PID
RWr5		02: C.PTNO.	RWw5		02: PTNO.
RWr6		02: SEG.N	RWw6		02: SST
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Page 4

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 4 (Ver.1.10, 3-station occupied)					
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)				(Unused)
	⋮				⋮	
	RX79	(Unused)				(Unused)
	RX80	(Reserved)			RY80	(Reserved)
	⋮				⋮	
	RX91	Remote Ready			RY91	(Reserved)
	⋮				⋮	
	RX95	(Reserved)			RY95	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: L.TY1	RWw1		01: L.TY1
RWr2		01: L.EV1	RWw2		01: L.EV1
RWr3		01: L.TY2	RWw3		01: L.TY2
RWr4		01: L.EV2	RWw4		01: L.EV2
RWr5		02: L.TY1	RWw5		02: L.TY1
RWr6		02: L.EV1	RWw6		02: L.EV1
RWr7		02: L.TY2	RWw7		02: L.TY2
RWr8		02: L.EV2	RWw8		02: L.EV2
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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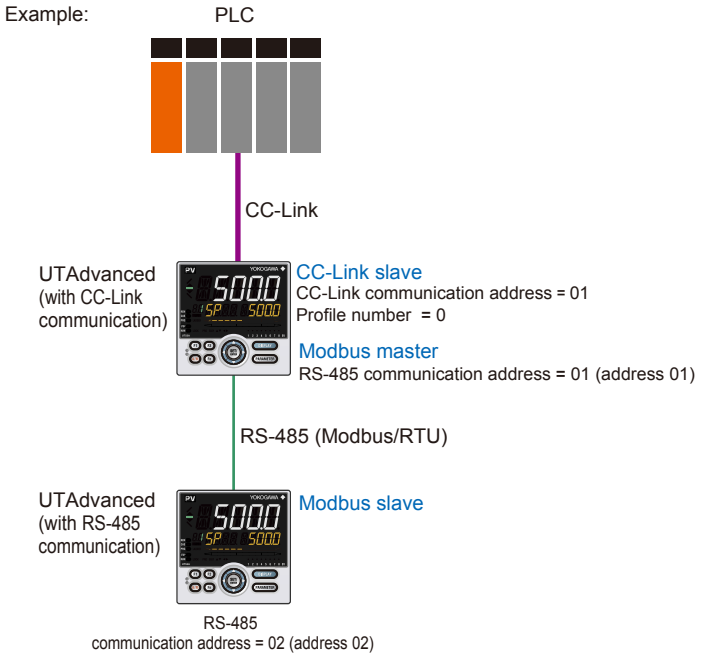
Profile number 11 (Simple PID control with 2 connected controllers) (Ver.1.10, 4-station occupied)

UP55A
UP35A



Page 1

Profile number 11 (Simple PID control with 2 connected controllers) on page 1 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON		RY48	01: RST_ON
	RX49	01: PRG_ON		RY49	01: PRG_ON
	RX50	01: LOC_ON		RY50	01: LOC_ON
	RX51	01: HOLD		RY51	01: HOLD
	RX52	(Unused)		RY52	01: ADV
	RX53	01: A.M_L1		RY53	01: A.M_L1
	RX54	01: ALM1_L1		RY54	(Unused)
	RX55	01: ALM2_L1		RY55	(Unused)
	RX56	01: PV_EV1		RY56	(Unused)
	RX57	01: PV_EV2		RY57	(Unused)
	RX58	01: PV_EV3 } UP35A: unused		RY58	(Unused)
	RX59	01: PV_EV4		RY59	(Unused)
	RX60	01: PV_EV5		RY60	(Unused)
	RX61	01: PV_EV6		RY61	(Unused)
	RX62	01: PV_EV7		RY62	(Unused)
	RX63	01: PV_EV8		RY63	(Unused)
	RX64	01: TIME_EV1		RY64	(Unused)
	RX65	01: TIME_EV2		RY65	(Unused)
	RX66	01: TIME_EV3		RY66	(Unused)
	RX67	01: TIME_EV4		RY67	(Unused)
	RX68	01: TIME_EV5 UP35A: unused		RY68	(Unused)
	RX69	01: TIME_EV6 UP35A: unused		RY69	(Unused)
	RX70	01: TIME_EV7 UP35A: unused		RY70	(Unused)

Profile number 11 (Simple PID control with 2 connected controllers) on page 1 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX71	01: TIME_EV8	UP35A: unused		RY71	(Unused)
	RX72	01: TIME_EV9			RY72	(Unused)
	RX73	01: TIME_EV10			RY73	(Unused)
	RX74	01: TIME_EV11			RY74	(Unused)
	RX75	01: TIME_EV12			RY75	(Unused)
	RX76	01: TIME_EV13			RY76	(Unused)
	RX77	01: TIME_EV14			RY77	(Unused)
	RX78	01: TIME_EV15			RY78	(Unused)
	RX79	01: TIME_EV16			RY79	(Unused)
	RX80	02: RST_ON			RY80	02: RST_ON
	RX81	02: PRG_ON			RY81	02: PRG_ON
	RX82	02: LOC_ON			RY82	02: LOC_ON
	RX83	02: HOLD			RY83	02: HOLD
	RX84	(Unused)			RY84	02: ADV
	RX85	02: A.M_L1			RY85	02: A.M_L1
	RX86	02: ALM1_L1			RY86	(Unused)
	RX87	02: ALM2_L1			RY87	(Unused)
	RX88	02: PV_EV1			RY88	(Unused)
	RX89	02: PV_EV2			RY89	(Unused)
	RX90	02: PV_EV3	UP35A: unused		RY90	(Unused)
	RX91	02: PV_EV4			RY91	(Unused)
	RX92	02: PV_EV5			RY92	(Unused)
	RX93	02: PV_EV6			RY93	(Unused)
	RX94	02: PV_EV7			R94	(Unused)
	RX95	02: PV_EV8			RY95	(Unused)
	RX96	02: TIME_EV1			RY96	(Unused)
	RX97	02: TIME_EV2			RY97	(Unused)
	RX98	02: TIME_EV3			RY98	(Unused)
	RX99	02: TIME_EV4			RY99	(Unused)
	RX100	02: TIME_EV5	UP35A: unused		RY100	(Unused)
	RX101	02: TIME_EV6			RY101	(Unused)
	RX102	02: TIME_EV7			RY102	(Unused)
	RX103	02: TIME_EV8			RY103	(Unused)
	RX104	02: TIME_EV9			RY104	(Unused)
	RX105	02: TIME_EV10			RY105	(Unused)
	RX106	02: TIME_EV11			RY106	(Unused)
	RX107	02: TIME_EV12			RY107	(Unused)
	RX108	02: TIME_EV13			RY108	(Unused)
	RX109	02: TIME_EV14			RY109	(Unused)
	RX110	02: TIME_EV15			RY110	(Unused)
	RX111	02: TIME_EV16			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		01: H.TSP_L1
RWr2		01: CSP_L1	RWw2		01: H.SP_L1
RWr3		01: SEG_RTIME	RWw3		01: H.TM_L1
RWr4		01: LSP_L1	RWw4		01: LSP_L1
RWr5		01: OUT_L1	RWw5		01: MOUT_L1
RWr6		01: C.PTNO.	RWw6		01: PTNO.
RWr7		01: SEG.N	RWw7		01: SST
RWr8		(Unused)	RWw8		(Unused)
RWr9		02: PV_L1	RWw9		02: H.TSP_L1
RWr10		02: CSP_L1	RWw10		02: H.SP_L1
RWr11		02: SEG_RTIME	RWw11		02: H.TM_L1
RWr12		02: LSP_L1	RWw12		02: LSP_L1
RWr13		02: OUT_L1	RWw13		02: MOUT_L1
RWr14		02: C.PTNO.	RWw14		02: PTNO.
RWr15		02: SEG.N	RWw15		02: SST

Page 2

Profile number 11 (Simple PID control with 2 connected controllers) on page 2 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	⋮			⋮	
	RX123	Remote Ready		RY123	(Reserved)
	⋮			⋮	
	RX127	(Reserved)		RY127	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: L.PID	RWw4		01: L.PID
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		01: A3_L1_1 UP35A: unused	RWw7		01: A3_L1_1 UP35A: unused
RWr8		(Unused)	RWw8		(Unused)
RWr9		02: P_L1_1	RWw9		02: P_L1_1
RWr10		02: I_L1_1	RWw10		02: I_L1_1
RWr11		02: D_L1_1	RWw11		02: D_L1_1
RWr12		02: L.PID	RWw12		02: L.PID
RWr13		02: A1_L1_1	RWw13		02: A1_L1_1
RWr14		02: A2_L1_1	RWw14		02: A2_L1_1
RWr15		02: A3_L1_1 UP35A: unused	RWw15		02: A3_L1_1 UP35A: unused

Page 3

Profile number 11 (Simple PID control with 2 connected controllers) on page 3 (Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX111	(Unused)
	RX112	(Reserved)
	⋮	
	RX123	Remote Ready
	⋮	
	RX127	(Reserved)

OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY111	(Unused)
	RY112	(Reserved)
	⋮	
	RY123	(Reserved)
	⋮	
	RY127	(Reserved)

RWr0		Current page	
RWr1		01: L.TY1	
RWr2		01: L.EV1	
RWr3		01: L.TY2	
RWr4		01: L.EV2	
RWr5		01: L.TY3	UP35A: unused
RWr6		01: L.EV3	
RWr7		01: L.TY4	
RWr8		01: L.EV4	
RWr9		01: L.TY5	
RWr10		01: L.EV5.	
RWr11		01: L.TY6	
RWr12		01: L.EV6	
RWr13		01: L.TY7	
RWr14		01: L.EV7	
RWr15		(Unused)	

RWw0		Page change request	
RWw1		01: L.TY1	
RWw2		01: L.EV1	
RWw3		01: L.TY2	
RWw4		01: L.EV2	
RWw5		01: L.TY3	UP35A: unused
RWw6		01: L.EV3	
RWw7		01: L.TY4	
RWw8		01: L.EV4	
RWw9		01: L.TY5	
RWw10		01: L.EV5.	
RWw11		01: L.TY6	
RWw12		01: L.EV6	
RWw13		01: L.TY7	
RWw14		01: L.EV7	
RWw15		(Unused)	

Page 4

Profile number 11 (Simple PID control with 2 connected controllers) on page 4 (Ver.1.10, 4-station occupied)

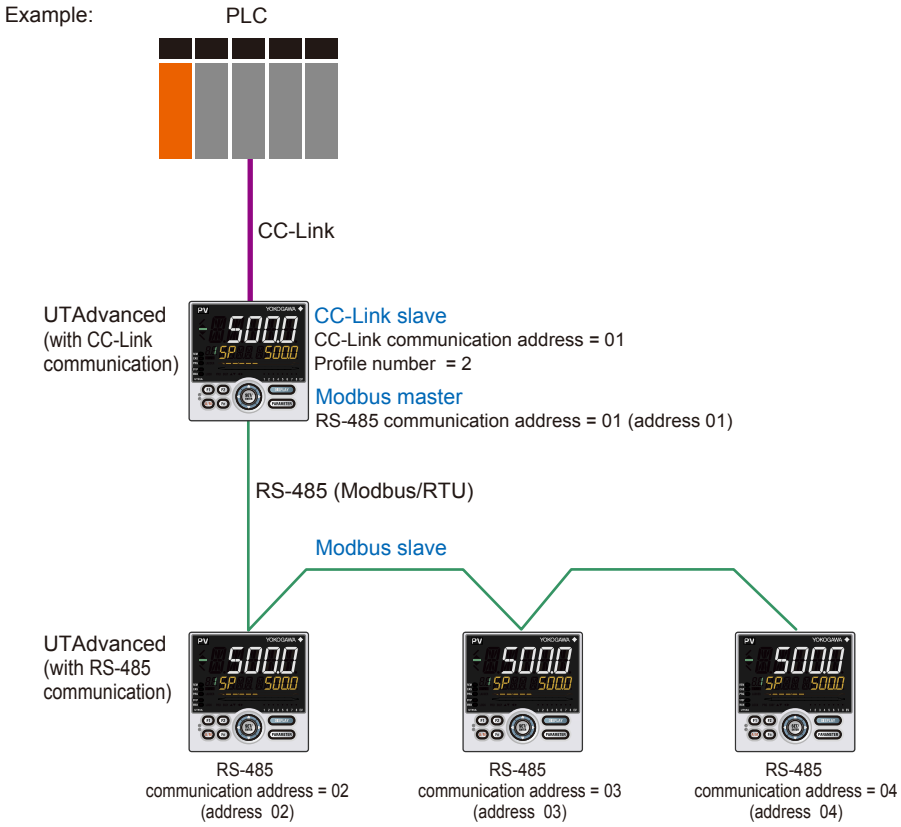
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	⋮				⋮	
	RX123	Remote Ready			RY123	(Reserved)
	⋮				⋮	
	RX127	(Reserved)			RY127	(Reserved)

RWr0		Current page		RWw0		Page change request
RWr1		02: L.TY1		RWw1		02: L.TY1
RWr2		02: L.EV1		RWw2		02: L.EV1
RWr3		02: L.TY2		RWw3		02: L.TY2
RWr4		02: L.EV2		RWw4		02: L.EV2
RWr5		02: L.TY3	UP35A: unused	RWw5		02: L.TY3 UP35A: unused
RWr6		02: L.EV3		RWw6		02: L.EV3
RWr7		02: L.TY4		RWw7		02: L.TY4
RWr8		02: L.EV4		RWw8		02: L.EV4
RWr9		02: L.TY5		RWw9		02: L.TY5
RWr10		02: L.EV5.		RWw10		02: L.EV5.
RWr11		02: L.TY6		RWw11		02: L.TY6
RWr12		02: L.EV6		RWw12		02: L.EV6
RWr13		02: L.TY7		RWw13		02: L.TY7
RWr14		02: L.EV7		RWw14		02: L.EV7
RWr15		(Unused)		RWw15		(Unused)

Intentionally blank

Profile number 12 (Simple PID control with 4 connected controllers) (Ver.2.00, 2-station occupied x4 setting)

UT55A
UT35A



Page 1

Profile number 12 (Simple PID control with 4 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON		RY48	01: RST_ON
	RX49	01: PRG_ON		RY49	01: PRG_ON
	RX50	01: LOC_ON		RY50	01: LOC_ON
	RX51	01: HOLD		RY51	01: HOLD
	RX52	(Unused)		RY52	01: ADV
	RX53	01: A.M_L1		RY53	01: A.M_L1
	RX54	01: ALM1_L1		RY54	(Unused)
	RX55	01: ALM2_L1		RY55	(Unused)
	RX56	01: PV_EV1		RY56	(Unused)
	RX57	01: PV_EV2		RY57	(Unused)
	RX58	01: PV_EV3		RY58	(Unused)
	RX59	01: PV_EV4		RY59	(Unused)
	RX60	01: PV_EV5		RY60	(Unused)
	RX61	01: PV_EV6		RY61	(Unused)
	RX62	01: PV_EV7		RY62	(Unused)
	RX63	01: PV_EV8		RY63	(Unused)

UP35A: unused

Profile number 12 (Simple PID control with 4 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	01: TIME_EV1		RY64	(Unused)
	RX65	01: TIME_EV2		RY65	(Unused)
	RX66	01: TIME_EV3		RY66	(Unused)
	RX67	01: TIME_EV4		RY67	(Unused)
	RX68	01: TIME_EV5		RY68	(Unused)
	RX69	01: TIME_EV6		RY69	(Unused)
	RX70	01: TIME_EV7		RY70	(Unused)
	RX71	01: TIME_EV8		RY71	(Unused)
	RX72	01: TIME_EV9		RY72	(Unused)
	RX73	01: TIME_EV10		RY73	(Unused)
	RX74	01: TIME_EV11		RY74	(Unused)
	RX75	01: TIME_EV12		RY75	(Unused)
	RX76	01: TIME_EV13		RY76	(Unused)
	RX77	01: TIME_EV14		RY77	(Unused)
	RX78	01: TIME_EV15		RY78	(Unused)
	RX79	01: TIME_EV16		RY79	(Unused)
	RX80	02: RST_ON		RY80	02: RST_ON
	RX81	02: PRG_ON		RY81	02: PRG_ON
	RX82	02: LOC_ON		RY82	02: LOC_ON
	RX83	02: HOLD		RY83	02: HOLD
	RX84	(Unused)		RY84	02: ADV
	RX85	02: A.M_L1		RY85	02: A.M_L1
	RX86	02: ALM1_L1		RY86	(Unused)
	RX87	02: ALM2_L1		RY87	(Unused)
	RX88	02: PV_EV1		RY88	(Unused)
	RX89	02: PV_EV2		RY89	(Unused)
	RX90	02: PV_EV3		RY90	(Unused)
	RX91	02: PV_EV4		RY91	(Unused)
	RX92	02: PV_EV5		RY92	(Unused)
	RX93	02: PV_EV6		RY93	(Unused)
	RX94	02: PV_EV7		RY94	(Unused)
	RX95	02: PV_EV8		RY95	(Unused)
	RX96	02: TIME_EV1		RY96	(Unused)
	RX97	02: TIME_EV2		RY97	(Unused)
	RX98	02: TIME_EV3		RY98	(Unused)
	RX99	02: TIME_EV4		RY99	(Unused)
	RX100	02: TIME_EV5		RY100	(Unused)
	RX101	02: TIME_EV6		RY101	(Unused)
	RX102	02: TIME_EV7		RY102	(Unused)
	RX103	02: TIME_EV8		RY103	(Unused)
	RX104	02: TIME_EV9		RY104	(Unused)
	RX105	02: TIME_EV10		RY105	(Unused)
	RX106	02: TIME_EV11		RY106	(Unused)
	RX107	02: TIME_EV12		RY107	(Unused)
	RX108	02: TIME_EV13		RY108	(Unused)
	RX109	02: TIME_EV14		RY109	(Unused)
	RX110	02: TIME_EV15		RY110	(Unused)
	RX111	02: TIME_EV16		RY111	(Unused)
	RX112	03: RST_ON		RX112	03: RST_ON
	RX113	03: PRG_ON		RX113	03: PRG_ON
	RX114	03: LOC_ON		RX114	03: LOC_ON
	RX115	03: HOLD		RX115	03: HOLD
	RX116	(Unused)		RX116	03: ADV
	RX117	03: A.M_L1		RX117	03: A.M_L1
	RX118	03: ALM1_L1		RX118	03: ALM1_L1
	RX119	03: ALM2_L1		RX119	(Unused)
	RX120	03: PV_EV1		RX120	(Unused)
	RX121	03: PV_EV2		RX121	(Unused)
	RX122	03: PV_EV3		RX122	(Unused)
	RX123	03: PV_EV4		RX123	(Unused)
	RX124	03: PV_EV5		RX124	(Unused)
	RX125	03: PV_EV6		RX125	(Unused)
	RX126	03: PV_EV7		RX126	(Unused)
	RX127	03: PV_EV8		RX127	(Unused)

UP35A: unused

Profile number 12 (Simple PID control with 4 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX128	03: TIME_EV1			RX128	(Unused)
	RX129	03: TIME_EV2			RX129	(Unused)
	RX130	03: TIME_EV3			RY130	(Unused)
	RX131	03: TIME_EV4			RY131	(Unused)
	RX132	03: TIME_EV5	UP35A: unused		RY132	(Unused)
	RX133	03: TIME_EV6			RY133	(Unused)
	RX134	03: TIME_EV7			RY134	(Unused)
	RX135	03: TIME_EV8			RY135	(Unused)
	RX136	03: TIME_EV9			RY136	(Unused)
	RX137	03: TIME_EV10			RY137	(Unused)
	RX138	03: TIME_EV11			RY138	(Unused)
	RX139	03: TIME_EV12			RY139	(Unused)
	RX140	03: TIME_EV13			RY140	(Unused)
	RX141	03: TIME_EV14			RY141	(Unused)
	RX142	03: TIME_EV15			RY142	(Unused)
	RX143	03: TIME_EV16			RY143	(Unused)
	RX144	04: RST_ON			RY144	04: RST_ON
	RX145	04: PRG_ON			RY145	04: PRG_ON
	RX146	04: LOC_ON			RY146	04: LOC_ON
	RX147	04: HOLD			RY147	04: HOLD
	RX148	(Unused)			RY148	04: ADV
	RX149	04: A.M_L1			RY149	04: A.M_L1
	RX150	04: ALM1_L1			RY150	(Unused)
	RX151	04: ALM2_L1			RY151	(Unused)
	RX152	04: PV_EV1			RY152	(Unused)
	RX153	04: PV_EV2			RY153	(Unused)
	RX154	04: PV_EV3	UP35A: unused		RY154	(Unused)
	RX155	04: PV_EV4			RY155	(Unused)
	RX156	04: PV_EV5			RY156	(Unused)
	RX157	04: PV_EV6			RY157	(Unused)
	RX158	04: PV_EV7			RY158	(Unused)
	RX159	04: PV_EV8			RY159	(Unused)
	RX160	04: TIME_EV1			RY160	(Unused)
	RX161	04: TIME_EV2			RY161	(Unused)
	RX162	04: TIME_EV3			RY162	(Unused)
	RX163	04: TIME_EV4			RY163	(Unused)
	RX164	04: TIME_EV5	UP35A: unused		RY164	(Unused)
	RX165	04: TIME_EV6			RY165	(Unused)
	RX166	04: TIME_EV7			RY166	(Unused)
	RX167	04: TIME_EV8			RY167	(Unused)
	RX168	04: TIME_EV9			RY168	(Unused)
	RX169	04: TIME_EV10			RY169	(Unused)
	RX170	04: TIME_EV11			RY170	(Unused)
	RX171	04: TIME_EV12			RY171	(Unused)
	RX172	04: TIME_EV13			RY172	(Unused)
	RX173	04: TIME_EV14			RY173	(Unused)
	RX174	04: TIME_EV15			RY174	(Unused)
	RX175	04: TIME_EV16			RY175	(Unused)
	RX176	(Reserved)			RY176	(Reserved)
	⋮				⋮	
	RX187	Remote Ready			RY187	(Reserved)
	⋮				⋮	
	RX191	(Reserved)			RY191	(Reserved)

RWr0		Current page
RWr1		01: PV_L1
RWr2		01: CSP_L1
RWr3		01: SEG_RTIME
RWr4		01: LSP_L1
RWr5		01: OUT_L1
RWr6		01: C.PTNO.
RWr7		01: SEG.N
RWr8		(Unused)

RWw0		Page change request
RWw1		01: H.TSP_L1
RWw2		01: H.SP_L1
RWw3		01: H.TM_L1
RWw4		01: LSP_L1
RWw5		01: MOUT_L1
RWw6		01: PTNO.
RWw7		01: SST
RWw8		(Unused)

Profile number 12 (Simple PID control with 4 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr9		02: PV_L1		RWw9		02: H.TSP_L1
RWr10		02: CSP_L1		RWw10		02: H.SP_L1
RWr11		02: SEG_RTIME		RWw11		02: H.TM_L1
RWr12		02: LSP_L1		RWw12		02: LSP_L1
RWr13		02: OUT_L1		RWw13		02: MOUT_L1
RWr14		02: C.PTNO.		RWw14		02: PTNO.
RWr15		02: SEG.N		RWw15		02: SST
RWr16		(Unused)		RWw16		(Unused)
RWr17		03: PV_L1		RWw17		03: H.TSP_L1
RWr18		03: CSP_L1		RWw18		03: H.SP_L1
RWr19		03: SEG_RTIME		RWw19		03: H.TM_L1
RWr20		03: LSP_L1		RWw20		03: LSP_L1
RWr21		03: OUT_L1		RWw21		03: MOUT_L1
RWr22		03: C.PTNO.		RWw22		03: PTNO.
RWr23		03: SEG.N		RWw23		03: SST
RWr24		(Unused)		RWw24		(Unused)
RWr25		04: PV_L1		RWw25		04: H.TSP_L1
RWr26		04: CSP_L1		RWw26		04: H.SP_L1
RWr27		04: SEG_RTIME		RWw27		04: H.TM_L1
RWr28		04: LSP_L1		RWw28		04: LSP_L1
RWr29		04: OUT_L1		RWw29		04: MOUT_L1
RW30		04: C.PTNO.		RWw30		04: PTNO.
RWr31		04: SEG.N		RWw31		04: SST

Page 2

Profile number 12 (Simple PID control with 4 connected controllers) on page 2 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX175	(Unused)		RY175	(Unused)
	RX176	(Reserved)		RY176	(Reserved)
	⋮			⋮	
	RX187	Remote Ready		RY187	(Reserved)
	⋮			⋮	
	RX191	(Reserved)		RY191	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: L.PID	RWw4		01: L.PID
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		01: A3_L1_1 UP35A: unused	RWw7		01: A3_L1_1 UP35A: unused
RWr8		(Unused)	RWw8		(Unused)
RWr9		02: P_L1_1	RWw9		02: P_L1_1
RWr10		02: I_L1_1	RWw10		02: I_L1_1
RWr11		02: D_L1_1	RWw11		02: D_L1_1
RWr12		02: L.PID	RWw12		02: L.PID
RWr13		02: A1_L1_1	RWw13		02: A1_L1_1
RWr14		02: A2_L1_1	RWw14		02: A2_L1_1
RWr15		02: A3_L1_1 UP35A: unused	RWw15		02: A3_L1_1 UP35A: unused
RWr16		(Unused)	RWw16		(Unused)
RWr17		03: P_L1_1	RWw17		03: P_L1_1
RWr18		03: I_L1_1	RWw18		03: I_L1_1
RWr19		03: D_L1_1	RWw19		03: D_L1_1
RWr20		03: L.PID	RWw20		03: L.PID
RWr21		03: A1_L1_1	RWw21		03: A1_L1_1
RWr22		03: A2_L1_1	RWw22		03: A2_L1_1
RWr23		03: A3_L1_1 UP35A: unused	RWw23		03: A3_L1_1 UP35A: unused
RWr24		(Unused)	RWw24		(Unused)
RWr25		04: P_L1_1	RWw25		04: P_L1_1
RWr26		04: I_L1_1	RWw26		04: I_L1_1
RWr27		04: D_L1_1	RWw27		04: D_L1_1
RWr28		04: L.PID	RWw28		04: L.PID
RWr29		04: A1_L1_1	RWw29		04: A1_L1_1
RW30		04: A2_L1_1	RWw30		04: A2_L1_1
RWr31		04: A3_L1_1 UP35A: unused	RWw31		04: A3_L1_1 UP35A: unused

Page 3

Profile number 12 (Simple PID control with 4 connected controllers) on page 3 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	⋮	
	RX175	(Unused)
	RX176	(Reserved)
	⋮	
	RX187	Remote Ready
	⋮	
	RX191	(Reserved)

OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	⋮	
	RY175	(Unused)
	RY176	(Reserved)
	⋮	
	RY187	(Reserved)
	⋮	
	RY191	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: L.TY1	RWw1		01: L.TY1
RWr2		01: L.EV1	RWw2		01: L.EV1
RWr3		01: L.TY2	RWw3		01: L.TY2
RWr4		01: L.EV2	RWw4		01: L.EV2
RWr5		01: L.TY3	RWw5		01: L.TY3
RWr6		01: L.EV3	RWw6		01: L.EV3
RWr7		01: L.TY4	RWw7		01: L.TY4
RWr8		01: L.EV4	RWw8		01: L.EV4
RWr9		01: L.TY5	RWw9		01: L.TY5
RWr10		01: L.EV5.	RWw10		01: L.EV5.
RWr11		01: L.TY6	RWw11		01: L.TY6
RWr12		01: L.EV6	RWw12		01: L.EV6
RWr13		01: L.TY7	RWw13		01: L.TY7
RWr14		01: L.EV7	RWw14		01: L.EV7
RWr15		(Unused)	RWw15		(Unused)
RWr16		(Unused)	RWw16		(Unused)
RWr17		02: L.TY1	RWw17		02: L.TY1
RWr18		02: L.EV1	RWw18		02: L.EV1
RWr19		02: L.TY2	RWw19		02: L.TY2
RWr20		02: L.EV2	RWw20		02: L.EV2
RWr21		02: L.TY3	RWw21		02: L.TY3
RWr22		02: L.EV3	RWw22		02: L.EV3
RWr23		02: L.TY4	RWw23		02: L.TY4
RWr24		02: L.EV4	RWw24		02: L.EV4
RWr25		02: L.TY5	RWw25		02: L.TY5
RWr26		02: L.EV5.	RWw26		02: L.EV5.
RWr27		02: L.TY6	RWw27		02: L.TY6
RWr28		02: L.EV6	RWw28		02: L.EV6
RWr29		02: L.TY7	RWw29		02: L.TY7
RW30		02: L.EV7	RWw30		02: L.EV7
RWr31		(Unused)	RWw31		(Unused)

UP35A: unused

Page 4

Profile number 12 (Simple PID control with 4 connected controllers) on page 4 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX175	(Unused)			RY175	(Unused)
	RX176	(Reserved)			RY176	(Reserved)
	⋮				⋮	
	RX187	Remote Ready			RY187	(Reserved)
	⋮				⋮	
	RX191	(Reserved)			RY191	(Reserved)

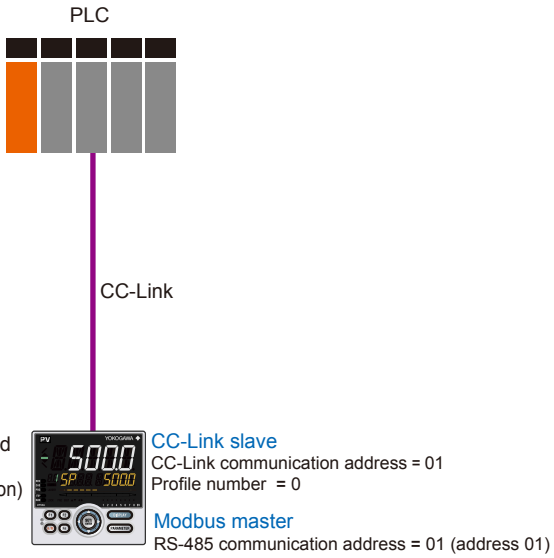
RWr0		Current page		RWw0		Page change request
RWr1		03: L.TY1		RWw1		03: L.TY1
RWr2		03: L.EV1		RWw2		03: L.EV1
RWr3		03: L.TY2		RWw3		03: L.TY2
RWr4		03: L.EV2		RWw4		03: L.EV2
RWr5		03: L.TY3	UP35A: unused	RWw5		03: L.TY3 UP35A: unused
RWr6		03: L.EV3		RWw6		03: L.EV3
RWr7		03: L.TY4		RWw7		03: L.TY4
RWr8		03: L.EV4		RWw8		03: L.EV4
RWr9		03: L.TY5		RWw9		03: L.TY5
RWr10		03: L.EV5.		RWw10		03: L.EV5.
RWr11		03: L.TY6		RWw11		03: L.TY6
RWr12		03: L.EV6		RWw12		03: L.EV6
RWr13		03: L.TY7		RWw13		03: L.TY7
RWr14		03: L.EV7		RWw14		03: L.EV7
RWr15		(Unused)		RWw15		(Unused)
RWr16		(Unused)		RWw16		(Unused)
RWr17		04: L.TY1		RWw17		04: L.TY1
RWr18		04: L.EV1		RWw18		04: L.EV1
RWr19		04: L.TY2		RWw19		04: L.TY2
RWr20		04: L.EV2		RWw20		04: L.EV2
RWr21		04: L.TY3	UP35A: unused	RWw21		04: L.TY3 UP35A: unused
RWr22		04: L.EV3		RWw22		04: L.EV3
RWr23		04: L.TY4		RWw23		04: L.TY4
RWr24		04: L.EV4		RWw24		04: L.EV4
RWr25		04: L.TY5		RWw25		04: L.TY5
RWr26		04: L.EV5.		RWw26		04: L.EV5.
RWr27		04: L.TY6		RWw27		04: L.TY6
RWr28		04: L.EV6		RWw28		04: L.EV6
RWr29		04: L.TY7		RWw29		04: L.TY7
RW30		04: L.EV7		RWw30		04: L.EV7
RWr31		(Unused)		RWw31		(Unused)

Intentionally blank

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) (Ver.2.00, 3-station occupied x8 setting)

UP55A
UP35A

Example:



Page 1

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 1 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)			・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	01: RST_ON			RY48	01: RST_ON
	RX49	01: PRG_ON			RY49	01: PRG_ON
	RX50	01: LOC_ON			RY50	01: LOC_ON
	RX51	01: HOLD			RY51	01: HOLD
	RX52	(Unused)			RY52	01: ADV
	RX53	01: A.M_L1			RY53	01: A.M_L1
	RX54	(Unused)			RY54	(Unused)
	RX55	(Unused)			RY55	(Unused)
	RX56	(Unused)			RY56	(Unused)
	RX57	(Unused)			RY57	(Unused)
	RX58	(Unused)			RY58	(Unused)
	RX59	(Unused)			RY59	(Unused)
	RX60	(Unused)			RY60	(Unused)
	RX61	(Unused)			RY61	(Unused)
	RX62	(Unused)			RY62	(Unused)
	RX63	(Unused)			RY63	(Unused)
	RX64	01: PV_EV1			RY64	(Unused)
	RX65	01: PV_EV2			RY65	(Unused)
	RX66	01: PV_EV3	UP35A: unused		RY66	(Unused)
	RX67	01: PV_EV4			RY67	(Unused)
	RX68	01: PV_EV5			RY68	(Unused)
	RX69	01: PV_EV6			RY69	(Unused)
	RX70	01: PV_EV7			RY70	(Unused)
	RX71	01: PV_EV8			RY71	(Unused)
	RX72	02: ALM1_L1			RY72	(Unused)

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 1 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
	RX73	02: ALM2_L1			RY73	(Unused)	
	RX74	02: ALM3_L1	UP35A: unused		RY74	(Unused)	
	RX75	02: ALM4_L1	UP35A: unused		RY75	(Unused)	
	RX76	(Unused)			RY76	(Unused)	
	RX77	(Unused)			RY77	(Unused)	
	RX78	(Unused)			RY78	(Unused)	
	RX79	(Unused)			RY79	(Unused)	
	RX80	01: TIME_EV1			RY80	(Unused)	
	RX81	01: TIME_EV2			RY81	(Unused)	
	RX82	01: TIME_EV3			RY82	(Unused)	
	RX83	01: TIME_EV4			RY83	(Unused)	
	RX84	01: TIME_EV5	UP35A: unused		RY84	(Unused)	
	RX85	01: TIME_EV6			RY85	(Unused)	
	RX86	01: TIME_EV7			RY86	(Unused)	
	RX87	01: TIME_EV8			RY87	(Unused)	
	RX88	01: TIME_EV9			RY88	(Unused)	
	RX89	01: TIME_EV10			RY89	(Unused)	
	RX90	01: TIME_EV11			RY90	(Unused)	
	RX91	01: TIME_EV12			RY91	(Unused)	
	RX92	01: TIME_EV13			RY92	(Unused)	
	RX93	01: TIME_EV14			RY93	(Unused)	
	RX94	01: TIME_EV15			RY94	(Unused)	
	RX95	01: TIME_EV16			RY95	(Unused)	
	RX196	(Unused)			RY96	(Unused)	
	⋮				⋮		
	RX623	(Unused)			RY623	(Unused)	
	RX624	(Reserved)			RY624	(Reserved)	
	⋮				⋮		
	RX635	Remote Ready			RY635	(Reserved)	
	⋮				⋮		
	RX639	(Reserved)			RY639	(Reserved)	

RWr0		Current page		RWw0		Page change request	
RWr1		01: PV_L1		RWw1		01: H.TSP_L1	
RWr2		01: CSP_L1		RWw2		01: H.SP_L1	
RWr3		01: CSP_L2	UP35A: unused	RWw3		01: H.SP_L2	UP35A: unused
RWr4		01: SEG.RTIME		RWw4		01: H.TM_L1	
RWr5		01: LSP_L1		RWw5		01: LSP_L1	
RWr6		01: LSP_L2	UP35A: unused	RWw6		01: LSP_L2	UP35A: unused
RWr7		01: OUT_L1		RWw7		01: MOUT_L1	
RWr8		01: H.OUT_L1		RWw8		01: MOUT_L1	
RWr9		01: C.OUT_L1		RWw9		01: MOUTc_L1	
RWr10		01: C.PTNO.		RWw10		01: PTNO.	
RWr11		01: SEG.N		RWw11		01: SST	
RWr12		(Unused)		RWw12		(Unused)	
⋮				⋮			
RWr95		(Unused)		RWw95		(Unused)	

Page 2

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 2 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: Pc_L1_1	RWw4		01: Pc_L1_1
RWr5		01: Ic_L1_1	RWw5		01: Ic_L1_1
RWr6		01: Dc_L1_1	RWw6		01: Dc_L1_1
RWr7		01: L.PID	RWw7		01: L.PID
RWr8		01: A1_L1_1	RWw8		01: A1_L1_1
RWr9		01: A2_L1_1	RWw9		01: A2_L1_1
RWr10		01: A3_L1_1 UP35A: unused	RWw10		01: A3_L1_1 UP35A: unused
RWr11		01: A4_L1_1 UP35A: unused	RWw11		01: A4_L1_1 UP35A: unused
RWr12		(Unused)	RWw12		(Unused)
⋮			⋮		
RWr20		(Unused)	RWw20		(Unused)
RWr21		01: L.TY1	RWw21		01: L.TY1
RWr22		01: L.EV1	RWw22		01: L.EV1
RWr23		01: L.TY2	RWw23		01: L.TY2
RWr24		01: L.EV2	RWw24		01: L.EV2
RWr25		01: L.TY3	RWw25		01: L.TY3
RWr26		01: L.EV3	RWw26		01: L.EV3
RWr27		01: L.TY4	RWw27		01: L.TY4
RWr28		01: L.EV4	RWw28		01: L.EV4
RWr29		01: L.TY5	RWw29		01: L.TY5
RWr30		01: L.EV5.	RWw30		01: L.EV5.
RWr31		01: L.TY6	RWw31		01: L.TY6
RWr32		01: L.EV6	RWw32		01: L.EV6
RWr33		01: L.TY7	RWw33		01: L.TY7
RWr34		01: L.EV7	RWw34		01: L.EV7
RWr35		01: L.TY8	RWw35		01: L.TY8
RWr36		01: L.EV8	RWw36		01: L.EV8
		UP35A: unused			UP35A: unused
RWr37		(Unused)	RWw37		(Unused)
⋮			⋮		
RWr70		(Unused)	RWw70		(Unused)
RWr71		01: CLR.P	RWw71		01: CLR.P
RWr72		01: CLR.TRG	RWw72		01: CLR.TRG
RWr73		01: PTN.ERR	RWw73		(Unused)
RWr74		(Unused)	RWw74		(Unused)
⋮			⋮		
RWr95		(Unused)	RWw95		(Unused)

Intentionally blank

Page 3

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 3 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PTNO._C	RWw1		01: PTNO._C
RWr2		01: SEGNO._C	RWw2		01: SEGNO._C
RWr3		01: SSP_L1	RWw3		01: SSP_L1
RWr4		01: SSP_L2 UP35A: unused	RWw4		01: SSP_L2 UP35A: unused
RWr5		01: STC	RWw5		01: STC
RWr6		01: WT.SW1	RWw6		01: WT.SW1
RWr7		01: WZ.UP1	RWw7		01: WZ.UP1
RWr8		01: WZ.LO1	RWw8		01: WZ.LO1
RWr9		01: WT.TM1	RWw9		01: WT.TM1
RWr10		01: WT.SW2	RWw10		01: WT.SW2
RWr11		01: WZ.UP2	RWw11		01: WZ.UP2
RWr12		01: WZ.LO2	RWw12		01: WZ.LO2
RWr13		01: WT.TM2	RWw13		01: WT.TM2
RWr14		01: WT.SW3	RWw14		01: WT.SW3
RWr15		01: WZ.UP3	RWw15		01: WZ.UP3
RWr16		01: WZ.LO3	RWw16		01: WZ.LO3
RWr17		01: WT.TM3	RWw17		01: WT.TM3
RWr18		01: WT.SW4 UP35A: unused	RWw18		01: WT.SW4 UP35A: unused
RWr19		01: WZ.UP4	RWw19		01: WZ.UP4
RWr20		01: WZ.LO4	RWw20		01: WZ.LO4
RWr21		01: WT.TM4	RWw21		01: WT.TM4
RWr22		01: WT.SW5	RWw22		01: WT.SW5
RWr23		01: WZ.UP5	RWw23		01: WZ.UP5
RWr24		01: WZ.LO5	RWw24		01: WZ.LO5
RWr25		01: WT.TM5	RWw25		01: WT.TM5
RWr26		01: R.CYCL	RWw26		01: R.CYCL
RWr27		01: R.STRT	RWw27		01: R.STRT
RWr28		01: R.END	RWw28		01: R.END
RWr29		(Unused)	RWw29		(Unused)
RWr30		(Unused)	RWw30		(Unused)
RWr31		01: P.NAME	RWw31		01: P.NAME
RWr32		01: P.NAME	RWw32		01: P.NAME
RWr33		01: P.NAME	RWw33		01: P.NAME
RWr34		01: P.NAME	RWw34		01: P.NAME
RWr35		01: P.NAME	RWw35		01: P.NAME
RWr36		01: P.NAME	RWw36		01: P.NAME
RWr37		01: P.NAME	RWw37		01: P.NAME
RWr38		01: P.NAME	RWw38		01: P.NAME
RWr39		01: P.NAME	RWw39		01: P.NAME
RWr40		01: P.NAME	RWw40		01: P.NAME

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 3 (Ver.2.00, 3-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
RWr41		01: P.NAME	RWw41		01: P.NAME
RWr42		01: PTN.ERR	RWw42		(Unused)
RWr43		(Unused)	RWw43		(Unused)
⋮			⋮		
RWr95		(Unused)	RWw95		(Unused)

Page 4

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 4 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)

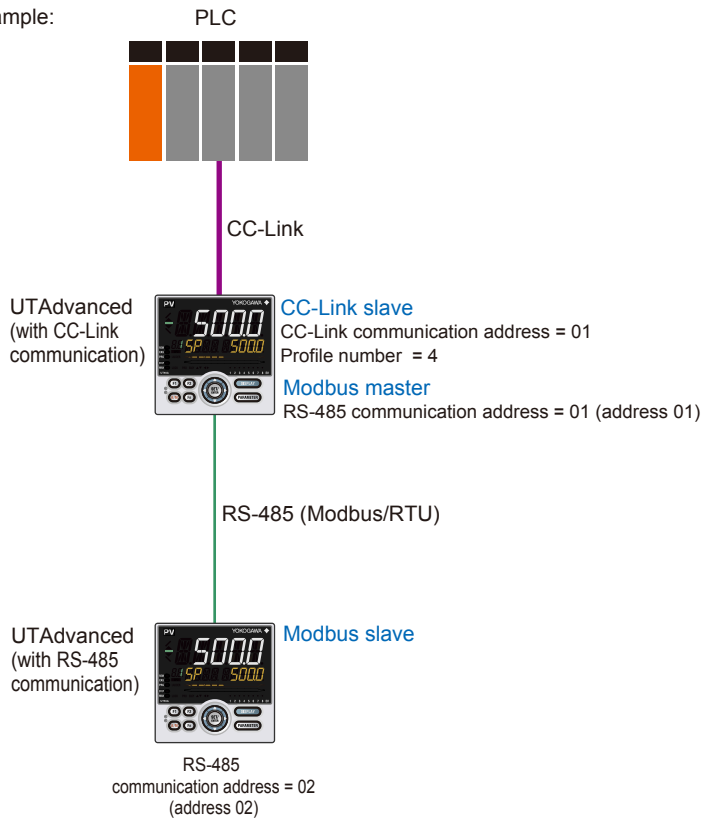
RWr0		Current page	RWw0		Page change request
RWr1		01: PTNO._C	RWw1		01: PTNO._C
RWr2		01: SEGNO._C	RWw2		01: SEGNO._C
RWr3		01: TSP_L1	RWw3		01: TSP_L1
RWr4		01: TSP_L2 UP35A: unused	RWw4		01: TSP_L2 UP35A: unused
RWr5		01: TIME	RWw5		01: TIME
RWr6		01: TM.RT	RWw6		01: TM.RT
RWr7		01: S.PID	RWw7		01: S.PID
RWr8		01: JC	RWw8		01: JC
RWr9		01: PV.TY1	RWw9		01: PV.TY1
RWr10		01: PV.EV1	RWw10		01: PV.EV1
RWr11		01: PV.TY2	RWw11		01: PV.TY2
RWr12		01: PV.EV2	RWw12		01: PV.EV2
RWr13		01: PV.TY3	RWw13		01: PV.TY3
RWr14		01: PV.EV3	RWw14		01: PV.EV3
RWr15		01: PV.TY4	RWw15		01: PV.TY4
RWr16		01: PV.EV4	RWw16		01: PV.EV4
RWr17		01: PV.TY5	RWw17		01: PV.TY5
RWr18		01: PV.EV5 } UP35A: unused	RWw18		01: PV.EV5 } UP35A: unused
RWr19		01: PV.TY6	RWw19		01: PV.TY6
RWr20		01: PV.EV6	RWw20		01: PV.EV6
RWr21		01: PV.TY7	RWw21		01: PV.TY7
RWr22		01: PV.EV7	RWw22		01: PV.EV7
RWr23		01: PV.TY8	RWw23		01: PV.TY8
RWr24		01: PV.EV8	RWw24		01: PV.EV8
RWr25		01: TME1	RWw25		01: TME1
RWr26		01: T.ON1	RWw26		01: T.ON1
RWr27		01: T.OF1	RWw27		01: T.OF1
RWr28		01: TME2	RWw28		01: TME2
RWr29		01: T.ON2	RWw29		01: T.ON2
RWr30		01: T.OF2	RWw30		01: T.OF2
RWr31		01: TME3	RWw31		01: TME3
RWr32		01: T.ON3	RWw32		01: T.ON3
RWr33		01: T.OF3	RWw33		01: T.OF3
RWr34		01: TME4	RWw34		01: TME4
RWr35		01: T.ON4	RWw35		01: T.ON4
RWr36		01: T.OF4	RWw36		01: T.OF4

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) on page 4 (Ver.2.00, 3-station occupied x8 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr37		01: TME5		RWw37		01: TME5
RWr38		01: T.ON5		RWw38		01: T.ON5
RWr39		01: T.OF5		RWw39		01: T.OF5
RWr40		01: TME6		RWw40		01: TME6
RWr41		01: T.ON6		RWw41		01: T.ON6
RWr42		01: T.OF6		RWw42		01: T.OF6
RWr43		01: TME7		RWw43		01: TME7
RWr44		01: T.ON7		RWw44		01: T.ON7
RWr45		01: T.OF7		RWw45		01: T.OF7
RWr46		01: TME8		RWw46		01: TME8
RWr47		01: T.ON8		RWw47		01: T.ON8
RWr48		01: T.OF8		RWw48		01: T.OF8
RWr49		01: TME9		RWw49		01: TME9
RWr50		01: T.ON9		RWw50		01: T.ON9
RWr51		01: T.OF9		RWw51		01: T.OF9
RWr52		01: TME10		RWw52		01: TME10
RWr53		01: T.ON10		RWw53		01: T.ON10
RWr54		01: T.OF10		RWw54		01: T.OF10
RWr55		01: TME11	UP35A: unused	RWw55		01: TME11 UP35A: unused
RWr56		01: T.ON11		RWw56		01: T.ON11
RWr57		01: T.OF11		RWw57		01: T.OF11
RWr58		01: TME12		RWw58		01: TME12
RWr59		01: T.ON12		RWw59		01: T.ON12
RWr60		01: T.OF12		RWw60		01: T.OF12
RWr61		01: TME13		RWw61		01: TME13
RWr62		01: T.ON13		RWw62		01: T.ON13
RWr63		01: T.OF13		RWw63		01: T.OF13
RWr64		01: TME14		RWw64		01: TME14
RWr65		01: T.ON14		RWw65		01: T.ON14
RWr66		01: T.OF14		RWw66		01: T.OF14
RWr67		01: TME15		RWw67		01: TME15
RWr68		01: T.ON15		RWw68		01: T.ON15
RWr69		01: T.OF15		RWw69		01: T.OF15
RWr70		01: TME16		RWw70		01: TME16
RWr71		01: T.ON16		RWw71		01: T.ON16
RWr72		01: T.OF16		RWw72		01: T.OF16
RWr73		01: PTN.ERR		RWw73		(Unused)
RWr74		(Unused)		RWw74		(Unused)
⋮				⋮		
RWr95		(Unused)		RWw95		(Unused)

Profile number 14 (Cascade control with 2 connected controllers) (Ver.2.00, 2-station occupied x4 setting)

UP55A

Example:



Page 1

Profile number 14 (Cascade control with 2 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Unused)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON		RY48	01: RST_ON
	RX49	01: PRG_ON		RY49	01: PRG_ON
	RX50	01: LOC_ON		RY50	01: LOC_ON
	RX51	01: HOLD		RY51	01: HOLD
	RX52	(Unused)		RY52	01: ADV
	RX53	01: A.M_L2		RY53	01: A.M_L2
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: L.C		RY56	01: L.C
	RX57	(Unused)		RY57	(Unused)
	RX58	(Unused)		RY58	(Unused)
	RX59	(Unused)		RY59	(Unused)
	RX60	(Unused)		RY60	(Unused)
	RX61	(Unused)		RY61	(Unused)
	RX62	(Unused)		RY62	(Unused)
	RX63	(Unused)		RY63	(Unused)

Profile number 14 (Cascade control with 2 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX64	01: PV_EV1		RY64	(Unused)
	RX65	01: PV_EV2		RY65	(Unused)
	RX66	01: PV_EV3		RY66	(Unused)
	RX67	01: PV_EV4		RY67	(Unused)
	RX68	01: PV_EV5		RY68	(Unused)
	RX69	01: PV_EV6		RY69	(Unused)
	RX70	01: PV_EV7		RY70	(Unused)
	RX71	01: PV_EV8		RY71	(Unused)
	RX72	01: ALM1_L1		RY72	(Unused)
	RX73	01: ALM2_L1		RY73	(Unused)
	RX74	01: ALM3_L1		RY74	(Unused)
	RX75	01: ALM4_L1		RY75	(Unused)
	RX76	(Unused)		RY76	(Unused)
	RX77	(Unused)		RY77	(Unused)
	RX78	(Unused)		RY78	(Unused)
	RX79	(Unused)		RY79	(Unused)
	RX80	01: TIME_EV1		RY80	(Unused)
	RX81	01: TIME_EV2		RY81	(Unused)
	RX82	01: TIME_EV3		RY82	(Unused)
	RX83	01: TIME_EV4		RY83	(Unused)
	RX84	01: TIME_EV5		RY84	(Unused)
	RX85	01: TIME_EV6		RY85	(Unused)
	RX86	01: TIME_EV7		RY86	(Unused)
	RX87	01: TIME_EV8		RY87	(Unused)
	RX88	01: TIME_EV9		RY88	(Unused)
	RX89	01: TIME_EV10		RY89	(Unused)
	RX90	01: TIME_EV11		RY90	(Unused)
	RX91	01: TIME_EV12		RY91	(Unused)
	RX92	01: TIME_EV13		RY92	(Unused)
	RX93	01: TIME_EV14		RY93	(Unused)
	RX94	01: TIME_EV15		R94	(Unused)
	RX95	01: TIME_EV16		RY95	(Unused)
	RX96	(Unused)		RY96	(Unused)
	RX97	(Unused)		RY97	(Unused)
	RX98	(Unused)		RY98	(Unused)
	RX99	(Unused)		RY99	(Unused)
	RX100	(Unused)		RY100	(Unused)
	RX101	(Unused)		RY101	(Unused)
	RX102	(Unused)		RY102	(Unused)
	RX103	(Unused)		RY103	(Unused)
	RX104	(Unused)		RY104	(Unused)
	RX105	(Unused)		RY105	(Unused)
	RX106	(Unused)		RY106	(Unused)
	RX107	(Unused)		RY107	(Unused)
	RX108	(Unused)		RY108	(Unused)
	RX109	(Unused)		RY109	(Unused)
	RX110	(Unused)		RY110	(Unused)
	RX111	(Unused)		RY111	(Unused)
	RX112	02: RST_ON		RY112	02: RST_ON
	RX113	02: PRG_ON		RY113	02: PRG_ON
	RX114	02: LOC_ON		RY114	02: LOC_ON
	RX115	02: HOLD		RY115	02: HOLD
	RX116	(Unused)		RY116	02: ADV
	RX117	02: A.M_L2		RY117	02: A.M_L2
	RX118	(Unused)		RY118	(Unused)
	RX119	(Unused)		RY119	(Unused)
	RX120	02: L.C		RY120	02: L.C
	RX121	(Unused)		RY121	(Unused)
	RX122	(Unused)		RY122	(Unused)
	RX123	(Unused)		RY123	(Unused)
	RX124	(Unused)		RY124	(Unused)
	RX125	(Unused)		RY125	(Unused)
	RX126	(Unused)		RY126	(Unused)
	RX127	(Unused)		RY127	(Unused)

Profile number 14 (Cascade control with 2 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX128	02: PV_EV1		RY128	(Unused)
	RX129	02: PV_EV2		RY129	(Unused)
	RX130	02: PV_EV3		RY130	(Unused)
	RX131	02: PV_EV4		RY131	(Unused)
	RX132	02: PV_EV5		RY132	(Unused)
	RX133	02: PV_EV6		RY133	(Unused)
	RX134	02: PV_EV7		RY134	(Unused)
	RX135	02: PV_EV8		RY135	(Unused)
	RX136	02: ALM1_L1		RY136	(Unused)
	RX137	02: ALM2_L1		RY137	(Unused)
	RX138	02: ALM3_L1		RY138	(Unused)
	RX139	02: ALM4_L1		RY139	(Unused)
	RX140	(Unused)		RY140	(Unused)
	RX141	(Unused)		RY141	(Unused)
	RX142	(Unused)		RY142	(Unused)
	RX143	(Unused)		RY143	(Unused)
	RX144	02: TIME_EV1		RY144	(Unused)
	RX145	02: TIME_EV2		RY145	(Unused)
	RX146	02: TIME_EV3		RY146	(Unused)
	RX147	02: TIME_EV4		RY147	(Unused)
	RX148	02: TIME_EV5		RY148	(Unused)
	RX149	02: TIME_EV6		RY149	(Unused)
	RX150	02: TIME_EV7		RY150	(Unused)
	RX151	02: TIME_EV8		RY151	(Unused)
	RX152	02: TIME_EV9		RY152	(Unused)
	RX153	02: TIME_EV10		RY153	(Unused)
	RX154	02: TIME_EV11		RY154	(Unused)
	RX155	02: TIME_EV12		RY155	(Unused)
	RX156	02: TIME_EV13		RY156	(Unused)
	RX157	02: TIME_EV14		RY157	(Unused)
	RX158	02: TIME_EV15		RY158	(Unused)
	RX159	02: TIME_EV16		RY159	(Unused)
	RX160	(Unused)		RY160	(Unused)
	RX161	(Unused)		RY161	(Unused)
	RX162	(Unused)		RY162	(Unused)
	RX163	(Unused)		RY163	(Unused)
	RX164	(Unused)		RY164	(Unused)
	RX165	(Unused)		RY165	(Unused)
	RX166	(Unused)		RY166	(Unused)
	RX167	(Unused)		RY167	(Unused)
	RX168	(Unused)		RY168	(Unused)
	RX169	(Unused)		RY169	(Unused)
	RX170	(Unused)		RY170	(Unused)
	RX171	(Unused)		RY171	(Unused)
	RX172	(Unused)		RY172	(Unused)
	RX173	(Unused)		RY173	(Unused)
	RX174	(Unused)		RY174	(Unused)
	RX175	(Unused)		RY175	(Unused)
	RX176	(Reserved)		RY176	(Reserved)
	⋮			⋮	
	RX187	Remote Ready		RY187	(Reserved)
	⋮			⋮	
	RX191	(Reserved)		RY191	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		01: H.TSP_L1
RWr2		01: CSP_L1	RWw2		01: H.SP_L1
RWr3		01: SEG_RTIME	RWw3		01: H.TM_L1
RWr4		01: LSP_L1	RWw4		01: LSP_L1
RWr5		01: OUT_L2	RWw5		01: MOUT_L2
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		01: C.PTNO.	RWw8		01: PTNO.

Profile number 14 (Cascade control with 2 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr9		01: SEG.N		RWw9		01: SST
RWr10		(Unused)		RWw10		(Unused)
RWr11		01: PV_L2		RWw11		(Unused)
RWr12		01: CSP_L2		RWw12		01: LSP_L2
RWr13		01: OUT_L2		RWw13		01: MOUT_L2
RWr14		(Unused)		RWw14		(Unused)
RWr15		(Unused)		RWw15		(Unused)
RWr16		(Unused)		RWw16		(Unused)
RWr17		02: PV_L1		RWw17		02: H.TSP_L1
RWr18		02: CSP_L1		RWw18		02: H.SP_L1
RWr19		02: SEG_RTIME		RWw19		02: H.TM_L1
RWr20		02: LSP_L1		RWw20		02: LSP_L1
RWr21		02: OUT_L2		RWw21		02: MOUT_L2
RWr22		(Unused)		RWw22		(Unused)
RWr23		(Unused)		RWw23		(Unused)
RWr24		02: C.PTNO.		RWw24		02: PTNO.
RWr25		02: SEG.N		RWw25		02: SST
RWr26		(Unused)		RWw26		(Unused)
RWr27		02: PV_L2		RWw27		(Unused)
RWr28		02: CSP_L2		RWw28		02: LSP_L2
RWr29		02: OUT_L2		RWw29		02: MOUT_L2
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

Page 2

Profile number 14 (Cascade control with 2 connected controllers) on page 2 (Ver.2.00, 2-station occupied x4 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX175	(Unused)		RY175	(Unused)
	RX176	(Reserved)		RY176	(Reserved)
	⋮			⋮	
	RX187	Remote Ready		RY187	(Reserved)
	⋮			⋮	
	RX191	(Reserved)		RY191	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: L.PID	RWw4		01: L.PID
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1
RWr8		01: A4_L1_1	RWw8		01: A4_L1_1
RWr9		01: P_L2_1	RWw9		01: P_L2_1
RWr10		01: I_L2_1	RWw10		01: I_L2_1
RWr11		01: D_L2_1	RWw11		01: D_L2_1
RWr12		01: A1_L2_1	RWw12		01: A1_L2_1
RWr13		01: A2_L2_1	RWw13		01: A2_L2_1
RWr14		01: A3_L2_1	RWw14		01: A3_L2_1
RWr15		01: A4_L2_1	RWw15		01: A4_L2_1
RWr16		(Unused)	RWw16		(Unused)
RWr17		02: P_L1_1	RWw17		02: P_L1_1
RWr18		03: I_L1_1	RWw18		03: I_L1_1
RWr19		02: D_L1_1	RWw19		02: D_L1_1
RWr20		02: L.PID	RWw20		02: L.PID
RWr21		02: A1_L1_1	RWw21		02: A1_L1_1
RWr22		02: A2_L1_1	RWw22		02: A2_L1_1
RWr23		02: A3_L1_1	RWw23		02: A3_L1_1
RWr24		02: A4_L1_1	RWw24		02: A4_L1_1
RWr25		02: P_L2_1	RWw25		02: P_L2_1
RWr26		02: I_L2_1	RWw26		02: I_L2_1
RWr27		02: D_L2_1	RWw27		02: D_L2_1
RWr28		02: A1_L2_1	RWw28		02: A1_L2_1
RWr29		02: A2_L2_1	RWw29		02: A2_L2_1
RW30		02: A3_L2_1	RWw30		02: A3_L2_1
RWr31		02: A4_L2_1	RWw31		02: A4_L2_1

Page 3

Profile number 14 (Cascade control with 2 connected controllers) on page 3 (Ver.2.00, 2-station occupied/ x4 setting)					
IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX175	(Unused)		RY175	(Unused)
	RX176	(Reserved)		RY176	(Reserved)
	⋮			⋮	
	RX187	Remote Ready		RY187	(Reserved)
	⋮			⋮	
	RX191	(Reserved)		RY191	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: L.TY1	RWw1		01: L.TY1
RWr2		01: L.EV1	RWw2		01: L.EV1
RWr3		01: L.TY2	RWw3		01: L.TY2
RWr4		01: L.EV2	RWw4		01: L.EV2
RWr5		01: L.TY3	RWw5		01: L.TY3
RWr6		01: L.EV3	RWw6		01: L.EV3
RWr7		01: L.TY4	RWw7		01: L.TY4
RWr8		01: L.EV4	RWw8		01: L.EV4
RWr9		01: L.TY5	RWw9		01: L.TY5
RWr10		01: L.EV5.	RWw10		01: L.EV5.
RWr11		01: L.TY6	RWw11		01: L.TY6
RWr12		01: L.EV6	RWw12		01: L.EV6
RWr13		01: L.TY7	RWw13		01: L.TY7
RWr14		01: L.EV7	RWw14		01: L.EV7
RWr15		01: L.TY8	RWw15		01: L.TY8
RWr16		01: L.EV8	RWw16		01: L.EV8
RWr17		(Unused)	RWw17		(Unused)
RWr18		(Unused)	RWw18		(Unused)
RWr19		(Unused)	RWw19		(Unused)
RWr20		(Unused)	RWw20		(Unused)
RWr21		(Unused)	RWw21		(Unused)
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31		(Unused)	RWw31		(Unused)

Page 4

Profile number 14 (Cascade control with 2 connected controllers) on page 4 (Ver.2.00, 2-station occupied x4 setting)

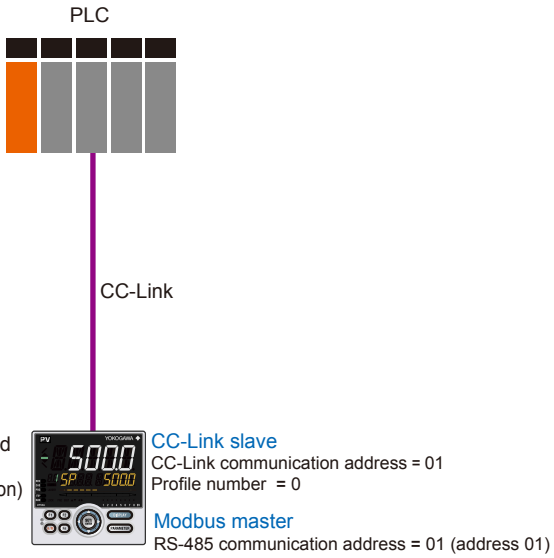
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)			⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	⋮				⋮	
	RX175	(Unused)			RY175	(Unused)
	RX176	(Reserved)			RY176	(Reserved)
	⋮				⋮	
	RX187	Remote Ready			RY187	(Reserved)
	⋮				⋮	
	RX191	(Reserved)			RY191	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		02: L.TY1		RWw1		02: L.TY1
RWr2		02: L.EV1		RWw2		02: L.EV1
RWr3		02: L.TY2		RWw3		02: L.TY2
RWr4		02: L.EV2		RWw4		02: L.EV2
RWr5		02: L.TY3		RWw5		02: L.TY3
RWr6		02: L.EV3		RWw6		02: L.EV3
RWr7		02: L.TY4		RWw7		02: L.TY4
RWr8		02: L.EV4		RWw8		02: L.EV4
RWr9		02: L.TY5		RWw9		02: L.TY5
RWr10		02: L.EV5.		RWw10		02: L.EV5.
RWr11		02: L.TY6		RWw11		02: L.TY6
RWr12		02: L.EV6		RWw12		02: L.EV6
RWr13		02: L.TY7		RWw13		02: L.TY7
RWr14		02: L.EV7		RWw14		02: L.EV7
RWr15		02: L.TY8		RWw15		02: L.TY8
RWr16		02: L.EV8		RWw16		02: L.EV8
RWr17		(Unused)		RWw17		(Unused)
RWr18		(Unused)		RWw18		(Unused)
RWr19		(Unused)		RWw19		(Unused)
RWr20		(Unused)		RWw20		(Unused)
RWr21		(Unused)		RWw21		(Unused)
RWr22		(Unused)		RWw22		(Unused)
RWr23		(Unused)		RWw23		(Unused)
RWr24		(Unused)		RWw24		(Unused)
RWr25		(Unused)		RWw25		(Unused)
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RW30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)

Intentionally blank

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) (Ver.2.00, 3-station occupied x8 setting)

UP55A

Example:



Page 1

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 1 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Unused)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)		・ ・ ・	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON		RY48	01: RST_ON
	RX49	01: PRG_ON		RY49	01: PRG_ON
	RX50	01: LOC_ON		RY50	01: LOC_ON
	RX51	01: HOLD		RY51	01: HOLD
	RX52	(Unused)		RY52	01: ADV
	RX53	01: A.M_L2		RY53	01: A.M_L2
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: L.C		RY56	01: L.C
	RX57	(Unused)		RY57	(Unused)
	RX58	(Unused)		RY58	(Unused)
	RX59	(Unused)		RY59	(Unused)
	RX60	(Unused)		RY60	(Unused)
	RX61	(Unused)		RY61	(Unused)
	RX62	(Unused)		RY62	(Unused)
	RX63	(Unused)		RY63	(Unused)
	RX64	01: PV_EV1		RY64	(Unused)
	RX65	01: PV_EV2		RY65	(Unused)
	RX66	01: PV_EV3		RY66	(Unused)
	RX67	01: PV_EV4		RY67	(Unused)
	RX68	01: PV_EV5		RY68	(Unused)
	RX69	01: PV_EV6		RY69	(Unused)
	RX70	01: PV_EV7		RY70	(Unused)
	RX71	01: PV_EV8		RY71	(Unused)
	RX72	01: ALM1_L1		RY72	(Unused)

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 1 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX73	01: ALM2_L1		RY73	(Unused)
	RX74	01: ALM3_L1		RY74	(Unused)
	RX75	01: ALM4_L1		RY75	(Unused)
	RX76	(Unused)		RY76	(Unused)
	RX77	(Unused)		RY77	(Unused)
	RX78	(Unused)		RY78	(Unused)
	RX79	(Unused)		RY79	(Unused)
	RX80	01: TIME_EV1		RY80	(Unused)
	RX81	01: TIME_EV2		RY81	(Unused)
	RX82	01: TIME_EV3		RY82	(Unused)
	RX83	01: TIME_EV4		RY83	(Unused)
	RX84	01: TIME_EV5		RY84	(Unused)
	RX85	01: TIME_EV6		RY85	(Unused)
	RX86	01: TIME_EV7		RY86	(Unused)
	RX87	01: TIME_EV8		RY87	(Unused)
	RX88	01: TIME_EV9		RY88	(Unused)
	RX89	01: TIME_EV10		RY89	(Unused)
	RX90	01: TIME_EV11		RY90	(Unused)
	RX91	01: TIME_EV12		RY91	(Unused)
	RX92	01: TIME_EV13		RY92	(Unused)
	RX93	01: TIME_EV14		RY93	(Unused)
	RX94	01: TIME_EV15		RY94	(Unused)
	RX95	01: TIME_EV16		RY95	(Unused)
	RX96	(Unused)		RY96	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		01: H.TSP_L1
RWr2		01: CSP_L1	RWw2		01: H.SP_L1
RWr3		01: SEG_RTIME	RWw3		01: H.TM_L1
RWr4		01: LSP_L1	RWw4		01: LSP_L1
RWr5		01: OUT_L2	RWw5		01: MOUT_L2
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		01: C.PTNO.	RWw8		01: PTNO.
RWr9		01: SEG.N	RWw9		01: SST
RWr10		(Unused)	RWw10		(Unused)
RWr11		01: PV_L2	RWw11		(Unused)
RWr12		01: CSP_L2	RWw12		01: LSP_L2
RWr13		01: OUT_L2	RWw13		01: MOUT_L2
RWr14		(Unused)	RWw14		(Unused)
⋮			⋮		
RWr95		(Unused)	RWw95		(Unused)

Page 2

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 2 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: L.PID	RWw4		01: L.PID
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1
RWr8		01: A4_L1_1	RWw8		01: A4_L1_1
RWr9		01: P_L2_1	RWw9		01: P_L2_1
RWr10		01: I_L2_1	RWw10		01: I_L2_1
RWr11		01: D_L2_1	RWw11		01: D_L2_1
RWr12		01: A1_L2_1	RWw12		01: A1_L2_1
RWr13		01: A2_L2_1	RWw13		01: A2_L2_1
RWr14		01: A3_L2_1	RWw14		01: A3_L2_1
RWr15		01: A4_L2_1	RWw15		01: A4_L2_1
RWr16		(Unused)	RWw16		(Unused)
RWr17		(Unused)	RWw17		(Unused)
RWr18		(Unused)	RWw18		(Unused)
RWr19		(Unused)	RWw19		(Unused)
RWr20		(Unused)	RWw20		(Unused)
RWr21		01: L.TY1	RWw21		01: L.TY1
RWr22		01: L.EV1	RWw22		01: L.EV1
RWr23		01: L.TY2	RWw23		01: L.TY2
RWr24		01: L.EV2	RWw24		01: L.EV2
RWr25		01: L.TY3	RWw25		01: L.TY3
RWr26		01: L.EV3	RWw26		01: L.EV3
RWr27		01: L.TY4	RWw27		01: L.TY4
RWr28		01: L.EV4	RWw28		01: L.EV4
RWr29		01: L.TY5	RWw29		01: L.TY5
RWr30		01: L.EV5.	RWw30		01: L.EV5.
RWr31		01: L.TY6	RWw31		01: L.TY6
RWr32		01: L.EV6	RWw32		01: L.EV6
RWr33		01: L.TY7	RWw33		01: L.TY7
RWr34		01: L.EV7	RWw34		01: L.EV7
RWr35		01: L.TY8	RWw35		01: L.TY8
RWr36		01: L.EV8	RWw36		01: L.EV8
RWr37		(Unused)	RWw37		(Unused)
⋮			⋮		
RWr70		(Unused)	RWw70		(Unused)

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 2 (Ver.2.00, 3-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr71		01: CLR.P		RWw71		01: CLR.P
RWr72		01: CLR.TRG		RWw72		01: CLR.TRG
RWr73		01: PTN.ERR		RWw73		(Unused)
RWr74		(Unused)		RWw74		(Unused)
⋮				⋮		
RWr95		(Unused)		RWw95		(Unused)

Page 3

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)

RWr0		Current page	RWw0		Page change request
RWr1		01: PTNO._C	RWw1		01: PTNO._C
RWr2		01: SEGNO._C	RWw2		01: SEGNO._C
RWr3		01: SSP_L1	RWw3		01: SSP_L1
RWr4		(Unused)	RWw4		(Unused)
RWr5		01: STC	RWw5		01: STC
RWr6		01: WT.SW1	RWw6		01: WT.SW1
RWr7		01: WZ.UP1	RWw7		01: WZ.UP1
RWr8		01: WZ.LO1	RWw8		01: WZ.LO1
RWr9		01: WT.TM1	RWw9		01: WT.TM1
RWr10		01: WT.SW2	RWw10		01: WT.SW2
RWr11		01: WZ.UP2	RWw11		01: WZ.UP2
RWr12		01: WZ.LO2	RWw12		01: WZ.LO2
RWr13		01: WT.TM2	RWw13		01: WT.TM2
RWr14		01: WT.SW3	RWw14		01: WT.SW3
RWr15		01: WZ.UP3	RWw15		01: WZ.UP3
RWr16		01: WZ.LO3	RWw16		01: WZ.LO3
RWr17		01: WT.TM3	RWw17		01: WT.TM3
RWr18		01: WT.SW4	RWw18		01: WT.SW4
RWr19		01: WZ.UP4	RWw19		01: WZ.UP4
RWr20		01: WZ.LO4	RWw20		01: WZ.LO4
RWr21		01: WT.TM4	RWw21		01: WT.TM4
RWr22		01: WT.SW5	RWw22		01: WT.SW5
RWr23		01: WZ.UP5	RWw23		01: WZ.UP5
RWr24		01: WZ.LO5	RWw24		01: WZ.LO5
RWr25		01: WT.TM5	RWw25		01: WT.TM5
RWr26		01: R.CYCL	RWw26		01: R.CYCL
RWr27		01: R.STRT	RWw27		01: R.STRT
RWr28		01: R.END	RWw28		01: R.END
RWr29		(Unused)	RWw29		(Unused)
RWr30		(Unused)	RWw30		(Unused)
RWr31		01: P.NAME	RWw31		01: P.NAME
RWr32		01: P.NAME	RWw32		01: P.NAME
RWr33		01: P.NAME	RWw33		01: P.NAME
RWr34		01: P.NAME	RWw34		01: P.NAME
RWr35		01: P.NAME	RWw35		01: P.NAME
RWr36		01: P.NAME	RWw36		01: P.NAME
RWr37		01: P.NAME	RWw37		01: P.NAME
RWr38		01: P.NAME	RWw38		01: P.NAME
RWr39		01: P.NAME	RWw39		01: P.NAME
RWr40		01: P.NAME	RWw40		01: P.NAME

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3 (Ver.2.00, 3-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr41		01: P.NAME		RWw41		01: P.NAME
RWr42		01: PTN.ERR		RWw42		(Unused)
RWr43		(Unused)		RWw43		(Unused)
⋮				⋮		
RWr95		(Unused)		RWw95		(Unused)

Page 4

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 4 (Ver.2.00, 3-station occupied x8 setting)

IN area CC-Link slave (UTAdvanced) → CC-Link master			IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	⋮	The fixed-part is omitted (See profile number 0 on page 1)		⋮	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	⋮			⋮	
	RX623	(Unused)		RY623	(Unused)
	RX624	(Reserved)		RY624	(Reserved)
	⋮			⋮	
	RX635	Remote Ready		RY635	(Reserved)
	⋮			⋮	
	RX639	(Reserved)		RY639	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: PTNO._C	RWw1		01: PTNO._C
RWr2		01: SEGNO._C	RWw2		01: SEGNO._C
RWr3		01: TSP_L1	RWw3		01: TSP_L1
RWr4		(Unused)	RWw4		(Unused)
RWr5		01: TIME	RWw5		01: TIME
RWr6		01: TM.RT	RWw6		01: TM.RT
RWr7		01: S.PID	RWw7		01: S.PID
RWr8		01: JC	RWw8		01: JC
RWr9		01: PV.TY1	RWw9		01: PV.TY1
RWr10		01: PV.EV1	RWw10		01: PV.EV1
RWr11		01: PV.TY2	RWw11		01: PV.TY2
RWr12		01: PV.EV2	RWw12		01: PV.EV2
RWr13		01: PV.TY3	RWw13		01: PV.TY3
RWr14		01: PV.EV3	RWw14		01: PV.EV3
RWr15		01: PV.TY4	RWw15		01: PV.TY4
RWr16		01: PV.EV4	RWw16		01: PV.EV4
RWr17		01: PV.TY5	RWw17		01: PV.TY5
RWr18		01: PV.EV5	RWw18		01: PV.EV5
RWr19		01: PV.TY6	RWw19		01: PV.TY6
RWr20		01: PV.EV6	RWw20		01: PV.EV6
RWr21		01: PV.TY7	RWw21		01: PV.TY7
RWr22		01: PV.EV7	RWw22		01: PV.EV7
RWr23		01: PV.TY8	RWw23		01: PV.TY8
RWr24		01: PV.EV8	RWw24		01: PV.EV8
RWr25		01: TME1	RWw25		01: TME1
RWr26		01: T.ON1	RWw26		01: T.ON1
RWr27		01: T.OF1	RWw27		01: T.OF1
RWr28		01: TME2	RWw28		01: TME2
RWr29		01: T.ON2	RWw29		01: T.ON2
RWr30		01: T.OF2	RWw30		01: T.OF2
RWr31		01: TME3	RWw31		01: TME3
RWr32		01: T.ON3	RWw32		01: T.ON3
RWr33		01: T.OF3	RWw33		01: T.OF3
RWr34		01: TME4	RWw34		01: TME4
RWr35		01: T.ON4	RWw35		01: T.ON4
RWr36		01: T.OF4	RWw36		01: T.OF4
RWr37		01: TME5	RWw37		01: TME5
RWr38		01: T.ON5	RWw38		01: T.ON5
RWr39		01: T.OF5	RWw39		01: T.OF5
RWr40		01: TME6	RWw40		01: TME6

Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 4 (Ver.2.00, 3-station occupied x8 setting)						
IN area CC-Link slave (UTAdvanced) → CC-Link master				IN area CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr41		01: T.ON6		RWw41		01: T.ON6
RWr42		01: T.OF6		RWw42		01: T.OF6
RWr43		01: TME7		RWw43		01: TME7
RWr44		01: T.ON7		RWw44		01: T.ON7
RWr45		01: T.OF7		RWw45		01: T.OF7
RWr46		01: TME8		RWw46		01: TME8
RWr47		01: T.ON8		RWw47		01: T.ON8
RWr48		01: T.OF8		RWw48		01: T.OF8
RWr49		01: TME9		RWw49		01: TME9
RWr50		01: T.ON9		RWw50		01: T.ON9
RWr51		01: T.OF9		RWw51		01: T.OF9
RWr52		01: TME10		RWw52		01: TME10
RWr53		01: T.ON10		RWw53		01: T.ON10
RWr54		01: T.OF10		RWw54		01: T.OF10
RWr55		01: TME11		RWw55		01: TME11
RWr56		01: T.ON11		RWw56		01: T.ON11
RWr57		01: T.OF11		RWw57		01: T.OF11
RWr58		01: TME12		RWw58		01: TME12
RWr59		01: T.ON12		RWw59		01: T.ON12
RWr60		01: T.OF12		RWw60		01: T.OF12
RWr61		01: TME13		RWw61		01: TME13
RWr62		01: T.ON13		RWw62		01: T.ON13
RWr63		01: T.OF13		RWw63		01: T.OF13
RWr64		01: TME14		RWw64		01: TME14
RWr65		01: T.ON14		RWw65		01: T.ON14
RWr66		01: T.OF14		RWw66		01: T.OF14
RWr67		01: TME15		RWw67		01: TME15
RWr68		01: T.ON15		RWw68		01: T.ON15
RWr69		01: T.OF15		RWw69		01: T.OF15
RWr70		01: TME16		RWw70		01: TME16
RWr71		01: T.ON16		RWw71		01: T.ON16
RWr72		01: T.OF16		RWw72		01: T.OF16
RWr73		01: PTN.ERR		RWw73		(Unused)
RWr74		(Unused)		RWw74		(Unused)
⋮				⋮		
RWr95		(Unused)		RWw95		(Unused)

4.10 Changing Automatic Rescan Time (SCAN in CC-L Menu)

When the automatic rescan time setting is changed, UTAdvanced will perform operation in the following ways.

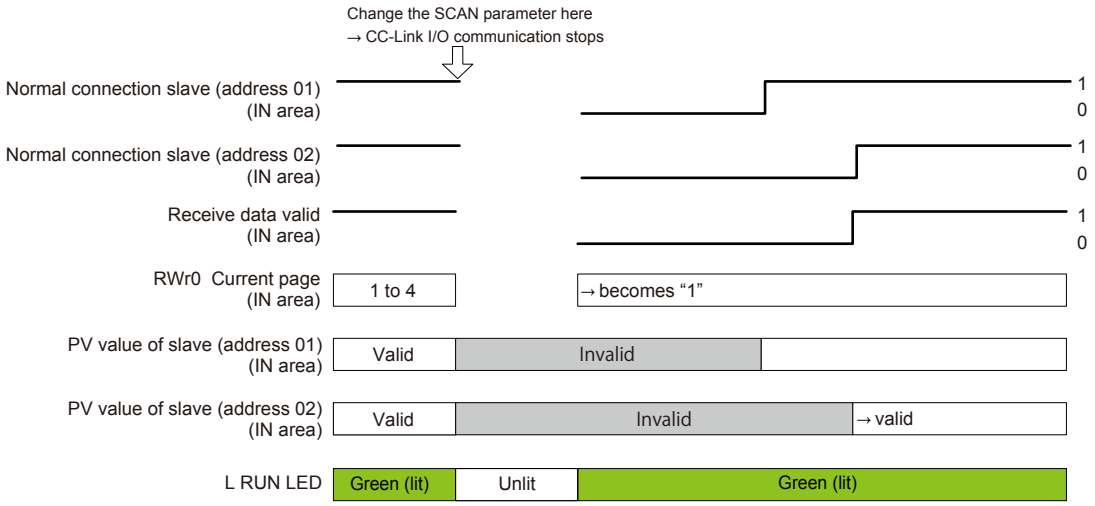
(1) Stops the CC-L I/O communication.
(2) Sets the timer according to the SCAN parameter value.
(3) Restarts the CC-L I/O communication.
(4) Sets the current page of the profile to 1 and restarts Modbus communication.

The automatic rescan time is set by the SCAN parameter in the CC-Link Communication Settings menu (CC-L).

► Setting SCAN parameters: “2.1.3 Setting CC-Link Communication (CC-Link Slave/Modbus Master)” in this manual

► 4.1 Overview: “Example: CC-Link Communication Connection” in this manual

Example of connecting 2 slaves (address 01 and 02):



4.11 Changing Profile Number (FILE in CC-L Menu)

The profile number is set by the FILE parameter in the CC-L Communication Settings menu (CC-L).

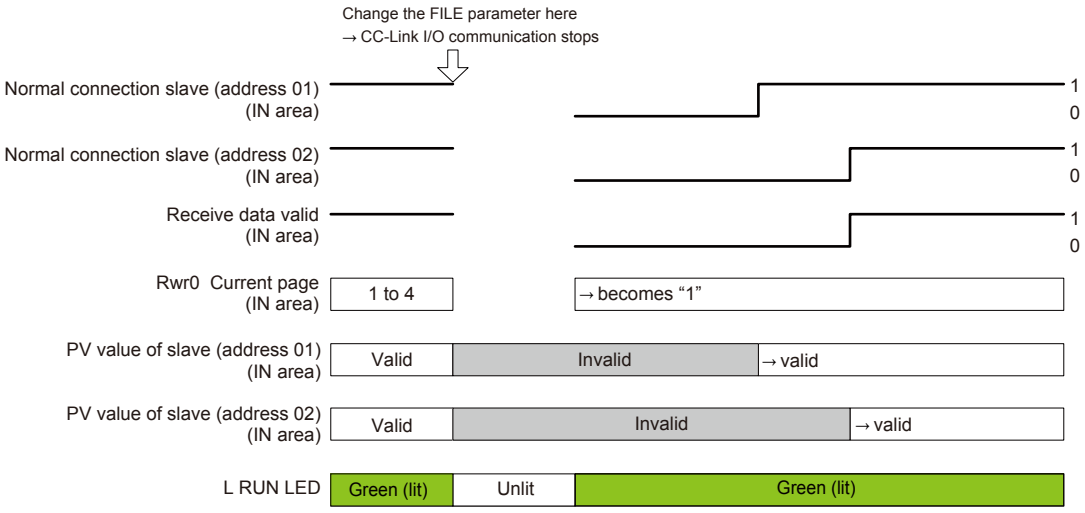
► Setting FILE parameters: “2.1.3 Setting CC-L Communication (CC-L Slave/Modbus Master)" in this manual

► 4.1 Overview: “Example: CC-Link Communication Connection” in this manual

When the profile number is changed, the CC-L I/O size changes. If the profile number (I/O size) matches that set in the CC-Link master, a connection can be established with the CC-Link master after the change is made. If it does not match, a connection cannot be established.

The following figure shows a case where the I/O size does not change after the profile number is changed. Since the I/O size usually changes, the CC-Link communication is disconnected either before or after, or both before and after the change (L ERR LED: blinking red).

Example of connecting 2 slaves (address 01 and 02):



4.12 Changing RS-485 Baud Rate (BPS in CC-L Menu)

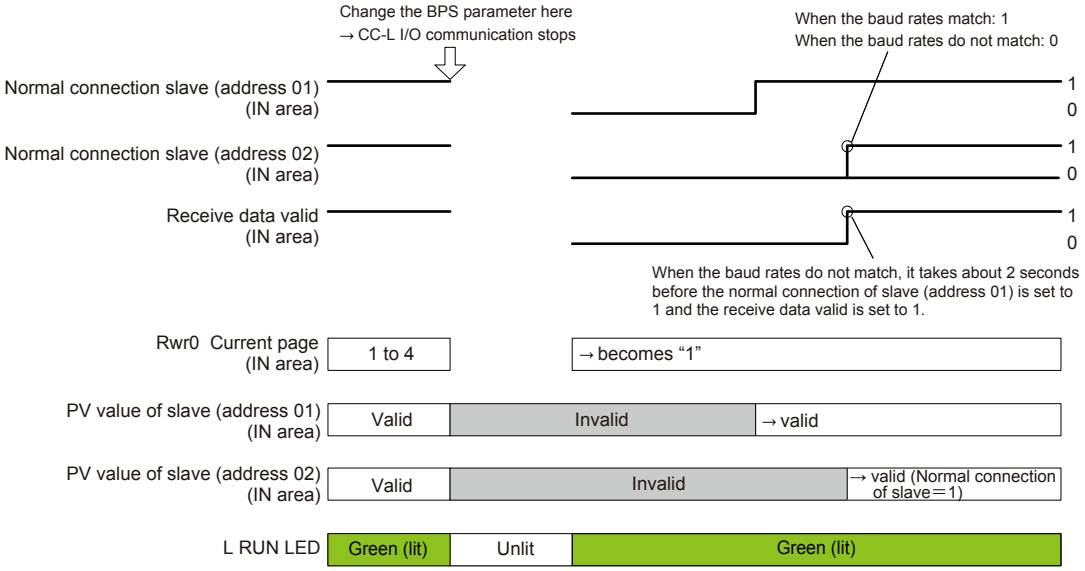
Make sure that the RS-485 baud rates of the Modbus master and Modbus slaves are identical.

The RS-485 baud rate of the Modbus master is set by the BPS parameter in the CC-Link Communication Settings menu (CC-L).

► Setting BPS parameters: “2.1.3 Setting CC-link Communication (CC-L Slave/Modbus Master)" in this manual

► 4.1 Overview: “Example: CC-L Communication Connection” in this manual

Example of connecting 2 slaves address 01 and 02:



4.13 PLC Memory Space

UTAdvanced that serves a CC-link slave occupies area of the memory space of the CC-Link master. The user needs to know from the perspective of a PLC where the data of UTAdvanced is assigned in the memory space.
Be careful because the size of the occupied memory space of the master varies depending on the profile number of UTAdvanced that serves as a CC-Link slave.

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